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FIELD MAINTENANCE PRINT SET

TABLE OF CONTENTS

K-TC-DSRVB-A-0	DSRVB-A PRINT SET
K-DD-DSRVB-A-0	DEC SERVER 200/MC DRAWING DIRECTORY
E-UA-DSRVB-A-DBU	DEC SERVER 200/MC
K-PL-DSRVB-A-DBP	DEC SERVER 200/MC (PL)
E-AD-7023163-0-DBU	COVER, ASSY
K-PL-7023163-0-DBP	COVER, ASSY, (PL)
E-AD-7023164-0-DBU	BASE, ASSY, DEC SERVER 200/MC
K-PL-7023164-0-DBP	BASE, ASSY, DEC SERVER 200/MC (PL)
D-IA-7023165-0-DBU	FAN ASSY
K-PL-7023165-0-DBP	FAN ASSY (PL)
K-DD-5418027-0-0	RS232 I/O BOARD
E-UA-5418027-0-0	RS232 I/O BOARD UNIT ASSY
K-PL-5418027-0-DBP	RS232 I/O BOARD PARTS LIST
K-BD-5418027-0-ESD1	RS232 I/O BOARD BLOCK DIAGRAM
K-CS-5418027-0-ESD2	RS232 I/O BOARD SCHEMATIC
K-CS-5418027-0-ESD3	RS232 I/O BOARD SCHEMATIC
K-CS-5418027-0-ESD4	RS232 I/O BOARD SCHEMATIC
K-CS-5418027-0-ESD5	RS232 I/O BOARD SCHEMATIC
K-CS-5418027-0-ESD6	RS232 I/O BOARD SCHEMATIC
K-CS-5418027-0-ESD7	RS232 I/O BOARD SCHEMATIC
D-EC-5018026-0-0	ETCH CUT DRAWING
K-DD-5418034-0-0	DEC SERVER 200
E-UA-5418034-0-0	DEC SERVER 200 UNIT ASSY
K-PL-5418034-0-DBP	DEC SERVER 200 PARTS LIST
K-BD-5418034-0-B1	DEC SERVER 200 BLOCK DIAGRAM
K-BD-5418034-0-B2	DEC SERVER 200 BLOCK DIAGRAM
K-BD-5418034-0-B3	DEC SERVER 200 BLOCK DIAGRAM
K-BD-5418034-0-CPU1	DEC SERVER 200 BLOCK DIAGRAM
K-CS-5418034-0-CPU2	DEC SERVER 200 SCHEMATIC
K-CS-5418034-0-CPU3	DEC SERVER 200 SCHEMATIC
K-CS-5418034-0-CPU4	DEC SERVER 200 SCHEMATIC
K-CS-5418034-0-CPU5	DEC SERVER 200 SCHEMATIC
K-CS-5418034-0-CPU6	DEC SERVER 200 SCHEMATIC

UNIT VARIATIONS
COVERED BY THIS
PRINT SET

DSRVB-A2
DSRVB-AA
DSRVB-AB

FIELD MAINTENANCE PRINT SET

DIGITAL EQUIPMENT CORPORATION

PRINT SET ORDER NO:

MP-01827-01 B01

REVISION HISTORY

CONT. REVISION HISTORY

MADE BY : L. REED

DATE : 13-OCT-87

REV: ECO NUMBER

DATE

REV: ECO NUMBER

DATE

CHECKED BY : F. CARBONE

DATE : 13-OCT-87

IC : DSRVBA-MK002

13-OCT-87

PROJECT ENGR : M. LAWERENCE
DATE : 13-OCT-87

RSPBLE ENGR : M. LAWERENCE
DATE : 13-OCT-87

FIELD SERVICE : A. RIVERA
DATE : 13-OCT-87

d i g i t a l

TITLE: FIELD MAINTENANCE PRINT SET

DSRVB-A
PRINT SET

SIZE | CODE | DOCUMENT NUMBER | REV.
K | TC | DSRVB-A-0 | C

SHEET | OF 3 | EDIT : 2

| TC FILENAME: DSRVFA1.TCF

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FIELD MAINTENANCE PRINT SET

TABLE OF CONTENTS

K-CS-5418034-0-CPU7	DECSERVER 200 SCHEMATIC
K-CS-5418034-0-CPU8	DECSERVER 200 SCHEMATIC
K-BD-5418034-0-MEM1	DECSERVER 200 BLOCK DIAGRAM
K-CS-5418034-0-MEM2	DECSERVER 200 SCHEMATIC
K-CS-5418034-0-MEM3	DECSERVER 200 SCHEMATIC
K-CS-5418034-0-MEM4	DECSERVER 200 SCHEMATIC
K-CS-5418034-0-MEM5	DECSERVER 200 SCHEMATIC
K-CS-5418034-0-MEM6	DECSERVER 200 SCHEMATIC
K-CS-5418034-0-MEM7	DECSERVER 200 SCHEMATIC
K-CS-5418034-0-MEM8	DECSERVER 200 SCHEMATIC
K-CS-5418034-0-MEM9	DECSERVER 200 SCHEMATIC
K-CS-5418034-0-MEM10	DECSERVER 200 SCHEMATIC
K-BD-5418034-0-INT1	DECSERVER 200 BLOCK DIAGRAM
K-CS-5418034-0-INT2	DECSERVER 200 SCHEMATIC
K-CS-5418034-0-INT3	DECSERVER 200 SCHEMATIC
K-CS-5418034-0-INT4	DECSERVER 200 SCHEMATIC
K-CS-5418034-0-ARB1	DECSERVER 200 SCHEMATIC
K-BD-5418034-0-B4	DECSERVER 200 BLOCK DIAGRAM
K-BD-5418034-0-TRM1	DECSERVER 200 BLOCK DIAGRAM
K-CS-5418034-0-TRM2	DECSERVER 200 SCHEMATIC
K-CS-5418034-0-TRM3	DECSERVER 200 SCHEMATIC
K-CS-5418034-0-TRM4	DECSERVER 200 SCHEMATIC
K-CS-5418034-0-TRM5	DECSERVER 200 SCHEMATIC
K-CS-5418034-0-TRM6	DECSERVER 200 SCHEMATIC
K-CS-5418034-0-TRM7	DECSERVER 200 SCHEMATIC
K-CS-5418034-0-TRM8	DECSERVER 200 SCHEMATIC
K-CS-5418034-0-TRM9	DECSERVER 200 SCHEMATIC
K-CS-5418034-0-TRM10	DECSERVER 200 SCHEMATIC
K-CS-5418034-0-TRM11	DECSERVER 200 SCHEMATIC
K-BD-5418034-0-NET1	DECSERVER 200 BLOCK DIAGRAM
K-CS-5418034-0-NET2	DECSERVER 200 SCHEMATIC
K-CS-5418034-0-NET3	DECSERVER 200 SCHEMATIC
K-CS-5418034-0-NET4	DECSERVER 200 SCHEMATIC

UNIT VARIATIONS
COVERED BY THIS
PRINT SET

DSRVB-A2
DSRVB-AA
DSRVB-AB

FIELD MAINTENANCE PRINT SET

DIGITAL EQUIPMENT CORPORATION
PRINT SET ORDER NO
MP-01827-01 B01

REVISION HISTORY

REV	ECO NUMBER	DATE	IREV	ECO NUMBER	DATE	MADE BY : L. REED	DATE : 13-OCT-87	d i g i t a l
						CHECKED BY : F. CARBONE	DATE : 13-OCT-87	TITLE: FIELD MAINTENANCE PRINT SET
C	DSRVBA-MK002	13-OCT-87	-	-	-	PROJECT ENGR : M. LAWERENCE	DATE : 13-OCT-87	DSRVB-A
-	-	-	-	-	-	RSBPLE ENGR : M. LAWERENCE	DATE : 13-OCT-87	PRINT SET
-	-	-	-	-	-	FIELD SERVICE : A. RIVERA	DATE : 13-OCT-87	SIZE CODE DOCUMENT NUMBER REV. K TC DSRVB-A-0 C
-	-	-	-	-	-	-	-	SHEET 2 OF 3 EDIT : 3

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FIELD MAINTENANCE PRINT SET

TABLE OF CONTENTS

K-CS-5418034-0-NET5	DEC SERVER 200 SCHEMATIC
K-CS-5418034-0-PWR	DEC SERVER 200 SCHEMATIC
K-CS-5418034-0-CAP1	DEC SERVER 200 SCHEMATIC
K-CS-5418034-0-CAP2	DEC SERVER 200 SCHEMATIC
K-CS-5418034-0-D1	DEC SERVER 200 SCHEMATIC
K-CS-5418034-0-D2	DEC SERVER 200 SCHEMATIC
K-CS-5418034-0-D3	DEC SERVER 200 SCHEMATIC
E-EC-5018033-0-0	ETCH CUT DRAWING

UNIT VARIATIONS
COVERED BY THIS
PRINT SET

DSRVB-A2
DSRVB-AA
DSRVB-AB

FIELD MAINTENANCE PRINT SET

DIGITAL EQUIPMENT CORPORATION

PRINT SET ORDER NO

MP-01827-01 B01

REVISION HISTORY

REV	ECO NUMBER	DATE	REV	ECO NUMBER	DATE	MADE BY : L. REED	DATE : 13-OCT-87	CHECKED BY : F. CARBONE	DATE : 13-OCT-87	PROJECT ENGR : M. LAWERENCE	DATE : 13-OCT-87	RSPBLE ENGR : M. LAWERENCE	DATE : 13-OCT-87	FIELD SERVICE : A. RIVERA	DATE : 13-OCT-87
A	INITIAL	- -	- -	- -	- -	-	-	-	-	-	-	-	-	-	-

J I G I T A

TITLE: FIELD MAINTENANCE PRINT SET

DSRVB-A

PRINT SET

SIZE | CODE | DOCUMENT NUMBER | REV.

K | TC | DSRVB-A-0 | C

SHEET 3 OF 3 | EDIT : !

DRAWING NO.	NO.SHTS	PART NO.	DESCRIPTION	REVISIONS
		DSRVB-AA	DEC SERVER 200 TERM SERVER 120 VOLT	B1 B2 B2
		DSRVB-AB	DEC SERVER 200 TERM SERVER 240 VOLT	B1 B2 B2
		DSRVB-A2	DEC SERVER 200 TERM SERVER 120 VOLT	B1 B2 B2
			W/O COUNTRY KIT	
E-UA-DSRVB-A-DBU	2		ASSY DWG, DSRVB	B C C
K-PL-DSRVB-A-DBP	1		PARTS LIST, DSRVB	B C D
K-TC-DSRVB-A-O		MP-01827-01	DEC SERVER 200 MC (TC) (MP)	- C C - B1 B1
B-TC-DSRVB-A-DBU	3		DEC SERVER 200 MC (TC)	B IN IN

NOTES:

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R	H	E	M M
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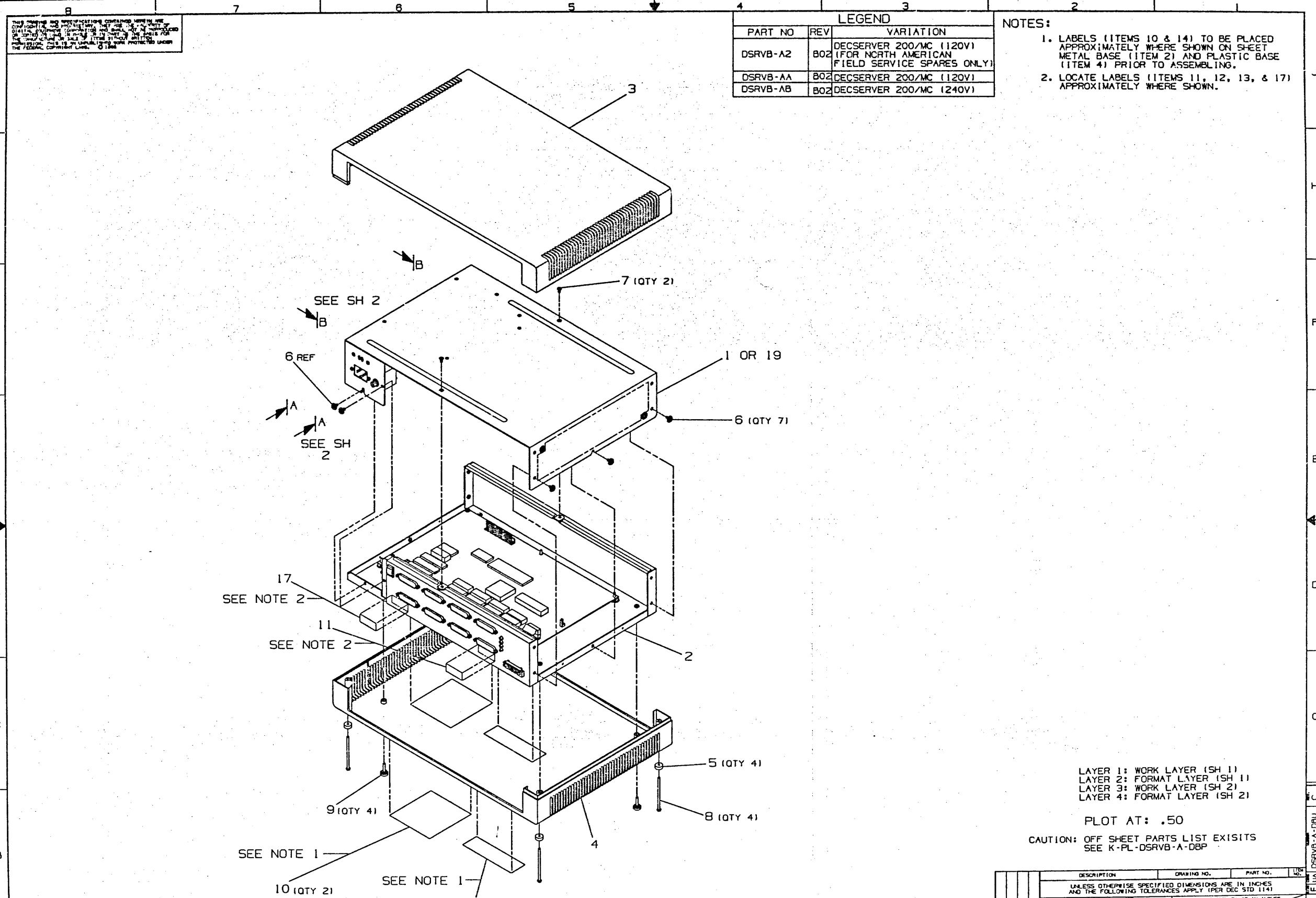
D	1	0
A	0	2
T	/	/
E	8	8
	7	8

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DRN: L. REED	DATE: 13-OCT-87	d i g i t a l
CHK'D: F. CARBONE	DATE: 13-OCT-87	TITLE DEC SERVER 200/MC
DES.ENG.: M. LAWERENCE	DATE: 13-OCT-87	SHEET 1 OF 1 EDIT: 4
RESP.ENG.: M. LAWERENCE	DATE: 13-OCT-87	DOCUMENT NUMBER IDD FILENAME: DSRVBAD.DDF
MFG.ENG.: A. RIVERA	DATE: 13-OCT-87	SIZE CODE NUMBER K DD DSRVB-A-0

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REVISION	DATE	INITIALS	NAME
REV A	01APR86	A	WILLIAM J. TAN
REV B	13JUN86	B	JOHN SODEBSKI
REV C	13JUN86	C	MCCARTHY
REV D	13JUN86	D	JOHN A. MCGEE
REV E	13JUN86	E	JOHN H. O'ROURKE

DESCRIPTION	DRAWING NO.	PART NO.	DIMENSION RANGE IN INCHES	
			THICKNESS	ANGLE 45° - 90°
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND THE FOLLOWING TOLERANCES APPLY (PER DEC STD 114)				
INCHES	APPLIED TO ALL SURFACES	ANGLE 45° - 90°	INCHES	INCHES
.001 ± .001	1.000 ± .000	.001 ± .001	.001 ± .001	.001 ± .001
.001 ± .002	1.000 ± .000	.001 ± .002	.001 ± .002	.001 ± .002
.001 ± .003	1.000 ± .000	.001 ± .003	.001 ± .003	.001 ± .003

FIRST DRAWING NUMBER: K-00-DSRVB-A-0

THIRD ANGLE PROJECTION

DO NOT SCALE DRAWING

REMOVE BURRS AND
BREAK SHARP CORNERS

MATERIAL

E. TENANDER 01APR86 TITLE digital

W. BOURGALUT/WM 13JUN86

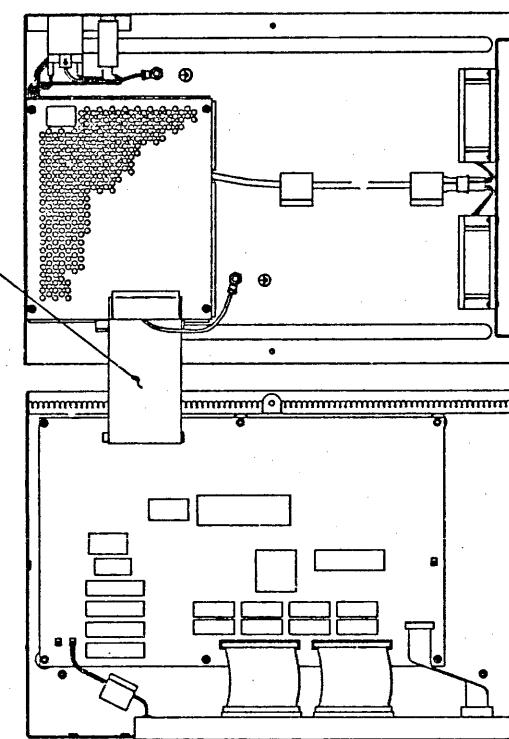
W. MCCARTHY 13JUN86

A. PARIKH 13JUN86

DOCUMENT NUMBER

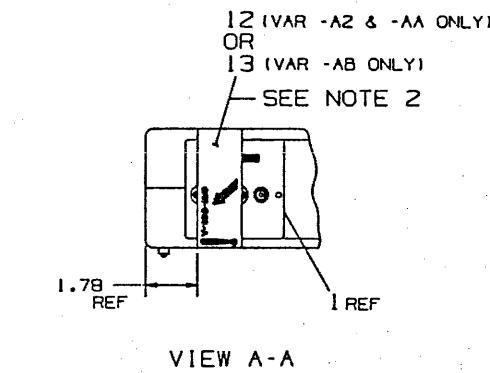
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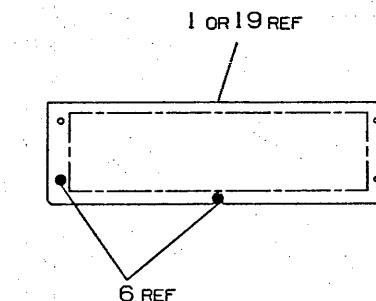


1 OR 19 REF

2 REF



VIEW A-A



VIEW B-B
VIEW SHOWN WITH ITEMS 3 & 4 REMOVED TO SHOW LOCATIONS OF ITEM 6

PLOT AT: .50

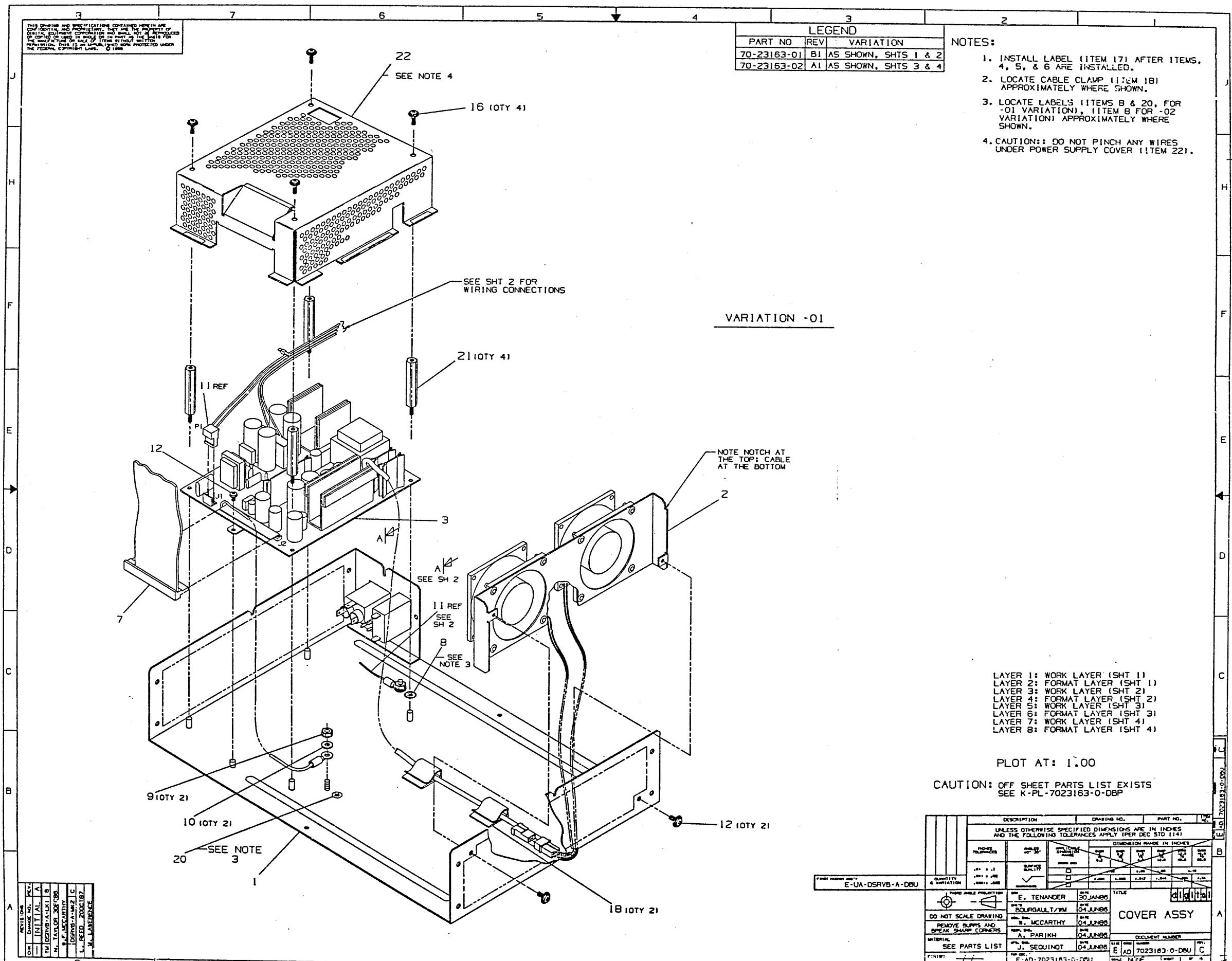
REVISION HISTORY
DATE ECO HISTORY REV

DOCUMENT NUMBER
SUBJECT NUMBER REV

LINE ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QUANTITY PER VARIATION/REVISION				
					A2 B2	AA B2	AB B2	REF	REF
1	1	E-AD-7023163-0-DBU	70-23163-01	COVER, ASSY.					
2	2	E-AD-7023164-0-DBU	70-23164-01	BASE ASSY, DEC SERVER 200/MC	1	1	1		
3	3	E-IA-7432072-0-DBU	74-32072-01	COVER, CORPORATE BOX	1	1	1		
4	4	E-IA-7432072-0-DBU	74-32072-02	BASE, CORPORATE BOX	1	1	1		
5	5	A-PS-1224506-0-0	12-24506-02	FOOT, RUBBER	.38THK	4	4	4	
6	6		90-00049-22	SCREW, SEMS PAN PHIL	6-32	7	7	7	
7	7		90-09749-00	SCREW, MACH FLAT PHIL	6-32	2	2	2	
8	8		90-08411-01	SCREW, MACH RND PHIL	8-32	4	4	4	
9	9		90-10718-01	SCREW, SHLD PHIL	6-32	4	4	4	
10	10	A-PS-3616786-0-0	36-16786-01	LABEL, ECO STATUS FOUR PART		2	2	2	
11	11	A-PS-3624044-0-0	36-24044-01	LABEL, ETHERNET ADDRESS		1	1	1	
12	12	A-PS-3617905-0-0	36-17905-16	LABEL, WARNING EQUIP. RATING, 100-12		1	1	-	
13	13	A-PS-3617905-0-0	36-17905-17	LABEL, WARNING EQUIP. RATING, 220-24		-	-	1	
14	14	A-PS-3625985-0-0	36-25985-01	LABEL, BOTTOM COMMON BOX DSRVB-A		2	2	2	
15	15	A-PS-3700805-0-0	37-00805-06	PKG COMM SERVER DSRVB/DSRVC CUS.		1	1	1	
16	16	K-PL-DSRVB-0-DBP	DSRVB-KA	DSRVB-AA,-BA KIT FOR US,CANADA(FRE		-	1	-	
17	17	A-PS-3626516-0-0	36-26516-02	LABEL, BAR CODE POLYESTER 4 UP		1	1	1	
18	18	A-PS-1215336-0-0	12-15336-08	CONN, D SUB 25POS	SE	-	1	1	
19	20	E-AD-7023163-0-DBU	70-23163-02	COVER ASSEMBLY		1	1	1	
20	21		QLZ06-UZ	DEC SVR 200 VMS LIC W/WARR		-	REF	REF	
21	22		QLZ14-UZ	DEC SVR 200 ULTRIX LIC W/WARR		-	REF	REF	

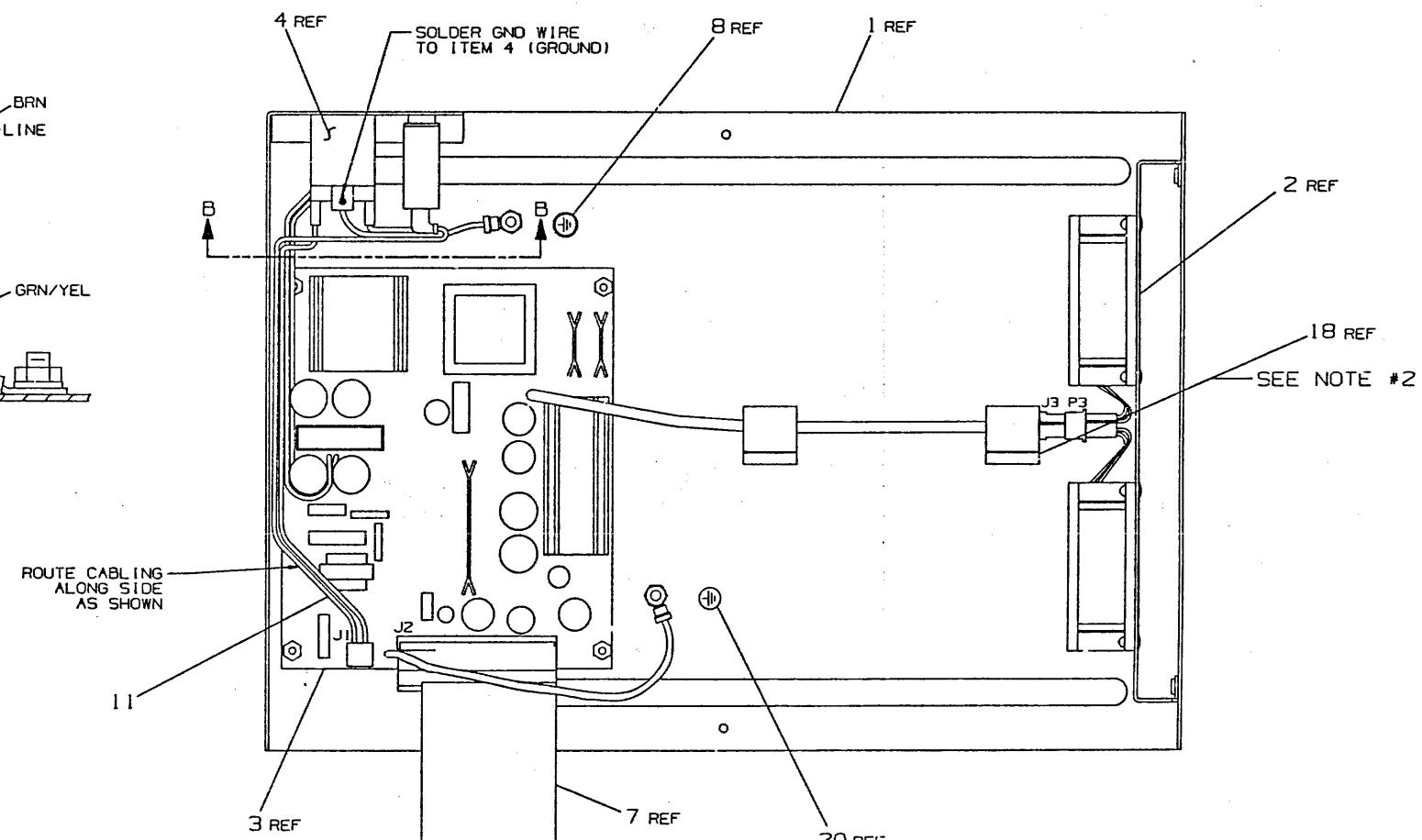
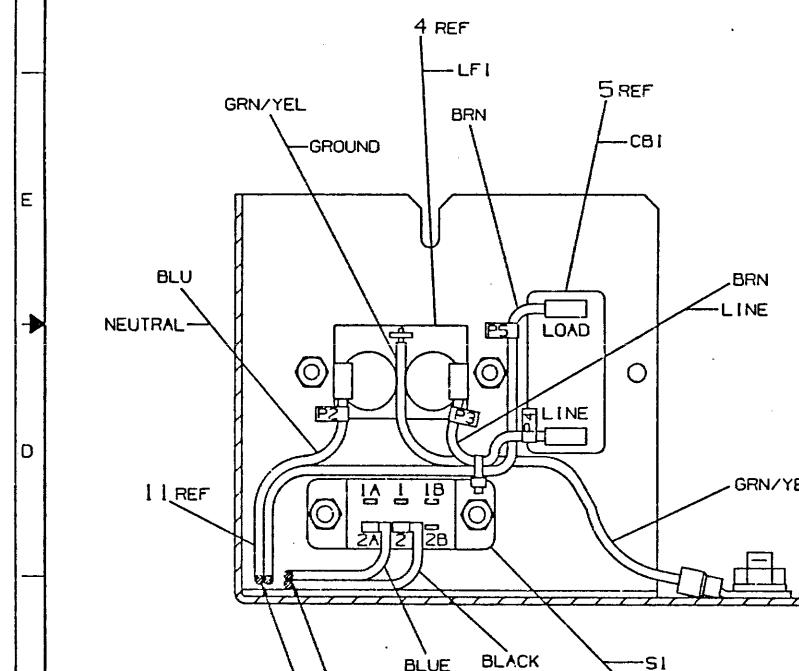
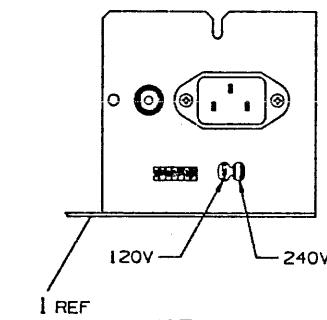
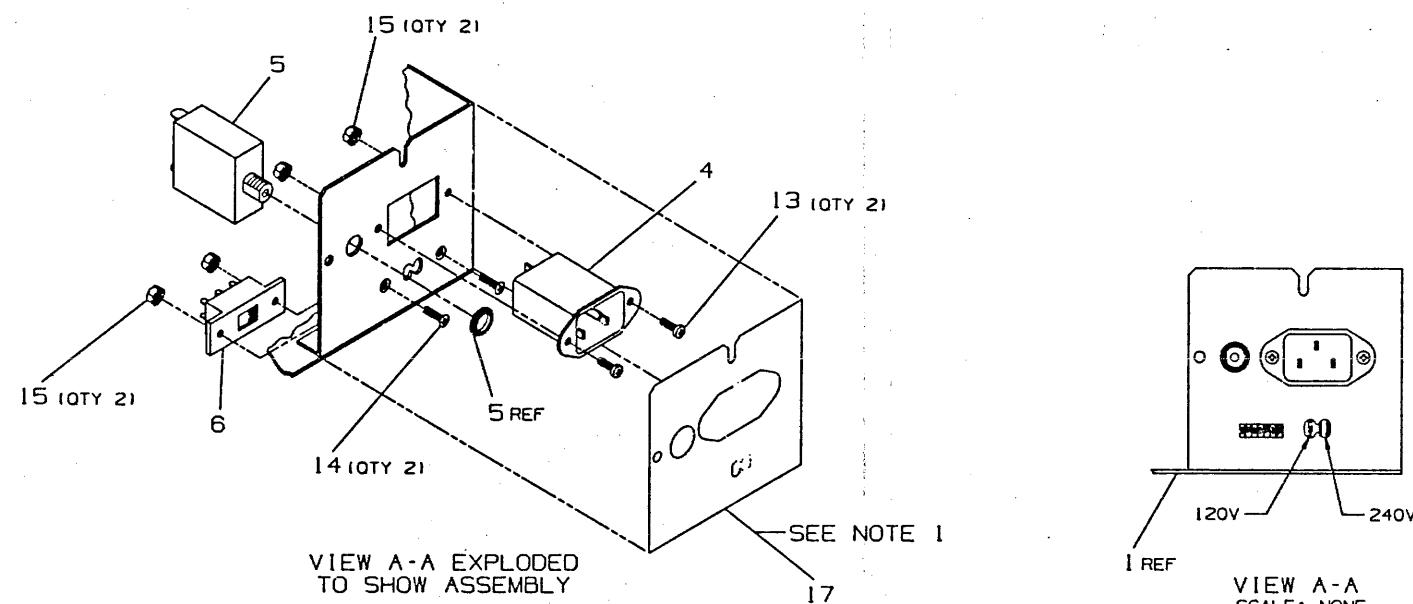
- 1 GEN: NOTE: 1. REFER TO K-PL-DSRVB-0-DBP FOR COUNTRY KITS.
 2 GEN: NOTE: 2. -A2 IS FOR NORTH AMERICAN FIELD SERVICE SPARES ONLY.
 3 GEN: NOTE: 3. ITEM #18 NOT SHOWN ON UNIT, BUT INSERTED SEPARATELY IN SHIPPING CONTAINER.
 4 GEN: IF ITEM #19 (70-23163-02) IS NOT AVAILABLE, ITEM #1 (70-23163-01) CAN BE SUBSTITUTED.

REVISION HISTORY			KPL MATRIX FORMAT		SECTION A OF A	DRN:	E. TENANDER		D	I	G	I	T	A	L
ENG	ECO NUMBER	REV	SECTION/VARIATION INDEX				DATE: 02-APR-86								
			[A]	A2,AA,AB		CHK'D:	BOURGAULT/WM		TITLE	PARTS LIST					
---	INITIAL	A	[B]			DATE:	13-JUN-86		DESERVER						
WM	DSRVB-A-LK001	B	[C]						200/MC						
ML	DSRVB-A-MK002	C	[D]			DES.ENG:	W. McCARTHY								
ML	DSRVB-A-MK003	D	[E]			DATE:	13-JUN-86			DOCUMENT NUMBER					
			[F]			RESP.ENG.:	A. PARIKH		SIZE	CODE	NUMBER	REV			
			[G]			DATE:	13-JUN-86		K	PL	DSRVB-A-DBP	D			
			[H]												
			[I]			MFG.ENG:	J. SEQUINOT		RELEASE DATE:	15-FEB-88					
			[J]			DATE:	13-JUN-86		RELEASE STATUS:	RELEASED					
						BASIC PART NUMBER:	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:	FILE NAME:	EDIT #				
						DSRVB	E-UA-DSRVB-A-DBU		K-DD-DSVRB-A-0	DSRVBAD.PLS	1				
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WIRING INTERCONNECTION LIST		
ITEM	FROM	TO
AC INPUT HARNESS 11	P1	PWR SPLY (J1)
	POINT P2 (BLU)	LFI (NEUTRAL)
	GROUND WIRE (G/Y)	LFI (GND W/SOLDER)
	POINT P3 (BRN)	LFI (LINE)
	POINT P4 (BRN)	CB1 (LINE)
	TERMINAL (G/Y)	COVER GND STUD
POWER SUPPLY 3	POINT P5 (BRN)	CBI (LOAD)
	BLUE WIRE	SI (2A)
	BLACK WIRE	SI (12)
	2 PIN MATE-N-LOK	FAN ASSY (P3)
DC POWER CABLE 7	GROUND WIRE (G/Y)	COVER GND STUD
	RIBBON CABLE	PWR SPLY J2

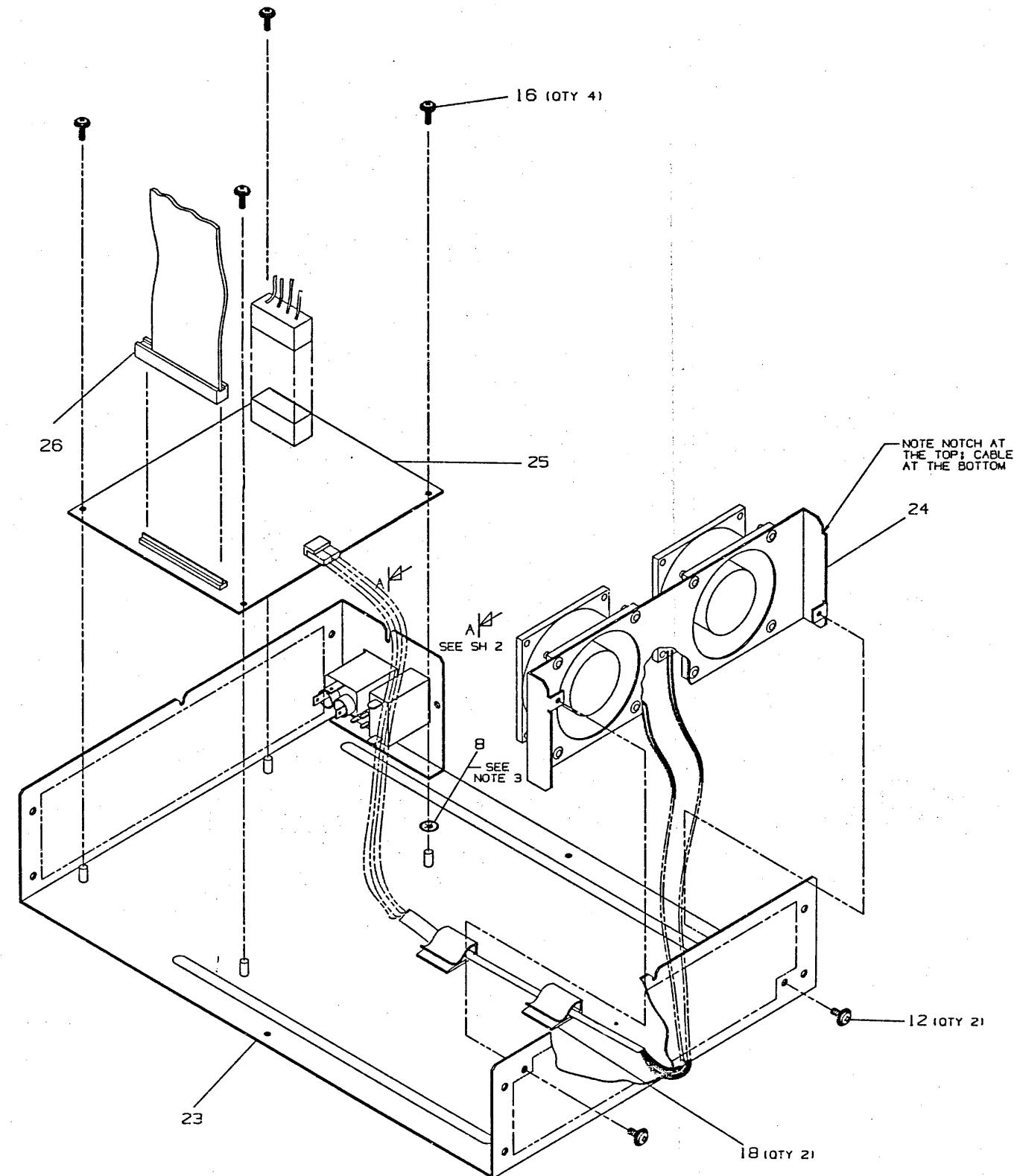


VARIATION -01

PLOT AT: 1.00

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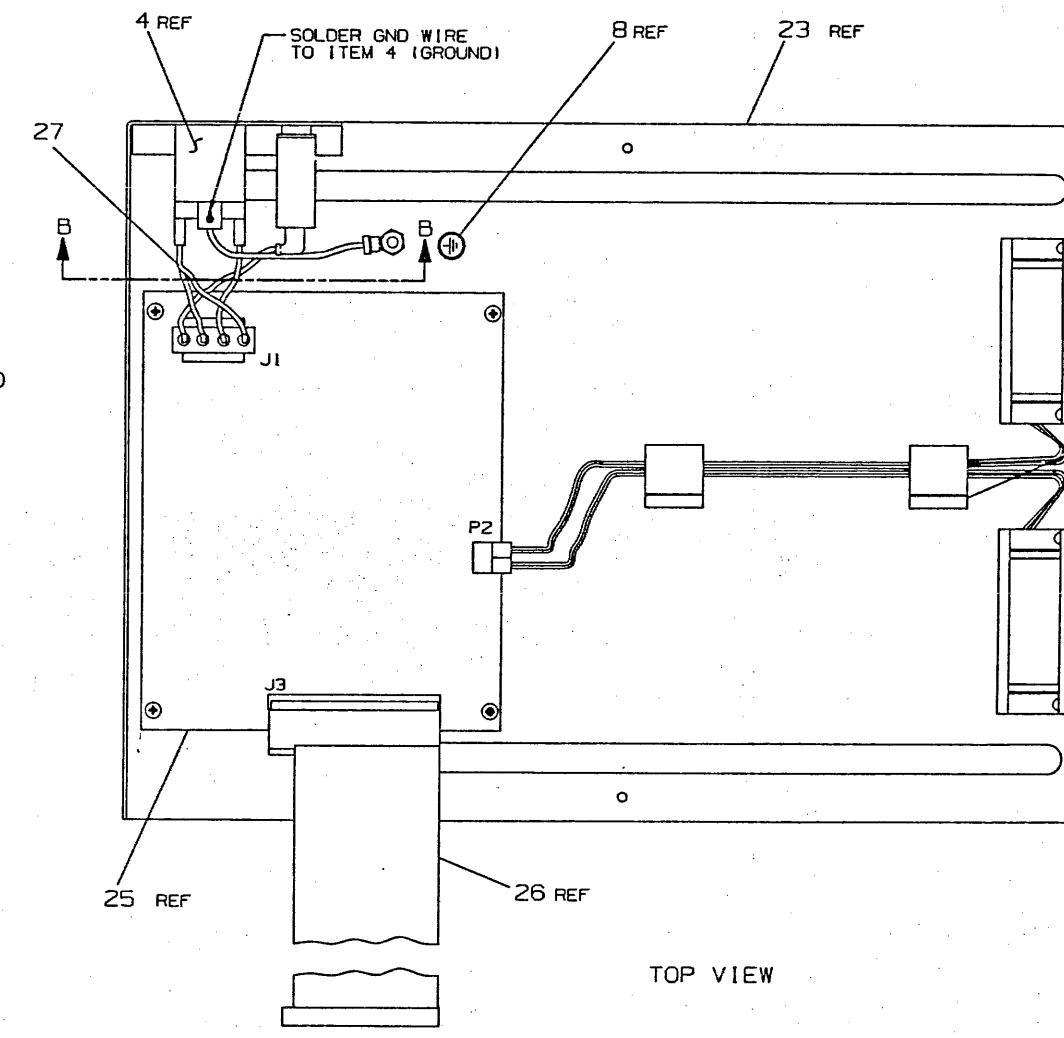
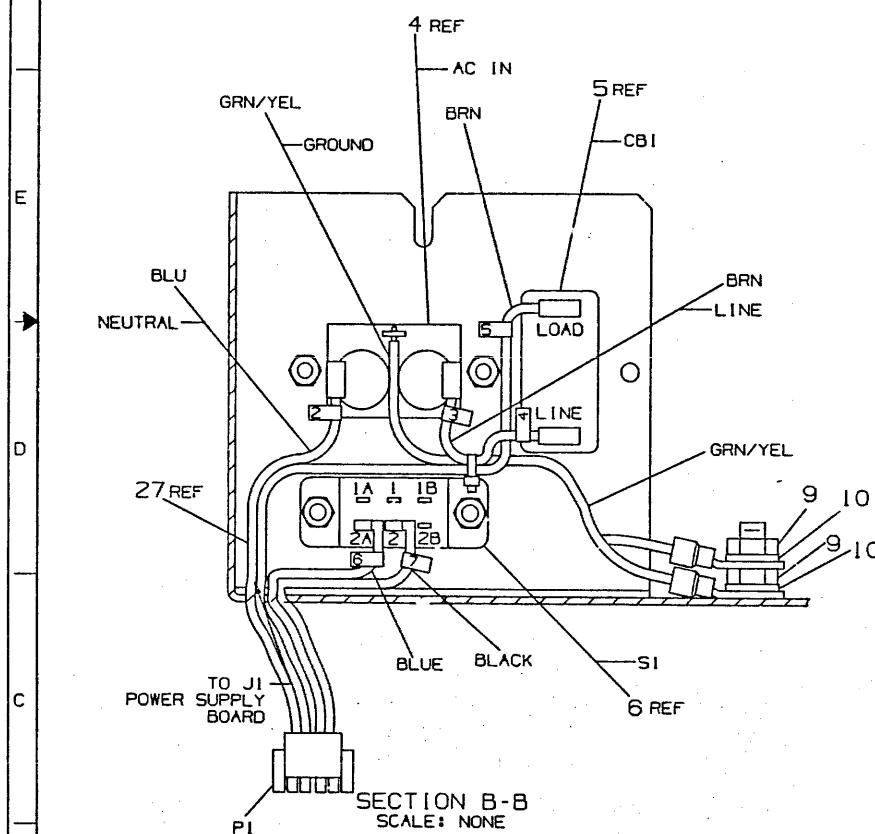
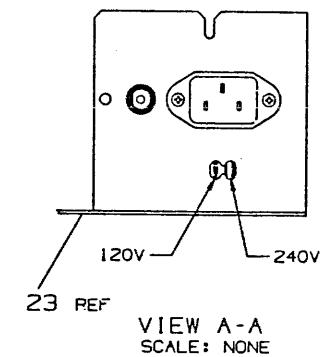
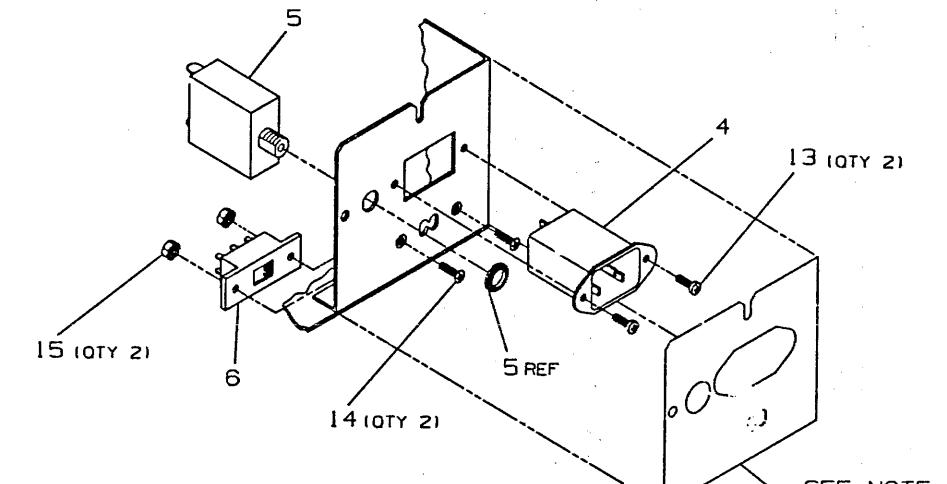


VARIATION -02

REVISION HISTORY		
DATE	ECO HISTORY	PER

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WIRING INTERCONNECTION LIST		
ITEM	FROM	TO
AC INPUT HARNESS 27	P1	PWR SPLY (J1)
	POINT P2 (BLU)	LFI (NEUTRAL)
	GROUND WIRE (G/Y)	AC IN (GND W/SOLDER)
	POINT 3 (BRN)	LFI (LINE)
	POINT 4 (BRN)	CBI (LINE)
	TERMINAL (G/Y)	COVER GND STUD
	POINT 5 (BRN)	CBI (LOAD)
	POINT 6 (BLU)	SI (2A)
POINT 7 (BRN)	SI (2)	
PS 25	GND WIRE (G/Y)	COVER GND STUD
DC PWR 26	RIBBON CABLE	PWR SUPPLY (J3)
FAN ASSY	FANS	PS (P2)



VARIATION -02

TOP VIEW

PLOT AT: 1.00

DATA FOR T-6

SHEET #1 OF #1

LINE ITEM	TOP DOCUMENT	PART NUMBER	REV	DESCRIPTION	QUANTITY PER VARIATION/REVISION		
					01 B1	02 A1	03 A1
1	E-1A-7432929-0-DBU	74-32929-01		COVER,DECSERVER 200	1	-	-
2	D-1A-7023165-0-DBU	70-23165-01		FAN, ASSY.	1	-	-
3	A-PS-3621383-0-0	30-21383-02		P.S. MULTIPLE OUTPUT	1	-	-
4	A-PS-1215905-0-0	12-15905-01		FILTER,LINE EMI/RFI 115/250V 50-60	1	1	-
5	A-PS-1219912-0-0	12-19912-07		BKR IP 2.0A 250V	1	1	-
6	A-PS-1236901-0-0	12-16901-04		SW, SLIDE 2POT VOLTAGE SELECT 6A25	1	1	-
7	A-PS-1701392-0-0	17-01392-01		CABLE ASSY,15 COND,FLAT,18AWG,1SPS	1	-	-
8	A-PS-3612680-0-0	36-12680-02		LABEL,GROUND SYMBOL W/CIRCLE BLACK	1	1	-
9		90-06565-00		NUT,HEX EXT TOOTH LOCKNSHR 10-32X	2	2	-
10		90-06664-00		WASHER,FLAT SST	2	2	-
11	A-PS-1701310-0-0	17-01310-01		WIRE HARN ASSY 18COND	1	-	-
12		90-10174-00		SCREW,SEMS PAN PHIL 6-32	3	2	-
13		90-06011-01		SCREW,MACH PAN PHIL 4-40	2	2	-
14		90-00039-19		SCREW,MACH FLAT PHIL 4-40	2	2	-
15		90-06557-00		NUT,HEX EXT TOOTH LOCKNSHR 4-40X	4	2	-
16		90-10174-03		SCREW,SEMS PAN PHIL 6-32	4	4	-
17		36-25994-01		LABEL,REAR COMMON BOX POWER SUPPLY	1	1	-
18		90-09836-00		CLAMP,CABLE ADH MNT	1	2	2
19				*** THIS ITEM IS NOT USED ***	-	-	-
20	A-PS-3612680-0-0	36-12680-00		LABEL,GROUND SYMBOL BLACK	1	-	-
21		90-00001-44		STANDOFF,MALE/FEMALE HEX 6-32 X	4	-	-
22	D-1A-7024395-0-DBU	70-24395-01		COVER ASSY,POWER SUPPLY	1	-	-
23	E-1A-7433798-02-DBU	74-33798-02		COVER,DECSERVER 200	1	1	-
24	D-1A-7023165-0-DBU	70-23165-02		FAN ASSEMBLY	-	1	-
25	A-PS-3027484-0-0	30-27484-01		P.S 59W 3 OUTPUT	-	1	-
26	A-PS-1701558-0-0	17-01558-01		CABLE ASSY,15 COND,FLAT,18AWG/16PH	-	1	-
27	A-PS-1701756-0-0	17-01756-01		WIRE HARN ASSY	-	1	-

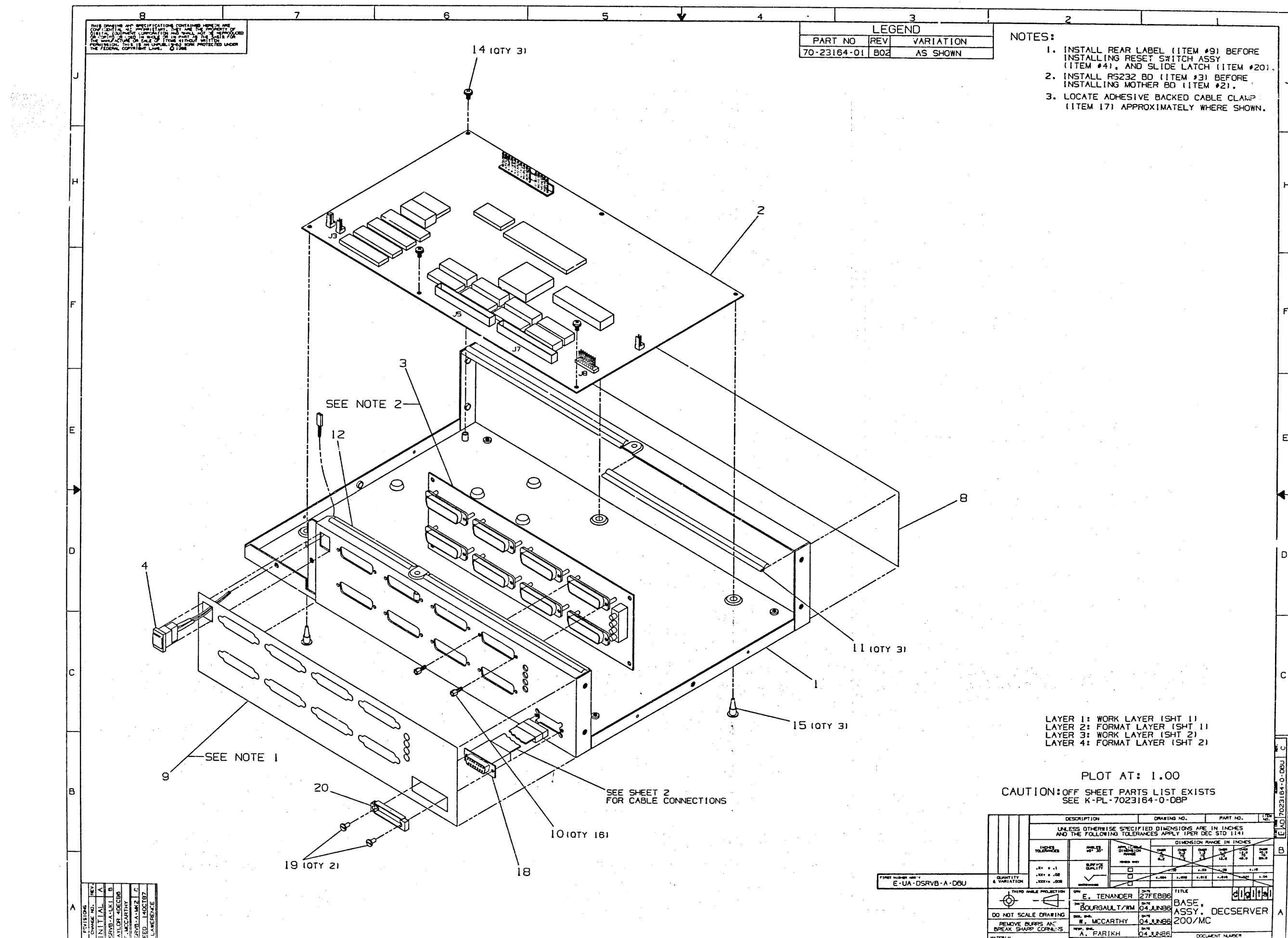
REVISION HISTORY

IKP1 MATRIX FORMAT SECTION A OF ADRN: E.TENANDER

A !DRN: E. TE
DATE: 11-FEB-86

P. 161 T. A. 11

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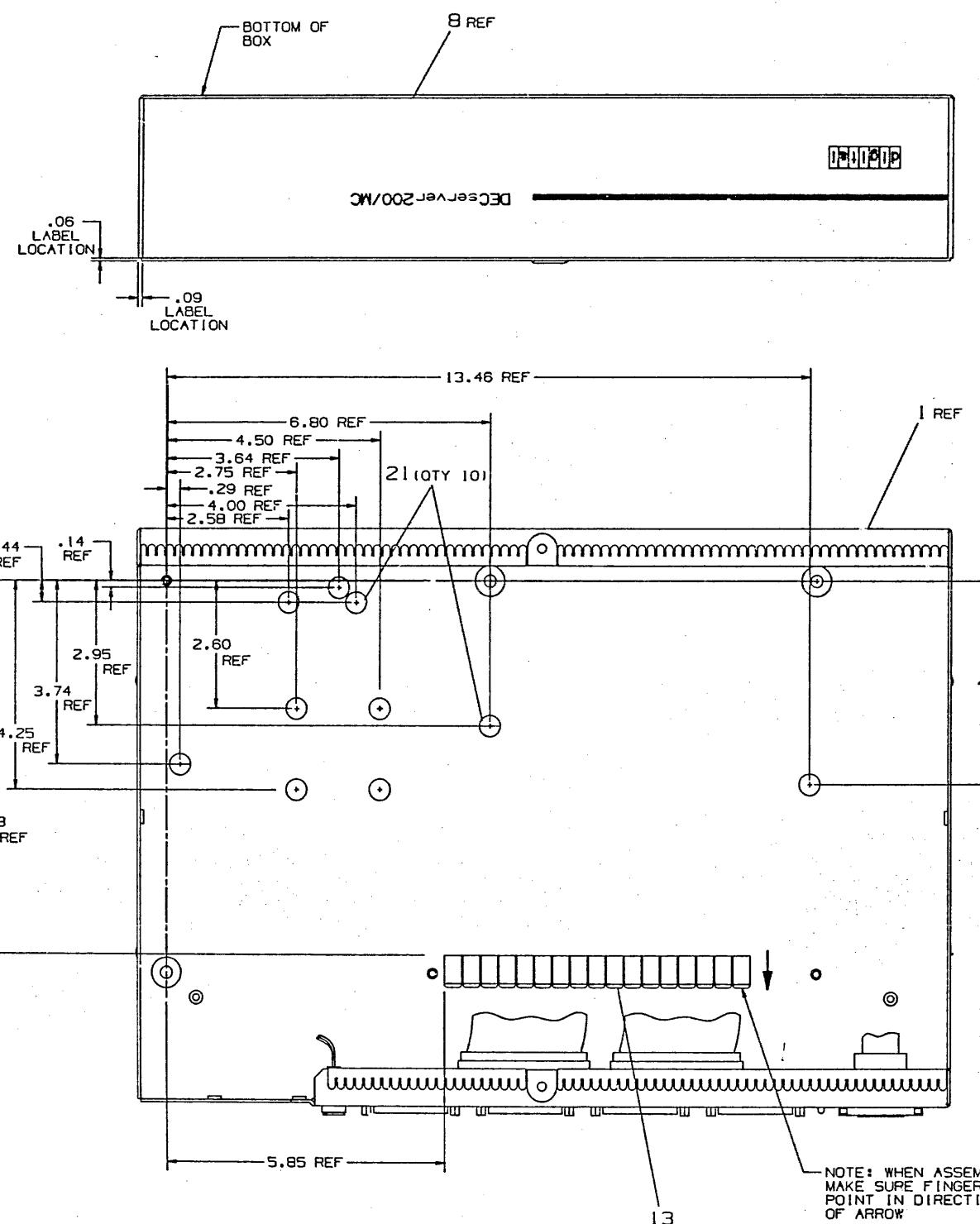
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B

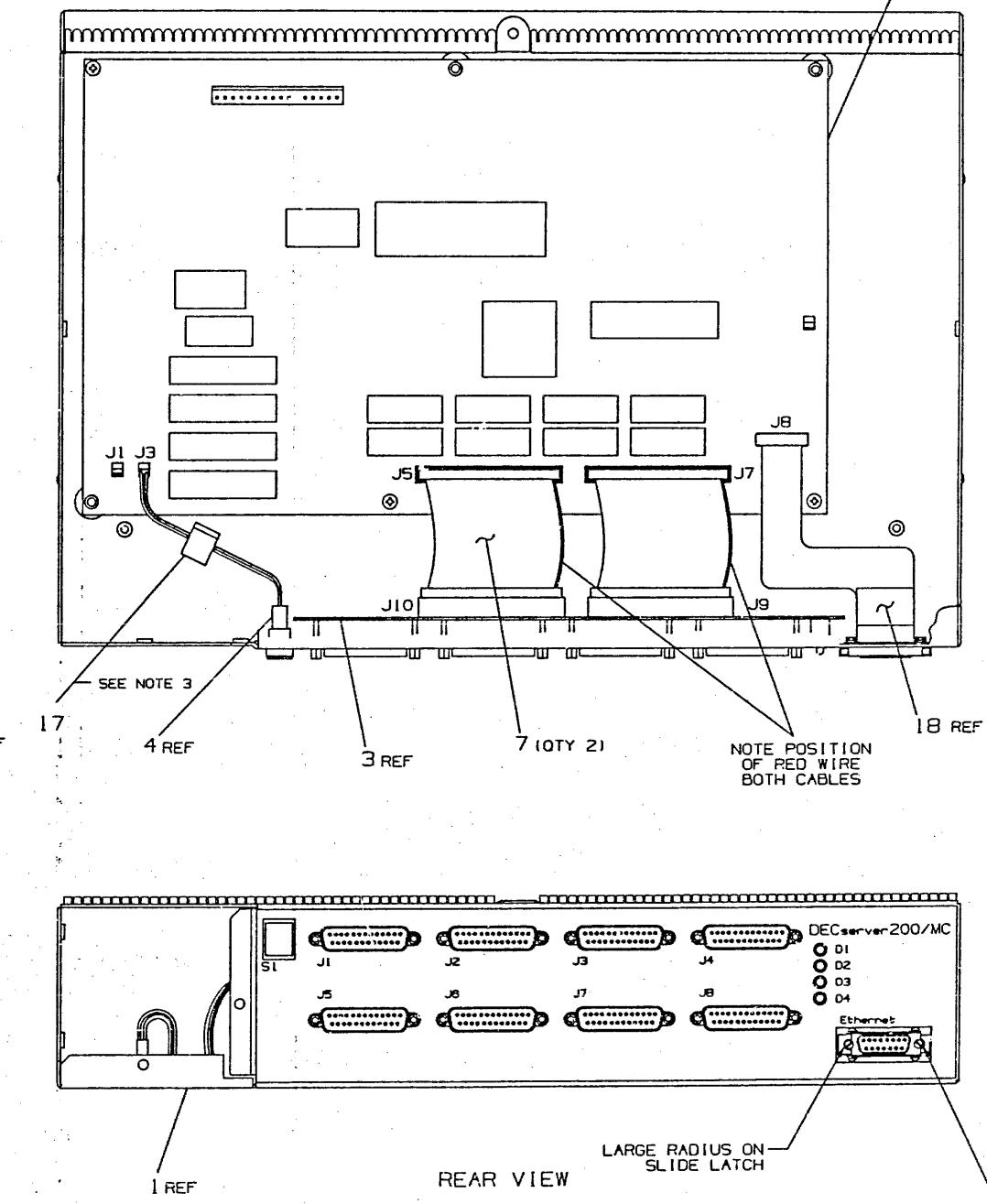
A

WIRING INTERCONNECT CHART

ITEM	FROM	TO
4 (S1)	RESET SWITCH	REAR PANEL (ITEM 1)
7	CABLE ASSY 50 COND.	RS 232 J10 INTERFACE BD (ITEM 3)
		J5 ETHERNET COMM SERVER BD (ITEM 2)
18	CABLE ASSY 15 COND.	RS 232 J9 INTERFACE BD (ITEM 3)
		J7 ETHERNET COMM SERVER BD (ITEM 2)
		REAR PANEL (ITEM 1)
		J8 ETHERNET COMM SERVER BD (ITEM 2)



TOP VIEW WITH ITEM 2 REMOVED



PLOT AT: 1.00

AUTOMATED BY VAXKPL (V1.3)

P A R T S L I S T

SHEET A1 OF A1

QUANTITY PER VARIATION/REVISION

LINE ITEM	TOP DOCUMENT	PART NUMBER	REV.	DESCRIPTION
1	1-E-IA-7433799-02-DSU	74-33799-02		BASE,DECSERVER 200
2	2	54-18034-01		DEC SERVER MAIN BOARD
3	3	54-18027-01		RS232 INTERFACE BD W/ESD PROTECTIO
4	4 A-PS-1701393-0-0	17-01393-01		WIRE HARN ASSY 02COND 22AWG PB SW-
5	5 A-PS-1218489-0-0	12-18489-02		*** THIS ITEM IS NOT USED ***
6	6 A-PS-1700559-0-0	17-00559-01		*** THIS ITEM IS NOT USED ***
7	7 A-PS-1701338-0-0	17-01338-01		CABLE ASSY,50COND,FLAT,50(2X25)REC
8	8 A-PS-3626084-0-0	36-26084-01		LABEL,FRONT COMMON BOX DSRV 200/MC
9	9 A-PS-3625984-0-0	36-25984-02		LABEL,REAR,COMMON BOX DESERVER 200
10	10	90-08451-05		SCREW LOCK,SCREW LOCK ONLY .060THK
11	11 A-PS-1226160-0-0	12-26160-04		SHIELD,EMI/RFI,COPPER FINGERS 8.00
12	12 A-PS-1226160-0-0	12-26160-01		SHIELD,EMI/RFI,COPPER FINGERS 4.12
13	13 A-PS-1226489-0-0	12-26489-01		SHIELD,EMI/RFI AD
14	14	90-10174-00		SCREW,SEMS PAN PHIL 6-32
15	15 A-PS-1223075-0-0	12-23075-03		STANDOFF PC BOARD NYLON
16	16	90-09538-00		*** THIS ITEM IS NOT USED ***
17	17	12-21249-05		CLIP,CABLE,ADH BACKED
18	18 A-PS-1701675-0-0	17-01675-01		CABLE ASSY,15COND,FLAT,
19	19 A-PS-1218489-0-0	12-18489-04		LATCH ASSY,SLIDE .29 LG. POST
20	20 A-PS-1218489-0-0	12-18489-03		LATCH ASSY,SLIDE
21	21	90-10076-00		BUMPER,DOME HD ADH BACK POLYUR

1 GEN: NOTE: WHEN MADE IN IRELAND, SUBSTITUTE ITEM 9 WITH THE FOLLOWING LABEL: 36-25984-05

REVISION HISTORY			MPL MATRIX FORMAT		SECTION A OF A	DRN: E. TENANDER							
ENG	ECO NUMBER	REV	SECTION/VARIATION INDEX			DATE: 26-FEB-86	O	I	G	I	T	A	L
---	INITIAL	A	[A] 01			CHK'D: BOURGAULT/WM	TITLE PARTS LIST						
JWM	DSRVB-A-LK001	B	[B]			DATE: 04-JUN-86	BASE, ASSY.						
JML	DSRVB-A-MK002	C	[C]			DES. ENG: W. McCARTHY	DEC SERVER 200/MC						
		D	[D]			DATE: 04-JUN-86	DOCUMENT NUMBER						
		E	[E]			RESP. ENG.: A. PARIKH	SIZE	CODE	NUMBER			REV	
		F	[F]			DATE: 04-JUN-86	K	PL	7023164-0-DBP			C	
		H	[H]			MFG. ENG: J. SEQUINOT	RELEASE DATE: 14-OCT-87						
		J	[J]			DATE: 04-JUN-86	RELEASE STATUS: RELEASED						
BASIC PART NUMBER:			ASSEMBLY NUMBER:			TOP DOCUMENT NUMBER:			FILE NAME:			EDIT #	
7023164			IE-AD-7023164-0-DBU			IE-AD-7023164-0-DBU			7023164C.PLS			2	

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8 7 6 5 4 3 2 1

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WIRE TABLE

ITEM NO	DESCRIPTION		FROM		TO		REMARKS
	AWG	COLOR	CONN	WITH	CONN	WITH	
PART OF ITEM 2	24	RED	FAN 1	—	P3-1	ITEM 5	
	24	RED	FAN 2	—			
	24	BLU	FAN 1	—	P3-2	ITEM 5	
	24	BLU	FAN 2	—			

LEGEND

PART NO	REV	VARIATION
70-23165-01	A1	AS SHOWN, SHEET 1
70-23165-02	A1	AS SHOWN, SHEET 2

NOTES:

1. FANS MUST BE POSITIONED ON BRACKET AS SHOWN WITH WIRES LOCATED AT TOP OF "FAN BRKT".

VIEW A-A
SCALE: 1/1

TOP OF "FAN BRKT"

BOTTOM OF "FAN BRKT" IS NOTCHED

VARIATION -01

3 (QTY 8)

5 (QTY 2)

4 REF

LOCATING PIN

AIR FLOW DIRECTION

2 REF

1

2 (QTY 2)

FAN 2

FAN 1

± .38

.50

P3

4

LAYER 1: WORK LAYER SHEET 1
LAYER 2: FORMAT LAYER SHEET 1
LAYER 3: WORK LAYER SHEET 2
LAYER 4: FORMAT LAYER SHEET 2

PLOT AT 1.00

CAUTION: FOR OFF SHEET PARTS LIST REFER TO K-PL-7023165-0-DBP

FIRST HIGHER ASSY E-AD-7023163-0-DBU

REVISIONS

CHG	CHANGE NO.	REV.
—	INITIAL	A
D	DSVB-A-MK02	B
L	REFID 200CTB7	
	M. LAWRENCE	

THIRD ANGLE PROJECTION

APPLICABLE DIMENSION RANGE	OVER .00	OVER .10	OVER .20	OVER .30	OVER .40	OVER .50
INCHES TOLERANCES	± .01	± .02	± .03	± .04	± .05	± .06
ANGLES 90° ± 30'	± 1°	± 2°	± 3°	± 4°	± 5°	± 6°
INCHES	± .001	± .002	± .003	± .004	± .005	± .006
SURFACE QUALITY	✓					
MICROINCHES	± 100	± 200	± 400	± 600	± 1000	± 2000

DO NOT SCALE DRAWING

REMOVE BURRS AND BREAK SHARP CORNERS

MATERIAL

SEE PARTS LIST

FINISH

TOP DOC.

D-1A-7023165-0-DBU

SIZE CODE NUMBER

D IA 7023165-0-DBU

REV.

B

SCALE

1/1

SHOOT 1 OF 2

1 MKO

2 DIA 7023165-0-DBU

1

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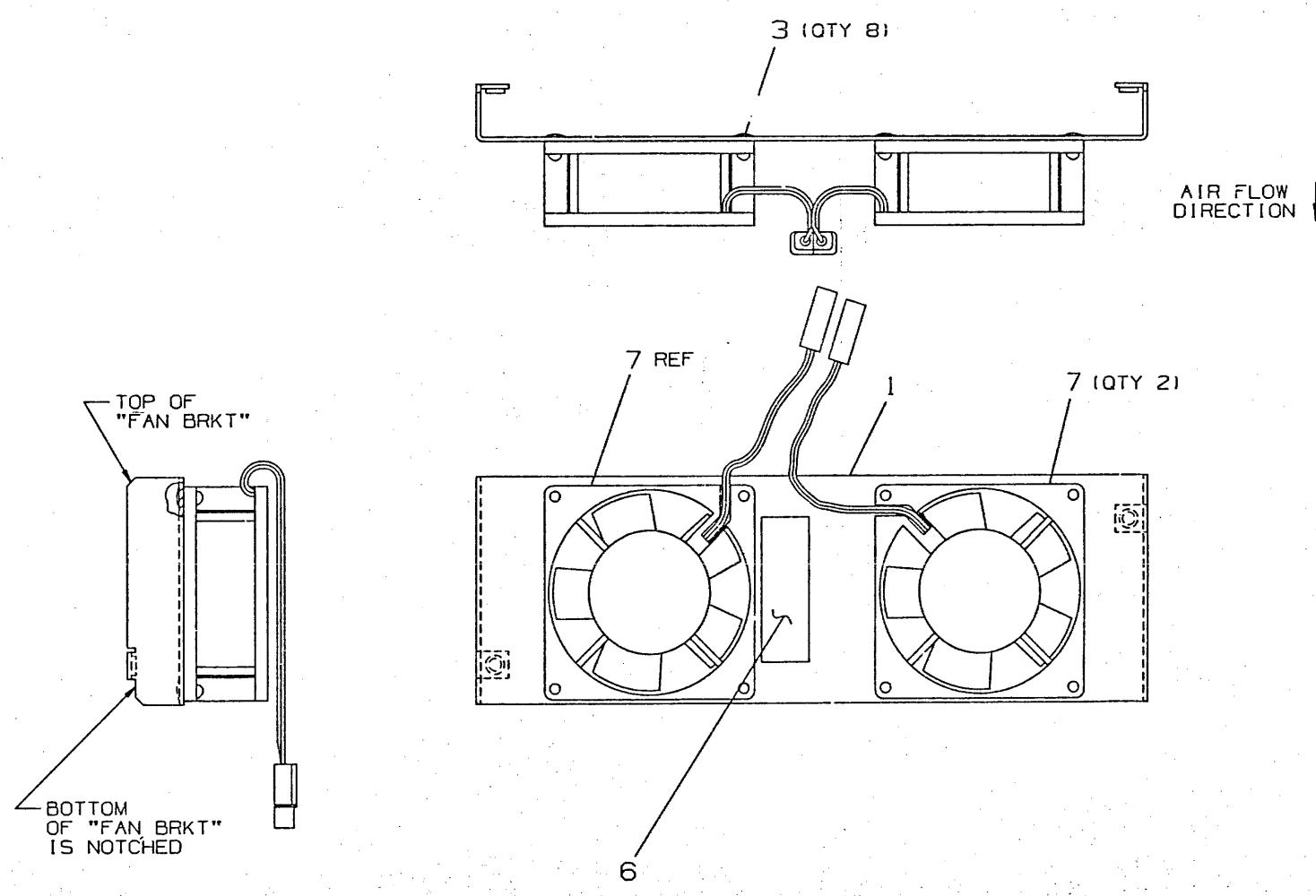
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DIA7023165-0-DBU B

C

B

A



VARIATION -02

REVISIONS		
CHK	CHANGE NO	REV

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1

TITLE FAN ASSY										SIZE CODE DIA	NUMBER 7023165-0-DBU	REV B
SCALE 1/1 SHEET 2 OF 2 DIST 1												

MKO

AUTOMATED BY VAXKPL (V1.3)

PARTS LIST

SHEET A1 OF A1

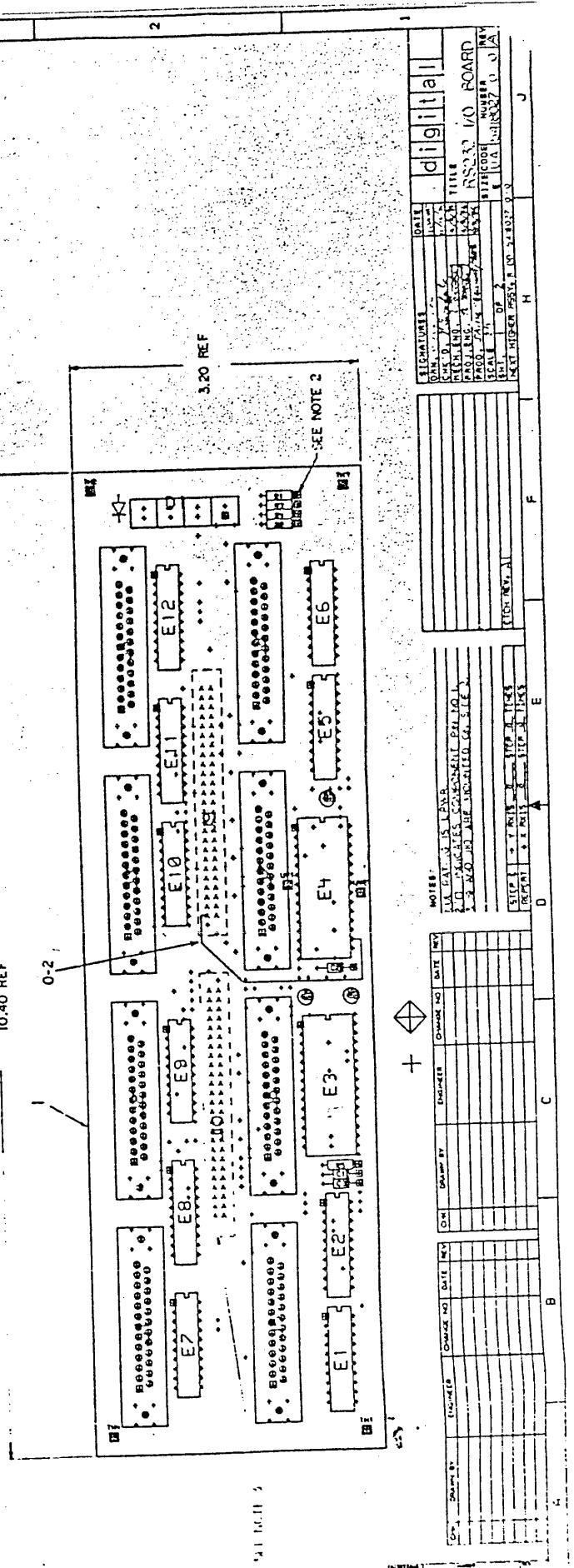
LINE ITEM	TOP DOCUMENT	PART NUMBER	REV	DESCRIPTION	QUANTITY PER VARIATION/REVISION	
					MIN	MAX
A1	A1					
1	1 D-IA-7432931-0-DBU	74-32931-01		BRACKET,FAN	1	1
2	2 A-PS-1221125-0-0	12-21125-01		FAN, TUBE AXIAL 3.125" 12VDC	2	-
3	3	90-00030-37		RIVET,BLIND LOBRK .5/32DX	8	8
4	4 A-PS-1210822-0-0	12-10822-02		MATE-N-LOK 02PIN(1X02) HSG	1	-
5	5 A-PS-1209378-0-0	12-09378-08		MATE-N-LOK 01PIN 22-18AWG .0850D	2	-
6	6	90-09255-03		LABEL,P.S./SUB ASSY ID	1	1
7	7	12-21125-03		FAN,TUBE AXIAL 12VDC	-	2

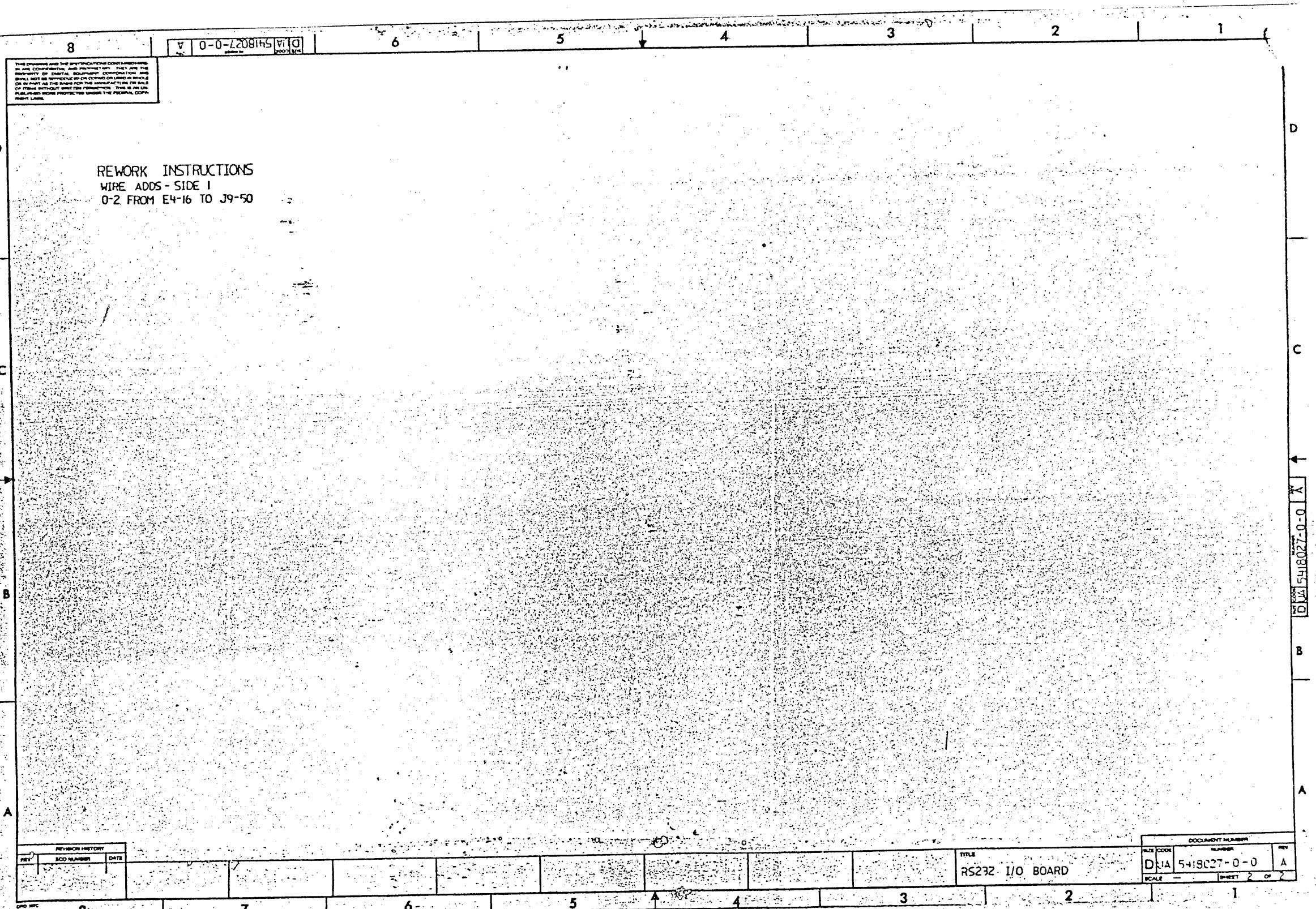
REVISION HISTORY			!KPL MATRIX FORMAT		SECTION A OF A	DRN: D. CHIASSON	D I G I T A L			
ENG	ECO NUMBER	REV	SECTION/VARIATION INDEX		DATE: 09-JAN-86	CHK'D: BOURGAULT/HM	TITLE PARTS LIST			
---	INITIAL	A	[A] 01,02				FAN ASSY			
ML	DERIVE-A-MR002	B	[B]				DOCUMENT NUMBER			
			[C]				SIZE/CODE NUMBER REV			
			[D]				[K] PL 7023165-0-DBF B			
			[E]				RELEASE DATE: 14-OCT-87			
			[F]				RELEASE STATUS: RELEASED			
			[H]							
			[J]							

DRAWING NO.	NO.SHTS	PART NO.	DESCRIPTION	REVISIONS
		5418027-01	PS232 I/O BOARD	A1 A2
E-UA-5418027-0-0	2		PS232 I/O BOARD UNIT ASSEMBLY	A A
K-PL-5418027-0-DBP	1		RS232 I/O BOARD PARTS LIST	A B
K-PC-5418027-0-DBV	-		IPC DATA BASE	A A
K-CS-5418027-0-DBD	-		DAISY DATA BASE	A A
K-BD-5418027-0-ESD1	1		RS232 I/O BOARD BLOCK DIAGRAM	A A
K-CS-5418027-0-ESD2	1		RS232 I/O BOARD SCHEMATIC	A A
K-CS-5418027-0-ESD3	1		RS232 I/O BOARD SCHEMATIC	A A
K-CS-5418027-0-ESD4	1		RS232 I/O BOARD SCHEMATIC	A A
K-CS-5418027-0-ESD5	1		RS232 I/O BOARD SCHEMATIC	A A
K-CS-5418027-0-ESD6	1		RS232 I/O BOARD SCHEMATIC	A A
K-CS-5418027-0-ESD7	1		RS232 I/O BOARD SCHEMATIC	A A
K-DD-5018026-0-0	1		DRAWING DIRECTORY	A A

NOTES:	A B																																																																					
	<table border="1"> <tr><td>R</td><td></td></tr> <tr><td>E</td><td></td></tr> <tr><td>V</td><td></td></tr> <tr><td colspan="2"> </td></tr> <tr><td>R</td><td></td></tr> <tr><td>E</td><td>H</td><td>I</td><td>M</td></tr> <tr><td>V</td><td>I</td><td>C</td><td>N</td><td>K</td></tr> <tr><td>I</td><td>S</td><td>O</td><td>I</td><td>O</td></tr> <tr><td>S</td><td>T</td><td>I</td><td>T</td><td>0</td></tr> <tr><td>I</td><td>O</td><td>N</td><td>1</td><td></td></tr> <tr><td>N</td><td>R</td><td>O</td><td></td><td></td></tr> <tr><td>Y</td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td>D</td><td>A</td><td>J</td><td></td></tr> <tr><td></td><td>A</td><td>P</td><td>JUL</td><td></td></tr> <tr><td></td><td>T</td><td>R</td><td>L</td><td></td></tr> <tr><td></td><td>E</td><td>3</td><td>8</td><td></td></tr> <tr><td></td><td></td><td>6</td><td>7</td><td></td></tr> </table>	R		E		V				R		E	H	I	M	V	I	C	N	K	I	S	O	I	O	S	T	I	T	0	I	O	N	1		N	R	O			Y						D	A	J			A	P	JUL			T	R	L			E	3	8				6	7	
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	CHK'D: DENK GARAFOLO	DATE: 29-apr-86	TITLE RS232 I/O BOARD
	DES.ENG.: R. BATTERSBY	DATE: 29-apr-86	SHEET 1 OF 1 EDIT: 1
<i>Martin Smith</i>	16-SEP-87		
	RESP.ENG.: R. BATTERSBY	DATE: 29-apr-86	DOCUMENT NUMBER
<i>Martin Smith</i>	16-SEP-87		DD FILENAME: 5418027B.DDF
	MFG.ENG.: JAIME SEGUINOT	DATE: 29-apr-86	SIZE CODE NUMBER REV
			K DD 5418027-0-0 B



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<p>D</p> <p>REWORK INSTRUCTIONS WIRE ADDS - SIDE I O-2 FROM E4-16 TO J9-50</p> <p>C</p> <p>B</p> <p>A</p>  <p>D</p> <p>C</p> <p>B</p> <p>A</p>																																																																							
<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td colspan="3" style="text-align: left; padding: 2px;">REVISION HISTORY</td> <td colspan="6" style="text-align: right; padding: 2px;">SCALE CODE: DIA 548027-0-0 A</td> </tr> <tr> <td style="text-align: center; width: 10%;">REV.</td> <td style="text-align: center; width: 10%;">ECN NUMBER</td> <td style="text-align: center; width: 10%;">DATE</td> <td colspan="6"></td> </tr> <tr> <td style="text-align: center;">X</td> <td style="text-align: center;">1</td> <td style="text-align: center;">12/10/00</td> <td colspan="6"></td> </tr> <tr> <td colspan="9" style="text-align: center; font-size: small;">DRAFTED BY: [Signature]</td> </tr> <tr> <td colspan="9" style="text-align: center; font-size: small;">CHECKED BY: [Signature]</td> </tr> <tr> <td colspan="9" style="text-align: center; font-size: small;">APPROVED BY: [Signature]</td> </tr> <tr> <td colspan="9" style="text-align: center; font-size: small;">DATE: 12/10/00</td> </tr> </table>									REVISION HISTORY			SCALE CODE: DIA 548027-0-0 A						REV.	ECN NUMBER	DATE							X	1	12/10/00							DRAFTED BY: [Signature]									CHECKED BY: [Signature]									APPROVED BY: [Signature]									DATE: 12/10/00								
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<p>DOCUMENT NUMBER: DIA 548027-0-0 A</p> <p>TITLE: RS232 I/O BOARD</p> <p>SCALE: — HEET: 2 of 2</p>																																																																							

AUTOMATED BY VAXKPL (V1.3)

PARTS LIST

SHEET AT OF A1

LINE ITEM	TOP DOCUMENT	PART NUMBER	REV	MIN	DESCRIPTION	QTY PER VAR/REV	REFERENCE DESIGNATORS
1	1	D-MD-5018026-0-0	50-18026-01		DRILL AND ETCH BOARD	1	C1-C3
2	2		10-13466-22	0.10 MFD 50V +/-20% ZSU C		3	C4-C6
3	3		10-23486-01	2.2 MFD 50V +/-20% ALU		3	D1
4	4		11-26094-01	QUAD GREEN LED ASSEMBLY -456HOU5	1	J1-J8	
5	5		12-25898-03	CONN.D. SUB 11POS ASSY STR GOLD P	8	J9-J10	
6	6		12-23637-01	BLOWER,EVAPORATOR	2	R1-R4	
7	7		13-00250-00	.150.0 .25 W 5.0 X CF	4	R5	
8	8		13-03303-00	.25 W 1.0 X RN55D-F10	1	E1,E2,E5-E12	
9	9		19-25894-01	PROTECTION DEVICE-TR	10	E3	
10	10		19-23889-02	EIA OCTAL LINE DRIVE	1	E4	
11	11		19-23890-02	EIA OCTAL LINE RECEI	1		
12	12		91-05740-55	WIRE(WRAP) 30AWG KYNAR UL14 A/R			

REVISION HISTORY			KPL MODULE FORMAT SECTION A OF A DRN: S. MANSOR			D I G I T A L				
ENG:	ECO NUMBER	REV	SECTION/VARIATION INDEX			CHK'D:	F.GARAFOLI	TITLE PARTS LIST		
ML	5418027-MK001	A	[A]	01	[M]			RS232 I/O BOARD		
		B	[B]		[N]					
		C	[C]		[P]	DES.ENG:	R. BATTERSBY	DOCUMENT NUMBER		
		D	[D]		[Q]	DATE:	18-NOV-85	SIZE:CODE: NUMBER: REV		
		E	[E]		[R]					
		F	[F]		[S]	RESP.ENG.:	R. BATTERSBY			
		G	[G]		[T]	DATE:	15-JAN-86	K PL 5418027-Q-DBP B		
		H	[H]							
		I	[I]		[V]	Tom (Csell)	Sept 10/86			
		J	[J]		[W]	MFG.ENG.:	JAIME SEGUINOT	RELEASE DATE: 02-SEP-87		
		K	[K]		[Y]	DATE:	15-JAN-86	RELEASE STATUS: RELEASED		
		L	[L]							
						BASIC PART NUMBER:	ASSEMBLY NUMBER:	TOP DOCUMENT NUMBER:	FILE NAME:	EDIT #
						5418027	E-UA-5418027-0-0	IK-00-5418027-0-0	5418027B.PLS	

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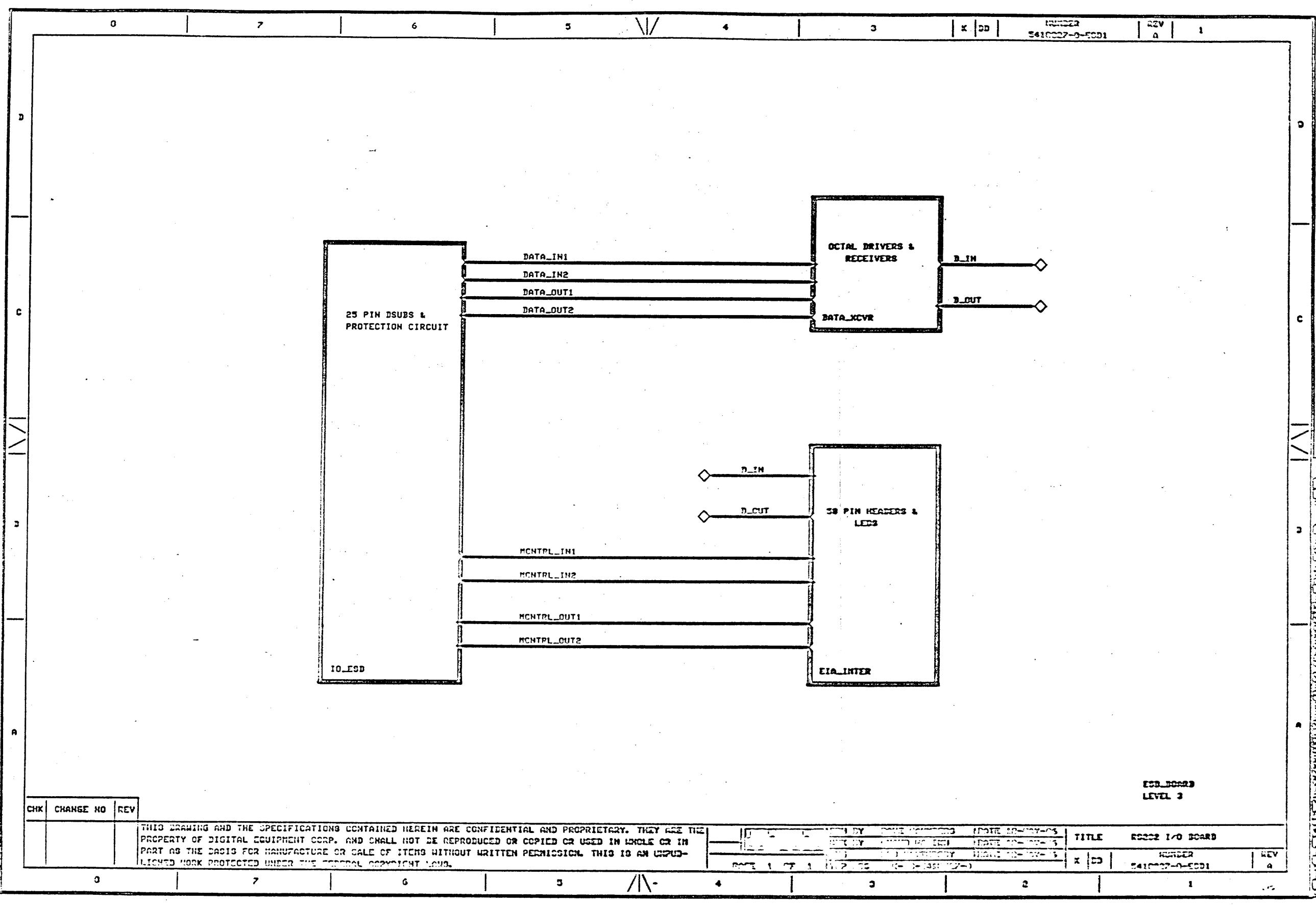
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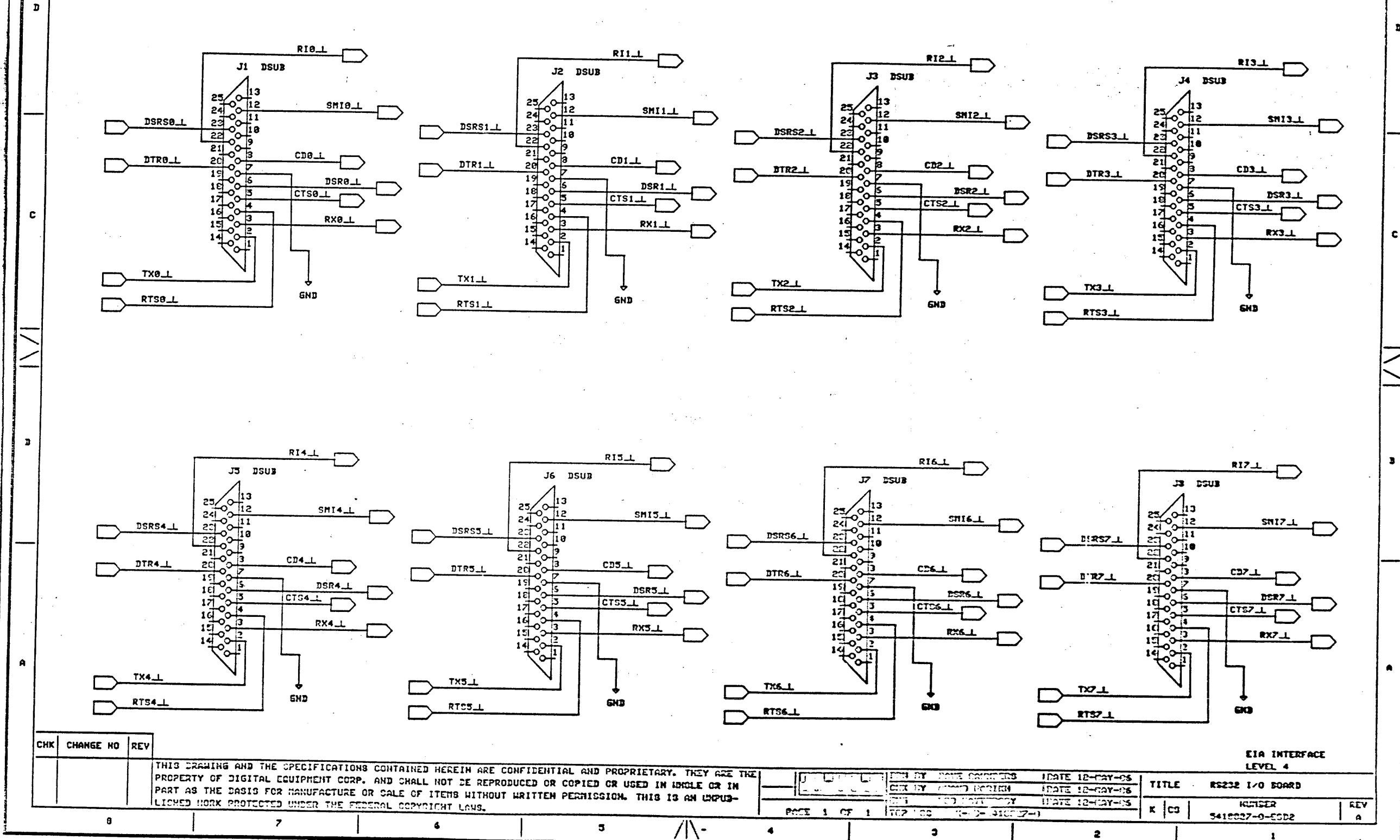
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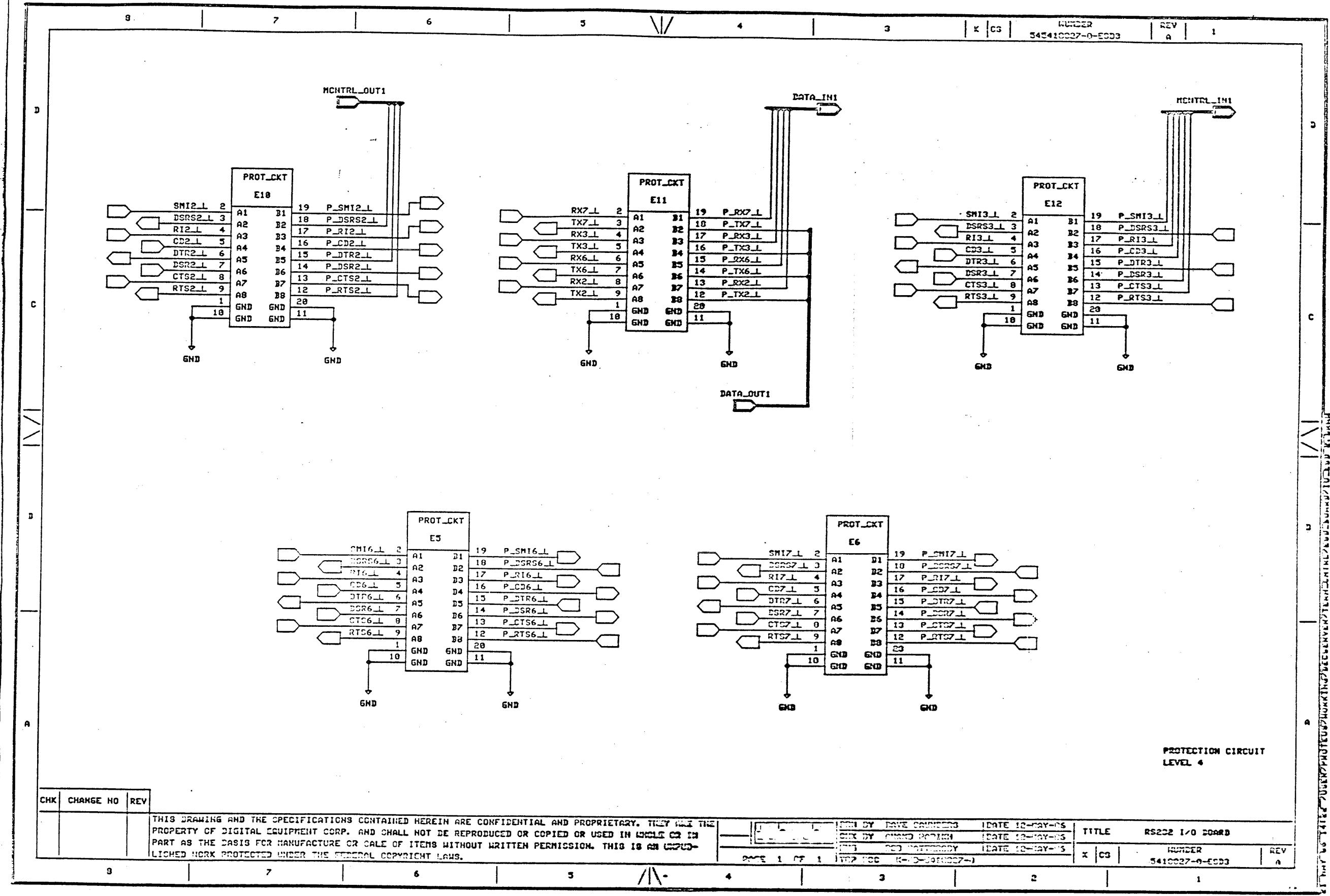
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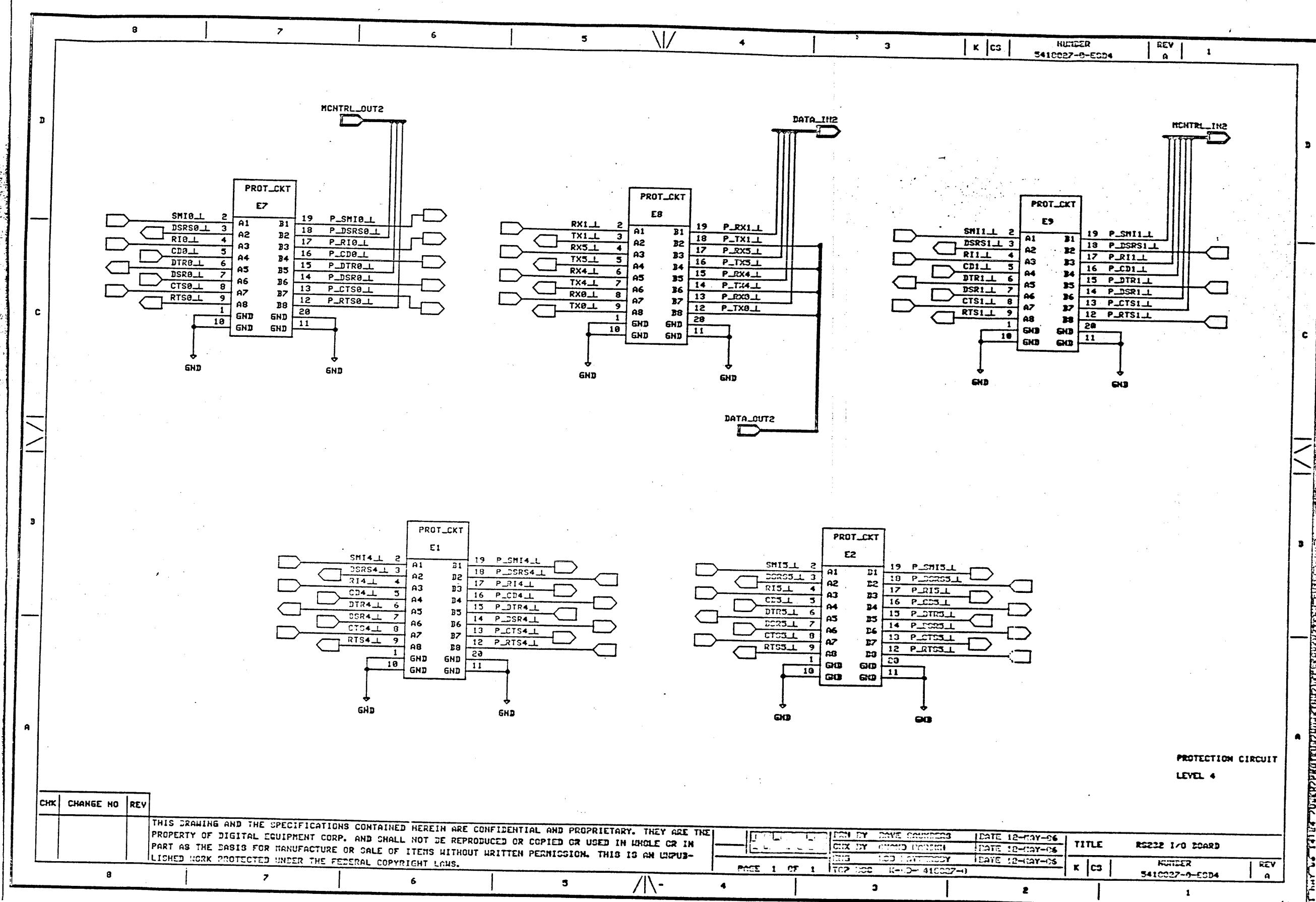
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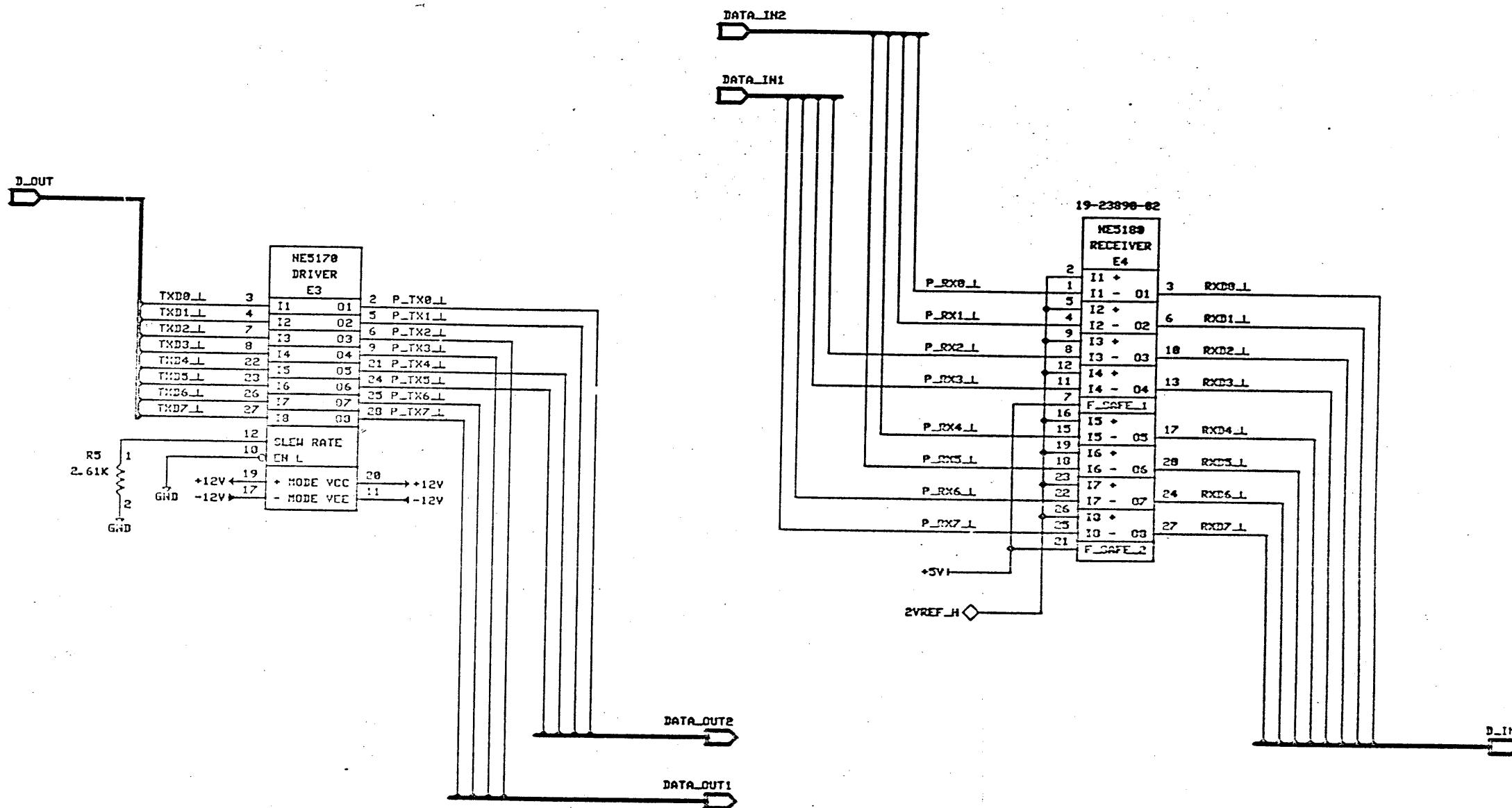
NUMBER
5410007-0-ESD1

8 | 7 | 6 | 5 | 4 | 3 | K | CS | NUMBER 5410027-0-ESD2 | REV A | 1







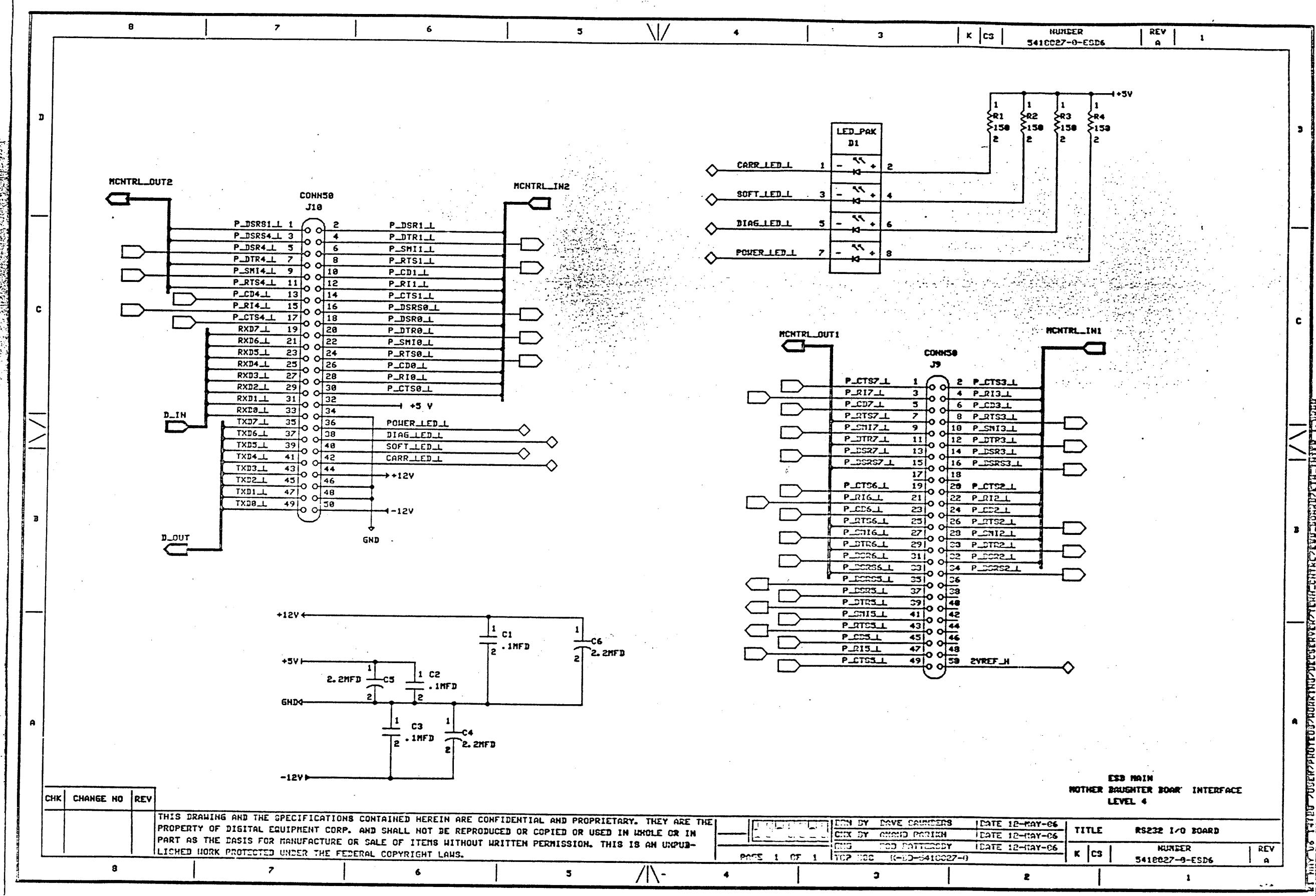


CHX	CHANGE NO	REV

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OCTAL DRIVER & RECEIVL.
LEVEL 4

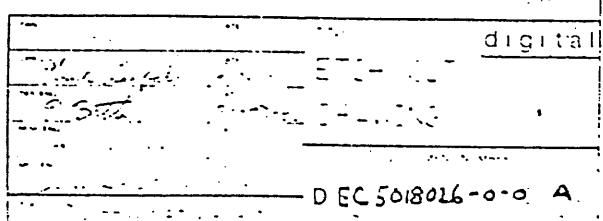
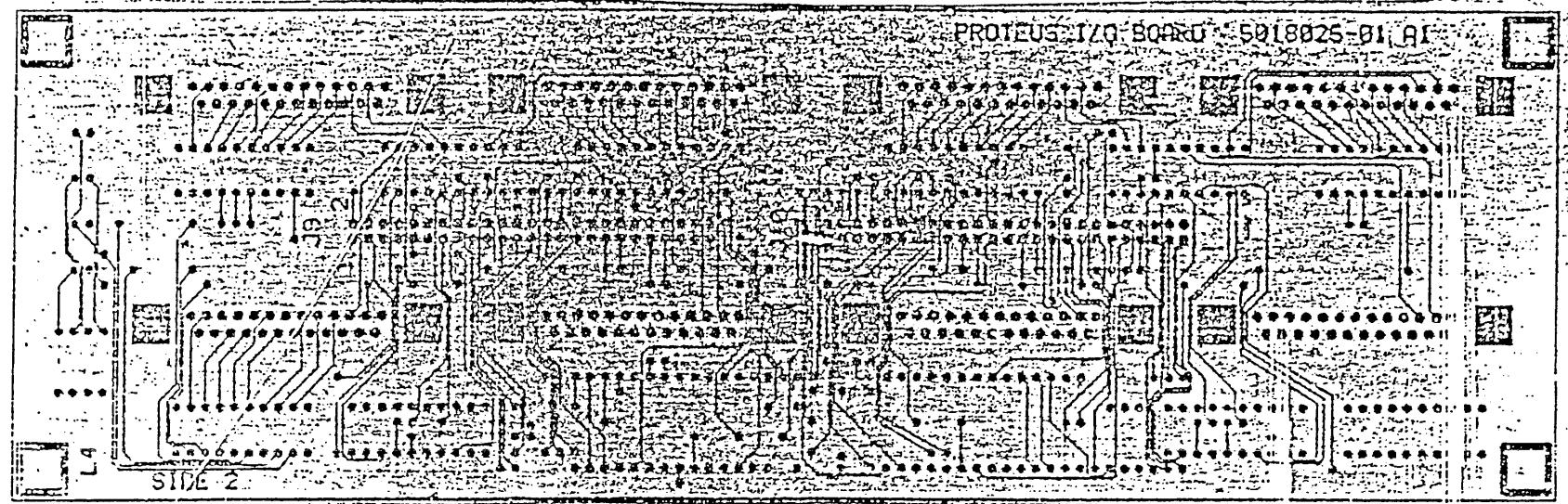
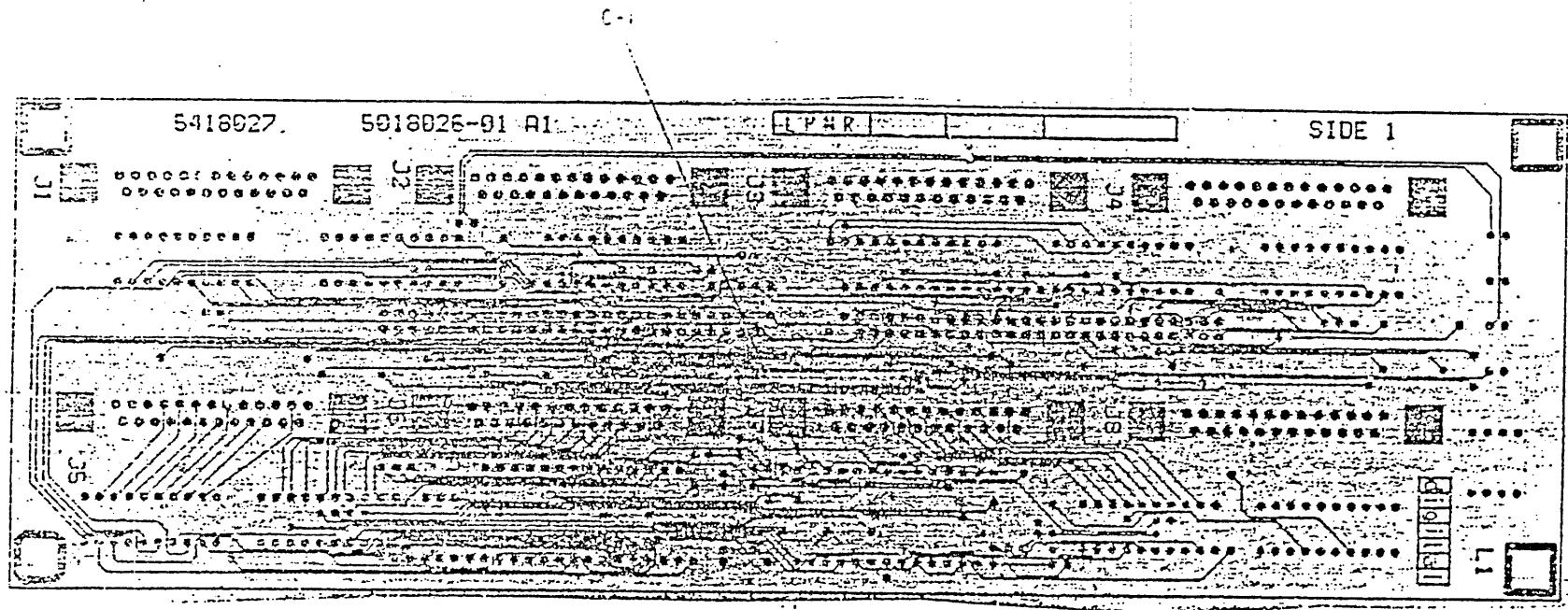
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1	2	3	4	5	6	7	8	9	10
1	2	3	4	5	6	7	8	9	10
1	2	3	4	5	6	7	8	9	10



NOTE: THIS PAGE DEFINES THE CONTENTS OF EACH BUNDLE USED IN THE RS232 I/O BOARD PRINT SET

BUNDLE NAME	CONTENTS
D_IN	RXD8_L, RXD1_L, RXD2_L, RXD3_L, RXD4_L, RXD5_L, RXD6_L & RXD7_L
D_OUT	TXD8_L, TXD1_L, TXD2_L, TXD3_L, TXD4_L, TXD5_L, TXD6_L & TXD7_L
DATA_IN1	P_RX2_L, P_RX3_L, P_RX6_L & P_RX7_L
DATA_IN2	P_RX8_L, P_RX1_L, P_RX4_L & P_RX5_L
DATA_OUT1	P_TX2_L, P_TX3_L, P_TX6_L & P_TX7_L
DATA_OUT2	P_TX0_L, P_TX1_L, P_TX4_L & P_TX5_L
MCNTRL_IN1	P_RI2_L, P_RI6_L, P_RI3_L, P_RI7_L, P_SMI2_L, P_SMI6_L, P_SMI3_L, P_SMI7_L, P_CD2_L, P_CD6_L, P_CD3_L, P_CD7_L, P_DSR2_L, P_DSR3_L, P_DSR6_L, P_DSR7_L, P_CTS2_L, P_CTS3_L, P_CTS6_L & P_CTS7_L
MCNTRL_IN2	P_RI0_L, P_RI1_L, P_RI4_L, P_RIS_L, P_SMI0_L, P_SMI1_L, P_SMI4_L, P_SMI5_L, P_CD0_L, P_CD1_L, P_CD4_L, P_CD3_L, P_DSR0_L, P_DSR1_L, P_DSR4_L, P_DSR5_L, P_CTS0_L, P_CTS1_L, P_CTS4_L & P_CTS5_L
MCNTRL_OUT1	P_DSR2_L, P_DSR3_L, P_DSR6_L, P_DSR37_L, P_DTR2_L, P_DTR3_L, P_DTR6_L, P_DTR7_L, P_RTS2_L, P_RTS3_L, P_RTS6_L & P_RTS7_L
MCNTRL_OUT2	P_DTR0_L, P_DTR1_L, P_DTR4_L, P_DTR5_L, P_DSR50_L, P_DSR51_L, P_DSR4_L, P_DSR3_L, P_RTS0_L, P_RTS1_L, P_RTS4_L & P_RTS3_L

CHK	CHANGE NO	REV	THIS DRAWING AND THE SPECIFICATIONS CONTAINED HEREIN ARE CONFIDENTIAL AND PROPRIETARY. THEY ARE THE PROPERTY OF DIGITAL EQUIPMENT CORP. AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION. THIS IS AN UNPUBLISHED WORK PROTECTED UNDER THE FEDERAL COPYRIGHT LAWS.				ISSUED BY	DAVE SAUNDERS	DATE 12-MAY-06	TITLE	RS232 I/O BOARD	
			<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	CORR BY	SHNO BORIKU	DATE 12-MAY-06	X CS	NUMBER
			<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	ENG	HOOT DOTTERTSBY	DATE 12-MAY-06	5410027-0-ESD7	REV A
							PAGE 1 OF 1	WIP NO	K-FD-5410027-1			



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REWORK INSTRUCTIONS

ETCH CUTS - SIDE 1
Q-1 AT E4-14

D

C

B

A

SIZE CODE 5018026-0-0 REV A

SIZE CODE 5018026-0-0 REV B

A

REVISION HISTORY		
REV	ECO NUMBER	DATE

DOCUMENT NUMBER	SIZE CODE	NUMBER	REV
ETCH CUT DRAWING	DEC	5018026-0-0	A

DRAWING NO.	NO. SHTS	PART NO.	DESCRIPTION	REVISIONS
		5418034-01	DECSERVER 200	A1 B1 B2 B3
		5418034-02	DECSERVER 200	A1 B1 B2 B3
		5418034-03	DECSERVER 200	A1 B1 B2 B3
D-UA-5418034-0-0	2		DECSERVER 200 UNIT ASSEMBLY	A B C D
K-PL-5418034-0-DBP	4		DECSERVER 200 PARTS LIST	A B C D
K-PC-5418034-0-DBV	-		DECSERVER 200 PC DATA BASE	C C C C
K-DD-5018033-0-C	1		DRAWING DIRECTORY	A B B B
K-CS-5418034-0-DBD	-		DAISY DATA BASE	A B B B
K-CS-5418034-0-AR31	1		DECSERVER 200 SCHEMATIC	A A A A
K-BD-5418034-0-B1	1		DECSERVER 200 BLOCK DIAGRAM	A A A A
K-BD-5418034-0-B2	1		DECSERVER 200 BLOCK DIAGRAM	A A A A
K-BD-5418034-0-B3	1		DECSERVER 200 BLOCK DIAGRAM	A A A A
K-BD-5418034-0-B4	1		DECSERVER 200 BLOCK DIAGRAM	A A A A
K-BD-5418034-0-CPU1	1		DECSERVER 200 BLOCK DIAGRAM	A A A A
K-CS-5418034-0-CPU2	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-0-CPU3	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-0-CPU4	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-0-CPUS	1		DECSERVER 200 SCHEMATIC	A A A A

NOTES:

R	A	B	C	D
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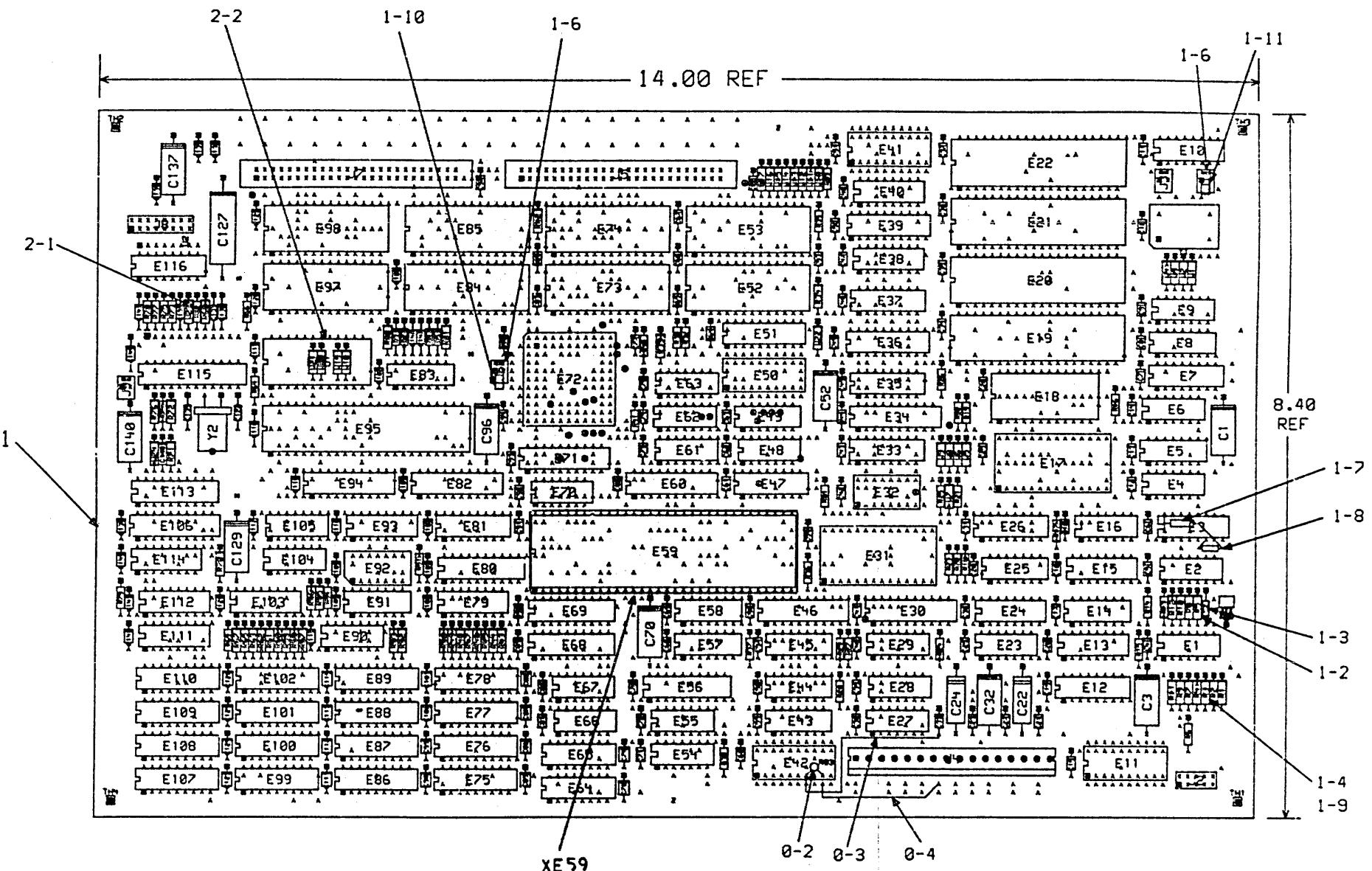
DRW: H. DENSMORE	DATE: 27-JAN-88	d	1	3	1	t	a	l
CHK'D: D. JAWUSKIEWICZ	DATE: 27-JAN-88	TITLE						
DECSERVER 200								
DES.ENG.: L. GRISWOLD	DATE: 27-JAN-88	SHEET 1 OF 2		EDIT:	3			
RESP.ENG.: R. BLANCHETTE	DATE: 27-JAN-88	DOCUMENT NUMBER						
Thomas R. Blanchette		DD FILENAME: 54180340.DDF						
MFG.ENG.: A. RIVERA	DATE: 27-JAN-88	SIZE	CODE	NUMBER	REV			
		K	DD	5418034-0-0	D			

DRAWING NO.	NO. SHTS	PART NO.	DESCRIPTION	REVISIONS
K-CS-5418034-0-CPU6	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-0-CPU7	1		DECSERVER 200 SCHEMATIC	A S B B
K-CS-5418034-0-CPU8	1		DECSERVER 200 SCHEMATIC	A A A A
K-BD-5418034-0-MEM1	1		DECSERVER 200 BLOCK DIAGRAM	A A A A
K-CS-5418034-0-MEM2	1		DECSERVER 200 SCHEMATIC	A B S B
K-CS-5418034-0-MEM3	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-0-MEM4	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-0-MEM5	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-0-MEM6	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-0-MEM7	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-0-MEM8	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-0-MEM9	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-0-MEM10	1		DECSERVER 200 SCHEMATIC	A A A A
K-BD-5418034-0-INT1	1		DECSERVER 200 BLOCK DIAGRAM	A A A A
K-CS-5418034-0-INT2	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-C-INT3	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-0-INT4	1		DECSERVER 200 SCHEMATIC	A A A A
K-BD-5418034-0-TRM1	1		DECSERVER 200 BLOCK DIAGRAM	A A A A
K-CS-5418034-0-TRM2	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-0-TRM3	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-0-TRM4	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-0-TRM5	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-0-TRM6	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-0-TRM7	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-0-TRM8	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-0-TRM9	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-0-TRM10	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-C-TRM11	1		DECSERVER 200 SCHEMATIC	A A A A
K-BD-5418034-0-NET1	1		DECSERVER 200 BLOCK DIAGRAM	A A A A
K-CS-5418034-C-NET2	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-0-NET3	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-C-NET6	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-0-NETS	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-0-PWR	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-0-CAP1	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-0-CAP2	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-0-D1	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-C-D2	1		DECSERVER 200 SCHEMATIC	A A A A
K-CS-5418034-C-D3	1		DECSERVER 200 SCHEMATIC	A A A A

d i g i t a l
TITLE
DECSERVER 200

SHEET 2 OF 2	SIZE	CODE	DOCUMENT NUMBER	REV
	K	D3	S418034-0-0	D

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REV	<p><u>NOTES</u></p> <ol style="list-style-type: none"> 1. UL RATING IS LPWR. 2. SPARE LOCATIONS: E11,E41,E50,E113. 4. FOR VARIATION -01, COMPONENTS NOT TO BE INSTALLED ARE: E78,E89,E102,E110,W1,M3-W17,W22,W24. 5. FOR VARIATION -02, COMPONENTS NOT TO BE INSTALLED ARE: E52,E53,E72-E74,E84,E85,E89,E97,E98,E102,E110,W1,W4.
-----	--

	W7,W24,C53,C59,C65-C67,C83,C84, C95,C97,C106,C120,C121,C146,C147, D2-D5,R34,R39,R42,R80,R81,J7.
6. FOR VARIATION -03, COMPONENTS NOT TO BE INSTALLED ARE, W2,W3,W5-W17, W22,W24.	S CH CH ME PR PS SC 'S

INITIATOR	DATE	digital	
JAN S. MONITOR	1/26/87		
CHECKED F. GARRALDO	2/23/87		
CH ENG D. DECROIX	1/26/86	TITLE DEC SERVER 222	
DOJ ENG D. DECROIX	1/26/86	MEDIA BOARD	
DOO J. SEGUINOT	5/13/86		
FILE 1-5/1	SIZE CODE	NUMBER	REV
HEET 1 OF 2	D	5418034-0-0	D
FILE NO. K-DD-5418034-3-2			

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D REWORK INSTRUCTIONS

COMPONENT ADDS:

- 0-2 ADD THERMISTER (13-26688-01) R83 TO SPARE
LOCATION E42 BETWEEN E42-9 AND E42-7.

ECO 5418034-MK002

FOR 5418034-01,02,03 REV B2 NEW BUILD
COMPONENT DELETES

- 2-1 REMOVE P/N 13-00229-00 (REF. DES. R82).
- 2-2 REMOVE P/N 21-25710-02 (REF. DES. E96).

WIRE ADDS - SIDE 1

- 0-3 FROM E42-7 TO C33-2.
- 0-4 FROM E42-9 TO PTH LOCATED NEAR J4-7 AND J4-8.

C ECO 5418034-LK001

COMPONENT DELETES

- 1-2 REMOVE COMPONENT R5 (27K RES P/N 13-05346-00) FROM BOARD.
ALSO SEE STEP NUMBER 1-7.
- 1-3 REMOVE COMPONENT C2 (100 pF CAP P/N 10-13466-06) FROM BOARD.
ALSO SEE STEP NUMBER 1-8.
- 1-4 REMOVE COMPONENT R3 (1.5K RES P/N 13-12930-00) FROM BOARD.
ALSO SEE STEP NUMBER 1-9.
- 1-5 REMOVE COMPONENT J6 (2 PIN HEADER P/N 12-19251-00) FROM BOARD.
- 1-6 REMOVE COMPONENT J1 (2 PIN HEADER P/N 12-19251-00) FROM BOARD.

COMPONENT ADDS

- 1-7 ADD A REPLACEMENT COMPONENT R5 (27K RES P/N 13-05346-00)
BETWEEN IC LEADS E3-16 AND E3-7 AND SOLDER.
- 1-8 ADD A REPLACEMENT COMPONENT C2 (100 pF CAP P/N 10-13466-06)
BETWEEN IC LEADS E3-6 AND E3-7 AND SOLDER.
- 1-9 ADD NEW VALUE OF COMPONENT R3 (2K RES P/N 13-02388-00) TO
SAME LOCATION ON BOARD.

WIRE ADDS

- 1-10 ADD WIRE (30 AWG P/N 90-05740-55) TO PTH LOCATIONS
WHERE J6 WAS.
- 1-11 ADD WIRE (30 AWG P/N 90-05740-55) TO PTH LOCATIONS
WHERE J1 WAS.

DOCUMENT NUMBER	
SIZE/CODE	NUMBER
D Ua	5418034-0-0
REV	
SHEET 1 OF 2	

AUTOMATED BY VAXXPL (V1.3)				PARTS LIST				SHEET A2 OF A6			
LINE ITEM	TOP DOCUMENT	PART NUMBER	REV	DESCRIPTION		QTY PER VAR/REV		REFERENCE DESIGNATORS			
				R1	R2	R3	R3				
18	20	12-16652-18		PCB HEADER 16PIN(2X08).155CC STR		1	1	1	J4		
19	21	12-19251-00		PCB HEADER 02PIN(1X02).100CC STR		1	1	1	J3		
20	22	12-23627-01		PCB HEADER 50PIN(2X25).100CC STR		2	-	2	J5,J7		
		CONT				-	1	-	J5		
21	23	13-00005-07	P	NET 4.7K-15 5.0		3	3	3	E7,E37,E31		
22	24	13-00197-00	33.0	.25 W 5.0 %	CF	21	21	21	R43,R50,R52,R53,R55-R63,R65,R67		
23	25	13-00229-00	100.0	.25 W 5.0 %	CF	21	21	21	R16-R17,R19-R20,R22-R26,R28		
		CONT							CONT R23,R31,R33,R35,R37,R39,R51		
		CONT							CONT R54,R64,R59,R74,R79		
24	26	13-00247-00	120.0	.25 W 5.0 %	CF	2	2	2	R29,R30		
25	27	13-00316-00	470.0	.25 W 5.0 %	CF	1	1	1	R9		
26	28	13-00363-00	1.0	K .25 W 5.0 %	CF	1	1	1	R8		
27	29	13-00432-00	3.0	K .25 W 5.0 %	CF	1	1	1	R71		
28	30	13-00539-00	120.0	K .25 W 5.0 %	CF	1	1	1	R11		
29	31	13-01322-00	180.0	.25 W 5.0 %	CF	2	2	2	R27,R32		
30	32	13-02338-00	2.0	K .25 W 5.0 %	CF	11	11	11	R3,R6,R13,R15,R21,R23,R25,R36,		
		CONT							CONT R40,R41,R66		
31	33	13-02397-00	180.0	K .25 W 5.0 %	CF	2	2	2	R1,R2		
32	34	13-02371-00	1.21	K .25 W 1.0 % RN55D-F10		1	1	1	R6		
33	35	13-03114-00	1.0	K .25 W 1.0 % RN55D-F10		1	1	1	R7		
34	36	13-03179-00	8.20	K .25 W 5.0 %	CF	2	2	2	R10,R12		
35	37	13-03303-00	2.61	K .25 W 1.0 % RN55D-F10		3	-	3	R36,R39,R42		
36	38	13-04341-00	75.0	K .25 W 5.0 %	CF	1	1	1	R70		
37	39	13-05346-00	27.0	K .25 W 5.0 %	CF	1	1	1	R5		
38	40	13-05621-00	111.1	.25 W 0.1 % RN55D-B10		2	-	2	R50,R81		
39	41	13-09422-00	5.10	.25 W 5.0 %	CF	1	1	1	R68		
40	42	13-11522-00	200.0	.25 W 5.0 %	CF	1	1	1	R75		
41	44	13-18150-75	40.20	.25 W 1.0 % RN55D-F10		4	4	4	R73,R75,R77,R78		
42	45	13-18247-00	510.0	.25 W 5.0 %	CF	1	1	1	R72		
43	46	13-26538-01		THERMISTOR PTC 0.052 15%		1	1	1	R83		
44	47	16-15555-01		DELAY= 100NS,10TAPS WITH TTLBU		1	1	1	R92		
45	48	16-23072-01		XFA9/75UH		1	1	1	E116		
46	49	13-11660-50		OSCILLATOR XTAL 29.6912 MHZ		1	1	1	Y1		
47	50	18-22515-01		XTAL 20.000 MHZ		1	1	1	Y2		
48	51	13-12108-01		LM 339ANVOLT. CMPTR,QUAD		1	1	1	E1		
49	52	19-12729-00		LS00 NAND-GATE-QUAD 2INx2		1	1	1	E9		
50	53	19-12805-00		LS08 AND GATE-QUAD 2INxPO		2	2	2	E5,E49		
51	54	19-12810-00		LS20 NAND GATE-DUAL 4IN		1	1	1	E16		
52	55	19-12811-01		LS21 AND GATE-DUAL 4INxPO		1	1	1	E4		
53	56	19-12816-00		LS32 OR GATE-QUAD 2INxPO		2	2	2	E8,E43		
54	57	19-12819-00		LS33 NAND BUFFER-QUAD 2IN		1	1	1	E25		
55	58	19-12824-00		LS74 FF-D DUL,EDGE TRIG		2	2	2	E24,E28		
56	59	19-12837-00		LS123 ONE SHOT-DUAL,RETIG		2	2	2	E3,E114		
57	60	19-12845-00		LS153 MUX 1 OF 4 (DUAL)		1	1	1	E15		
58	61	19-12847-00		LS157 MUX 1 OF 2 (QUAD)		3	3	3	E13,E33,E40		
59	62	19-12847-00		LS158 MUX 1 OF 2 (QUAD)		1	1	1	E12		
60	63	19-12853-00		LS273 FF-D OCTAL 4/CLEAR		1	1	1	E69		
61	64	19-13116-00		LS114 INVERTER GATE-HEX SE		1	1	1	E104		
62	65	19-16075-00		74S208 NOR GATE-DUAL,PO3		1	1	1	E65		
63	66	19-13173-00		LS244 DRIVER-LINE,OCTAL,TR		1	1	1	E34		
TITLE				SECTION A OF 4				SIZE CODE DOCUMENT NUMBER REV			
DEC SERVER 200 MAIN LOGIC								K PL 5418034-3-DBP D			

AUTOMATED BY VAXPL (V1.3)

LINE ITEM	TOP DOCUMENT	PART NUMBER	REV	PARTS LIST			STY PER VAR/REV 01 02 03 93 83 93	REFERENCE DESIGNATORS	SHEET A3 OF A6
				DESCRIPTION					
64	67	19-19006-00	L4	336A VOLT REF. 2.67V 1X	1	1	1	E1	
55	63	19-19018-00		LS143 PRIORITY ENCODER,3 T	4	4	4	E26,E33,E35,E47	
66	59	19-20441-01		74F245 TRANSCEIVER, BI-DIREC	2	2	2	E60,E71	
67	73	19-20342-01		74F374 FF-D, OCTAL,TRI-STATE	3	3	3	E36,E39,E51	
68	71	19-20572-01		74F246 BUFFER/LINE DRIVER,0	2	2	2	E36,E68	
59	72	19-21039-01		74F241 BUFFER/LINE DRIVER,4	1	1	1	E80	
70	73	19-21010-01		74F373 OCTAL TRANSPARENT LA	4	4	4	E65,E82,E94,E106	
71	74	19-21303-01		74F00 NAND GATE, QUAD,2-IN	1	1	1	E57	
72	75	19-21326-01		74F02 NOR GATE, QUAD,2-IN	1	1	1	E6	
73	76	19-21307-01		74F04 HEX INVERTER	3	3	3	E16,E44,E91	
74	77	19-21576-01		74F02 AND GATE, QUAD,2-IN	1	1	1	E58	
75	78	19-21311-01		74F20 NAND GATE, DUAL,4-IN	2	2	2	E27,E90	
76	79	19-21312-01		74F32 OR GATE, QUAD,2-IN	3	3	3	E23,E29,E62	
77	80	19-21314-01		74F76 FF-D,DUAL,POS.,EDGE	6	6	6	E63,E67,E03,E06,E70,E105	
78	81	19-21315-01		74F86 EXCLUSIVE OR GATE, QU	1	1	1	E56	
79	82	19-21316-01		74F138 DECODER/DEMUX,1-OF-3	1	1	1	E24	
80	83	19-21317-01		74F139 DECODER, DUAL,1-OF-4	1	1	1	E79	
81	84	19-21322-01		74F151 BINARY COUNTER, PRES	1	1	1	E10	
82	85	19-21331-01		74F258 MUX, QUAD,2-IN	3	3	3	E103,E111,E112	
83	86	19-21332-01		74F250 PARITY GEN/CHECK,9-B	2	2	2	E55,E67	
84	87	19-21671-03		INTERFACE, SERIAL ADAPTOR 300MIL	1	1	1	E115	
85	88	19-22191-01		74F353 MULTIPLEXER, DUAL 4I	1	1	1	E93	
86	89	19-23389-02		EIA OCTAL LINE DRIVE	3	-	3	E53,E74,E85	
87	90	19-23390-02		EIA OCTAL LINE RECEI	5	-	5	E52,E73,E84,E97,E98	
88	91	19-25705-01		74F37 QUAD 2-INPUT NAND BU	1	1	1	E83	
89	92	21-20057-01		68000 MICROPROCESSOR, 16BIT	1	1	1	E59	
90	93	21-20063-01		2531 REC./TRANS., ASYNC.,D	4	4	4	E19-E22	
91	94	21-21414-02		50256 RAM 256KX1 DYNAMIC 4	2	2	2	E64,E65	
92	95	21-21672-07		CONTROLLER, LOCAL AREA NET	1	1	1	E95	
93	96 SEE NOTE 2	21-22421-02		*** THIS ITEM IS NOT USED ***	-	-	-	E75-E77,E86-E88,E99-E101,	
94	97 SEE NOTE 4	21-22422-02		*** THIS ITEM IS NOT USED ***	-	-	-	CONT E107-E109	
95	98	21-24412-02		RAM, 64KX4, DYNAMIC 1S	12	12	-	E75-E78,E86-E89,E97-E102,	
		CONT					CONT E107-E110		
96	99 SEE NOTE 2	21-24414-02		*** THIS ITEM IS NOT USED ***	-	-	-		
97	100			*** THIS ITEM IS NOT USED ***	-	-	-		
98	101	21-25975-02		DC7053B DEC SERVER GATE ARRAY	1	-	1	E72	
99	102	23-15342-00		K3-03 PAL, QUAD, AND-0	1	1	1	E30	
100	103	23-27345-00		85-01,ES-02,ES-03	804	1	1	E31	
101	104	23-27543-00		85-01,ES-02,ES-03	804	1	1	E17	
102	105	23-36541-00		A1-09	1	1	1	E32	
103	106	25-471-00		N1-04	1	1	1	E18	
104	107	70-091725-00		JUMPER, WIRE, INSULATED, BLACK 9	6	-	-	W2,W15-W21,W23	
		CONT					W1-W6,W13-W21,W23		
105	108 NOTE	21-35740-55		PIPE(WRAP) 30AWG KYNAR	UL14	1	1	W2,W3,W5,W6,W8-W23	
106	109 NOTE	21-07727-50		PIPE(WRAP) 25AWG KYNAR	UL14	6	6		
					20	-			

0	1	2	3	4	5	6	7	8	9	
TITLE				SECTION A OF A				SIZE!CODE! DOCUMENT NUMBER ! REV		
DEC SERVER 200 MAIN BOARD								K ! PL ! 5418034-0-03P ! 0		

AUTOMATED BY VAXKPL (V1.3)

LINE ITEM TOP DOCUMENT PART NUMBER MIN REV

PARTS LIST

QTY PER VAR/REV
01 02 03
93 03 03SHEET A6 OF A6
REFERENCE DESIGNATORS

- 1 GEN: 1. SPARE LOCATIONS: E11, E41, E50, E113.
 2 GEN: 2. P/N 21-24414-02 MAY BE SUBSTITUTED FOR P/N 21-24412-02, USING THE SAME REFERENCE DESIGNATORS.
 3 GEN: 3. P/N 21-22421-02 OR 21-22422-02 MAY BE SUBSTITUTED FOR P/N 21-21414-02, USING THE SAME REFERENCE DESIGNATORS.
 4 GEN: 4. FOR VARIATION -01, COMPONENTS NOT TO BE INSTALLED ARE: E73, E39, E102, E110, W1, W3-W17, W22, W24, C88, C101, C114, C126.
 5 GEN: 5. FOR VARIATION -02, COMPONENTS NOT TO BE INSTALLED ARE: E52, E53, E72-E74, E78, E84, E85, E87, E93, E102, E110, W1, W4, W7,
 CONTINUED W24, C53, C59, C55-C57, C77, C33, C34, L94, C95, C97, C105, C120, C121, C146, C147, C83, C101, C114, C126, D2-D5, R36,
 CONTINUED R39, R42, R50, R51, J7.
 7 GEN: 6. FOR VARIATION -03, COMPONENTS NOT TO BE INSTALLED ARE: W2, W3, W5-W17, W22, W24.
 9 GEN: NOTE: QUANTITY OF ITEM #103 AND ITEM 4109 IS SHOWN IN INCHES.

D	I	G	Z	T	A	L	TITLE		SECTION A OF A		SIZE/CODES DOCUMENT NUMBER		REV	
						DEC 1978 F 200						< PL 5415034-0-05F		D

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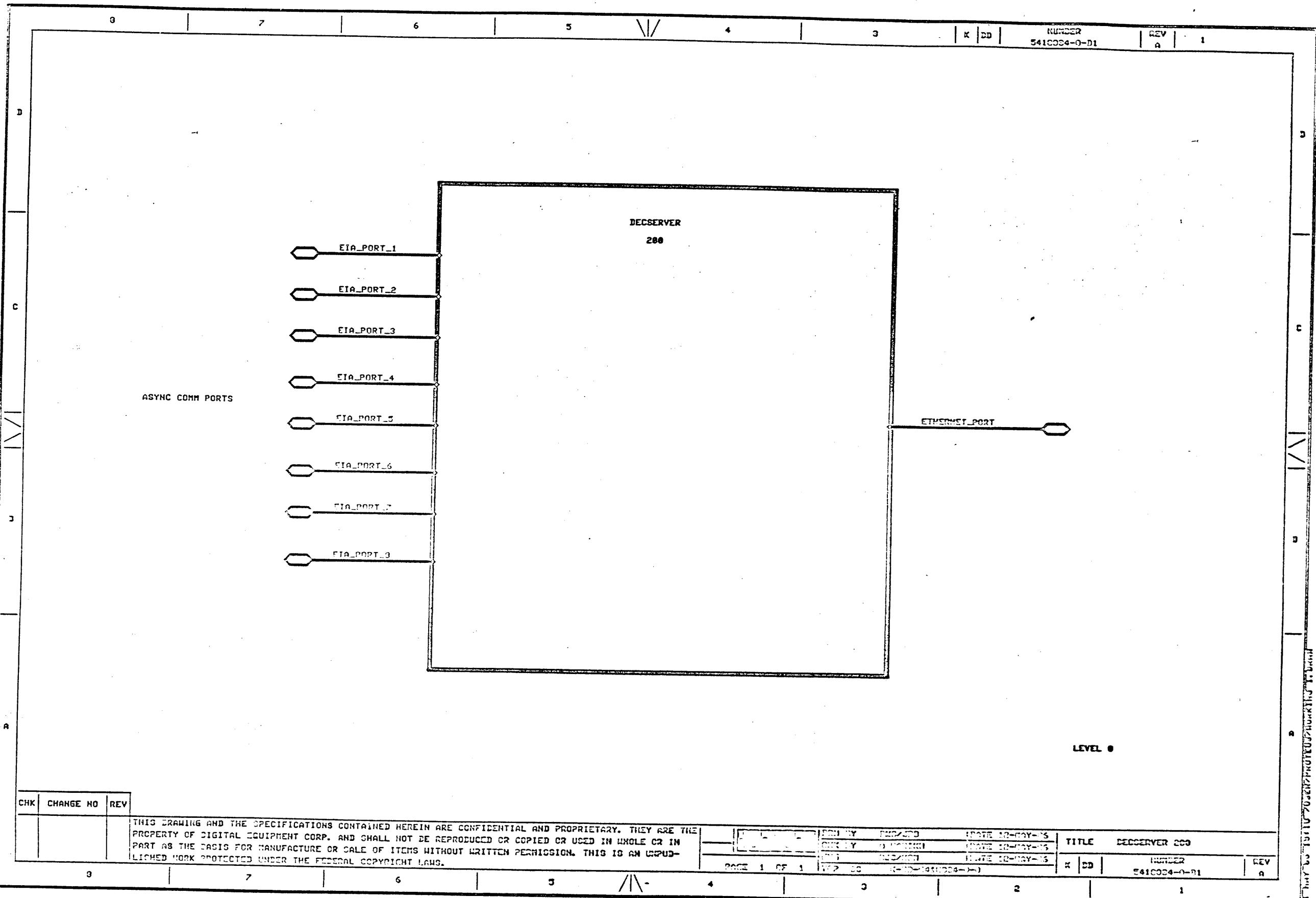
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541C034-0-01

REV

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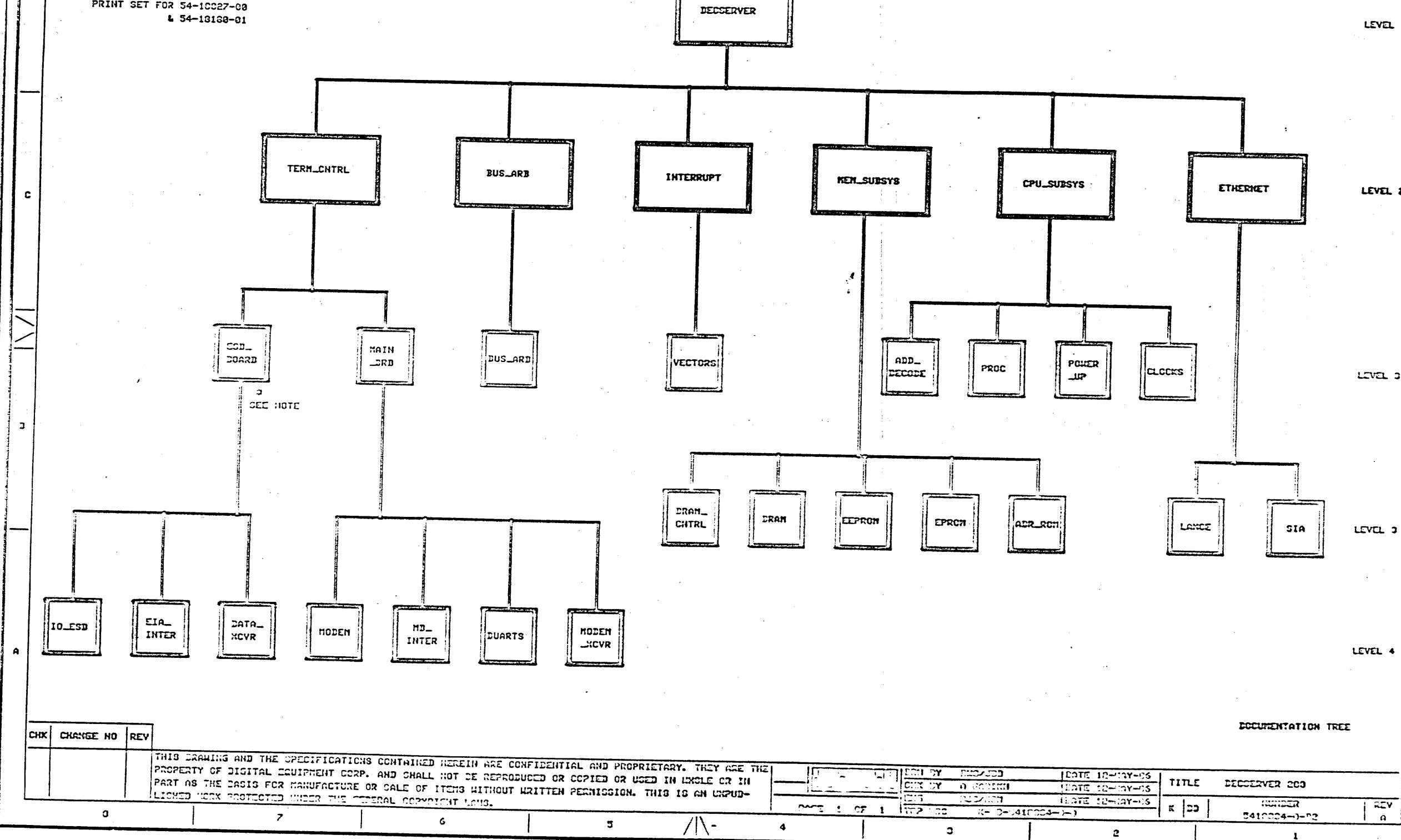
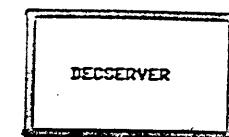
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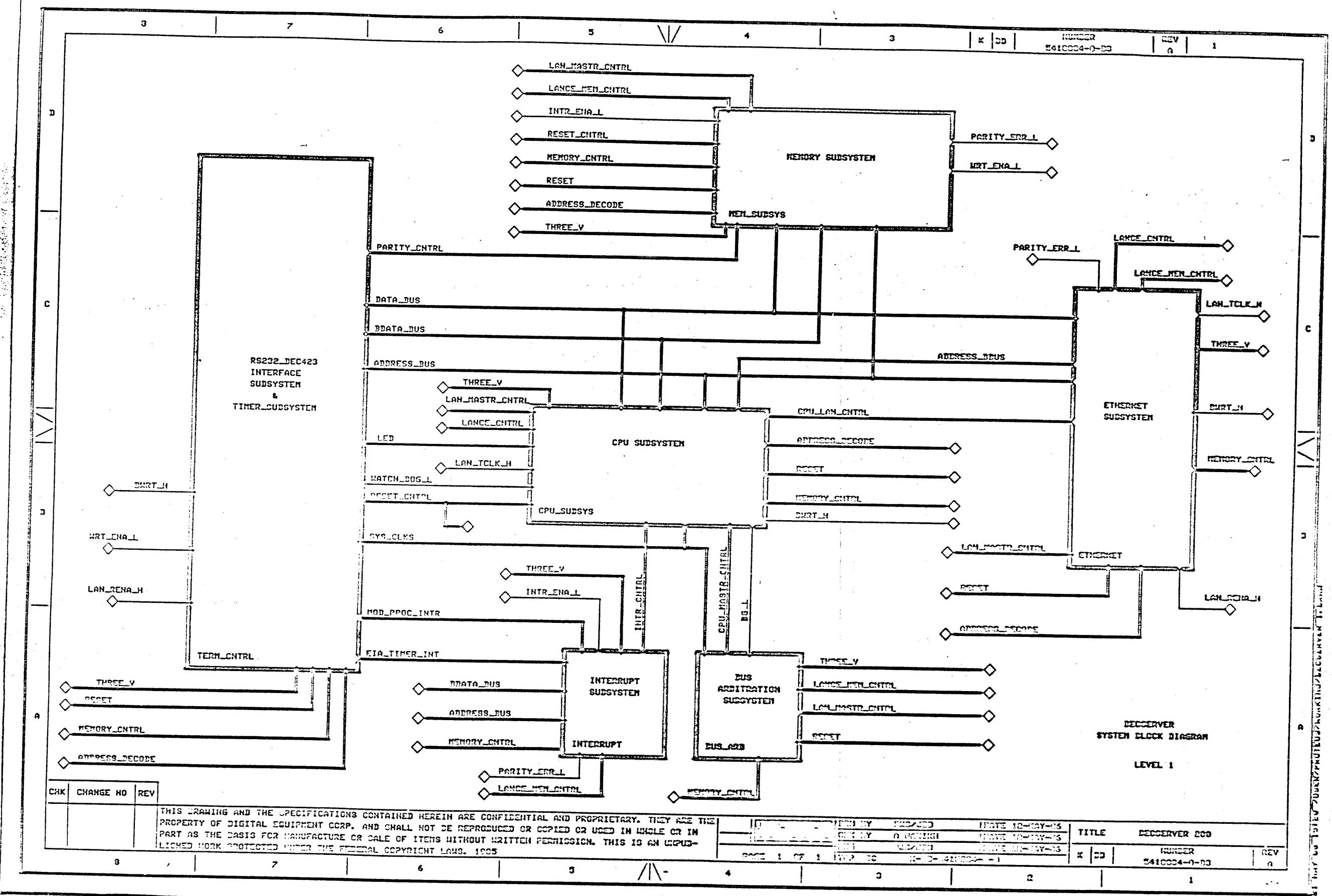


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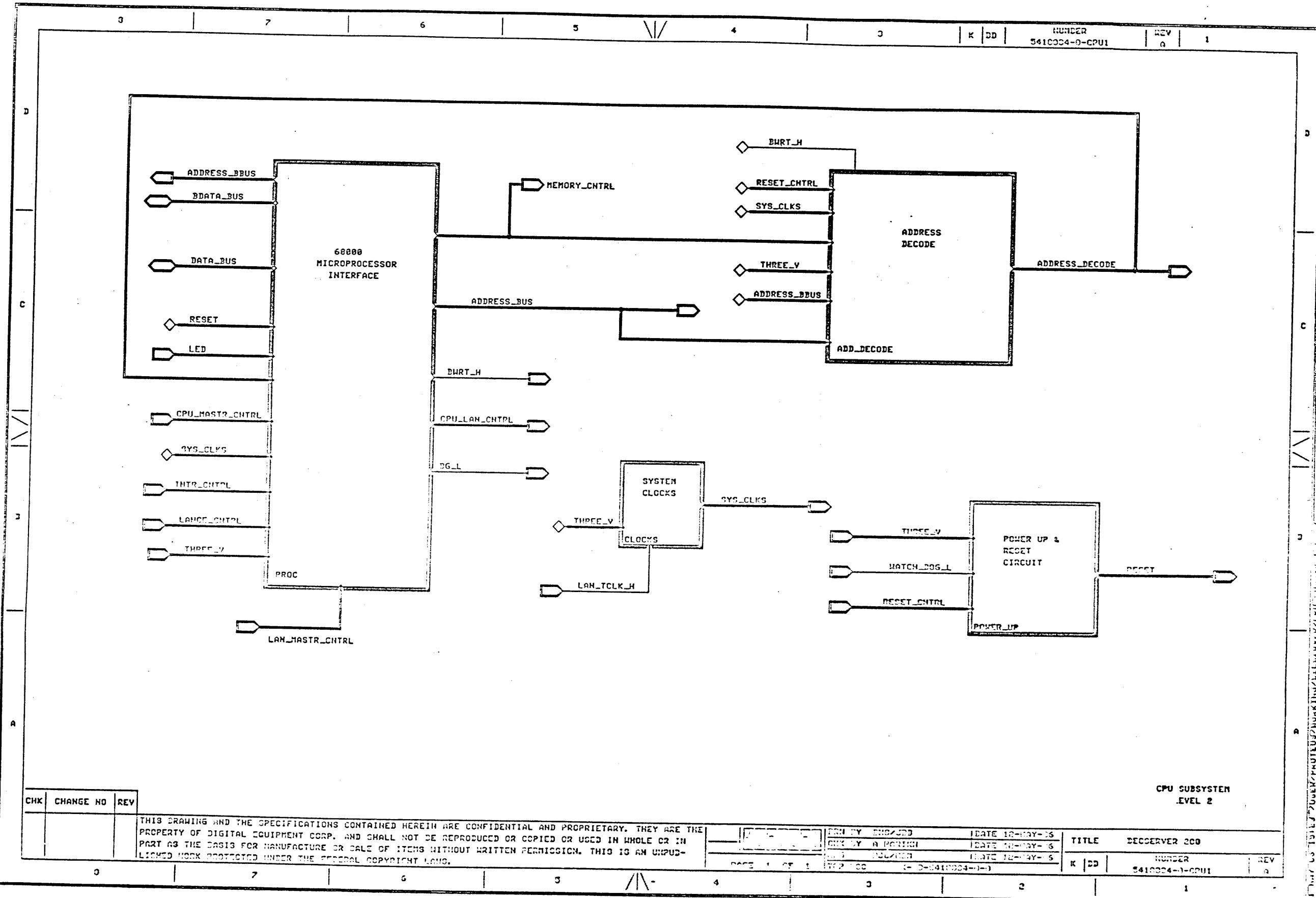
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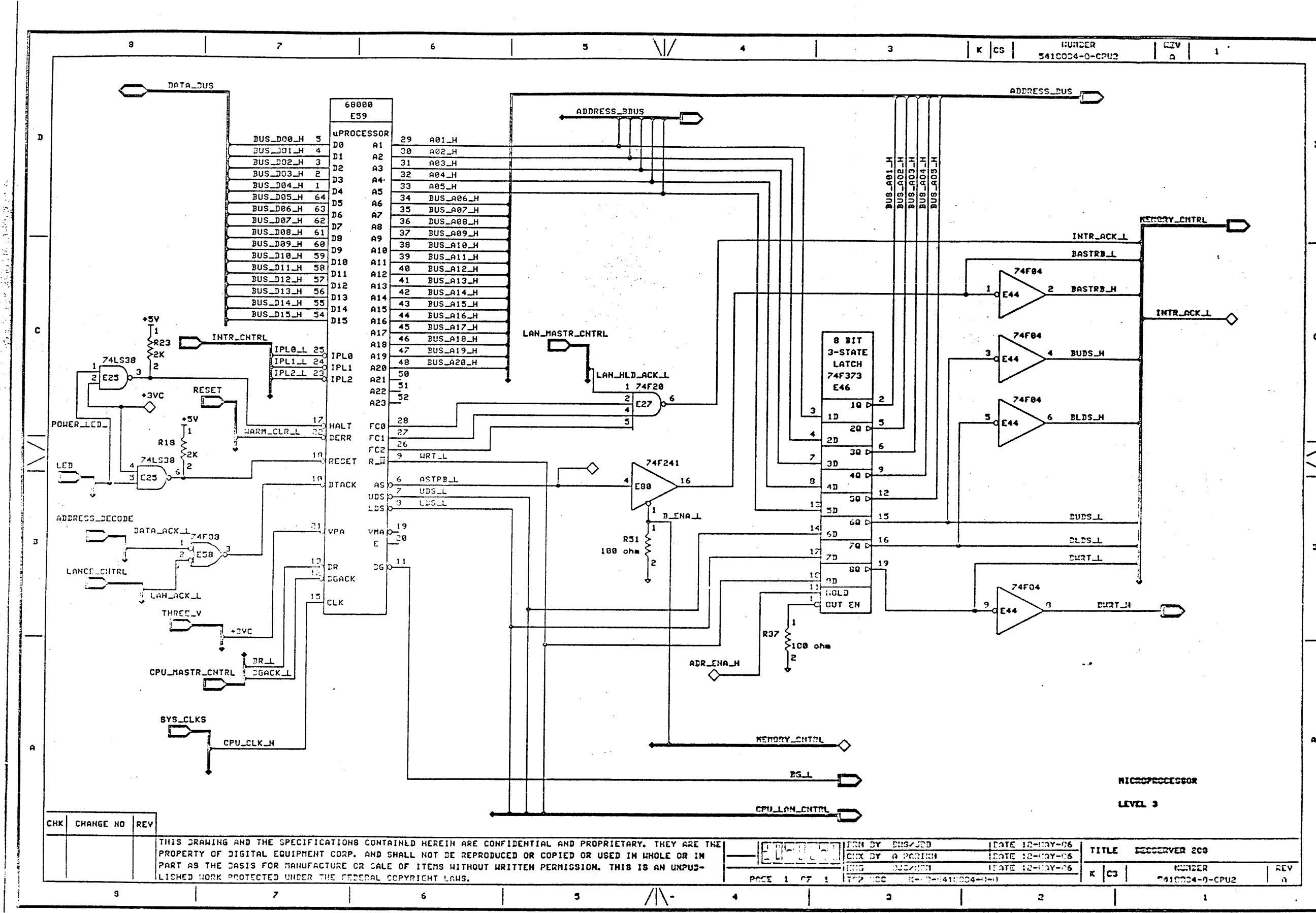
NOTE:
THE PRINT SET FOR THE
ECD BOARD IS RELEASED
SEPERATELY. REFER TO
PRINT SET FOR 54-10027-00
& 54-18168-01





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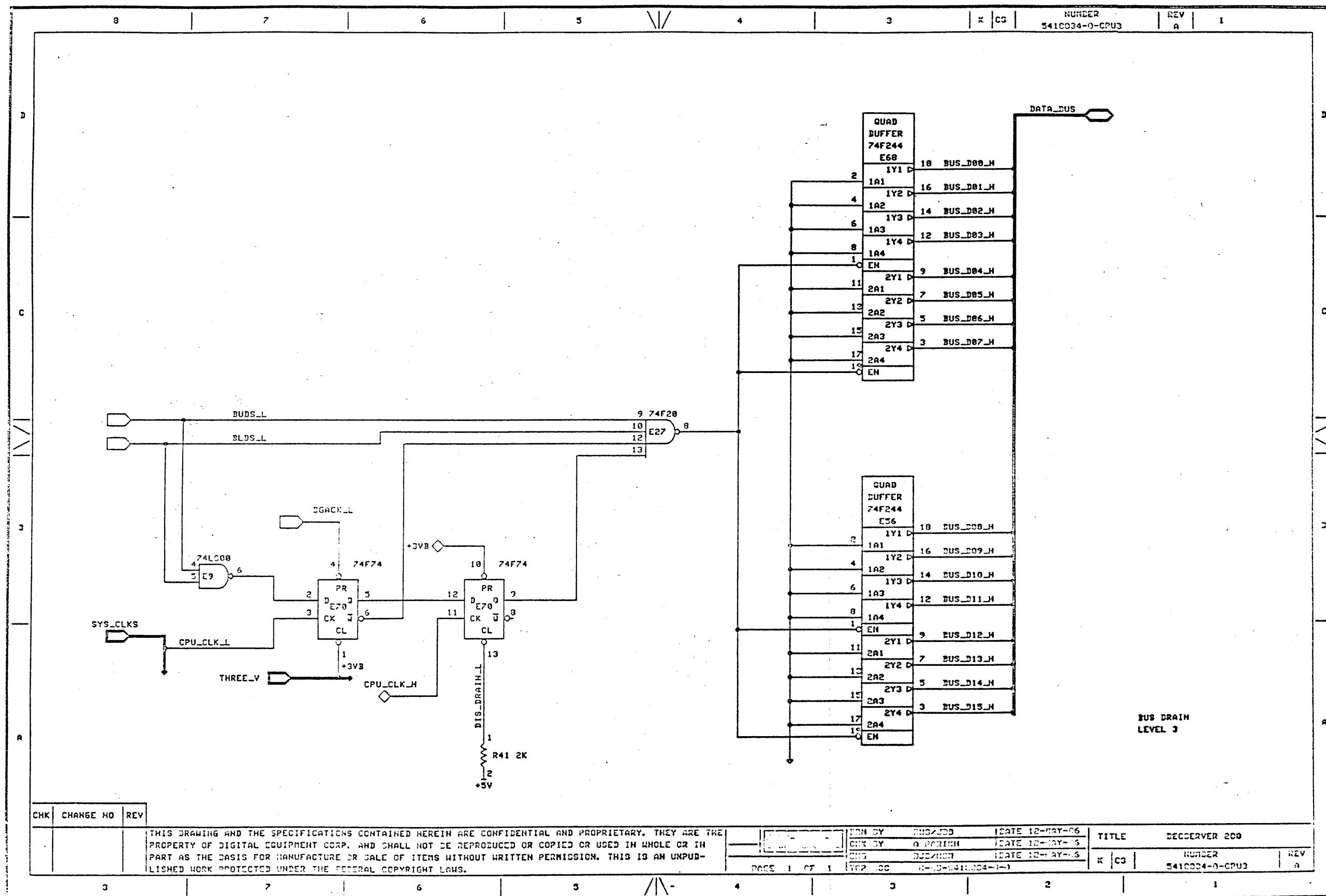


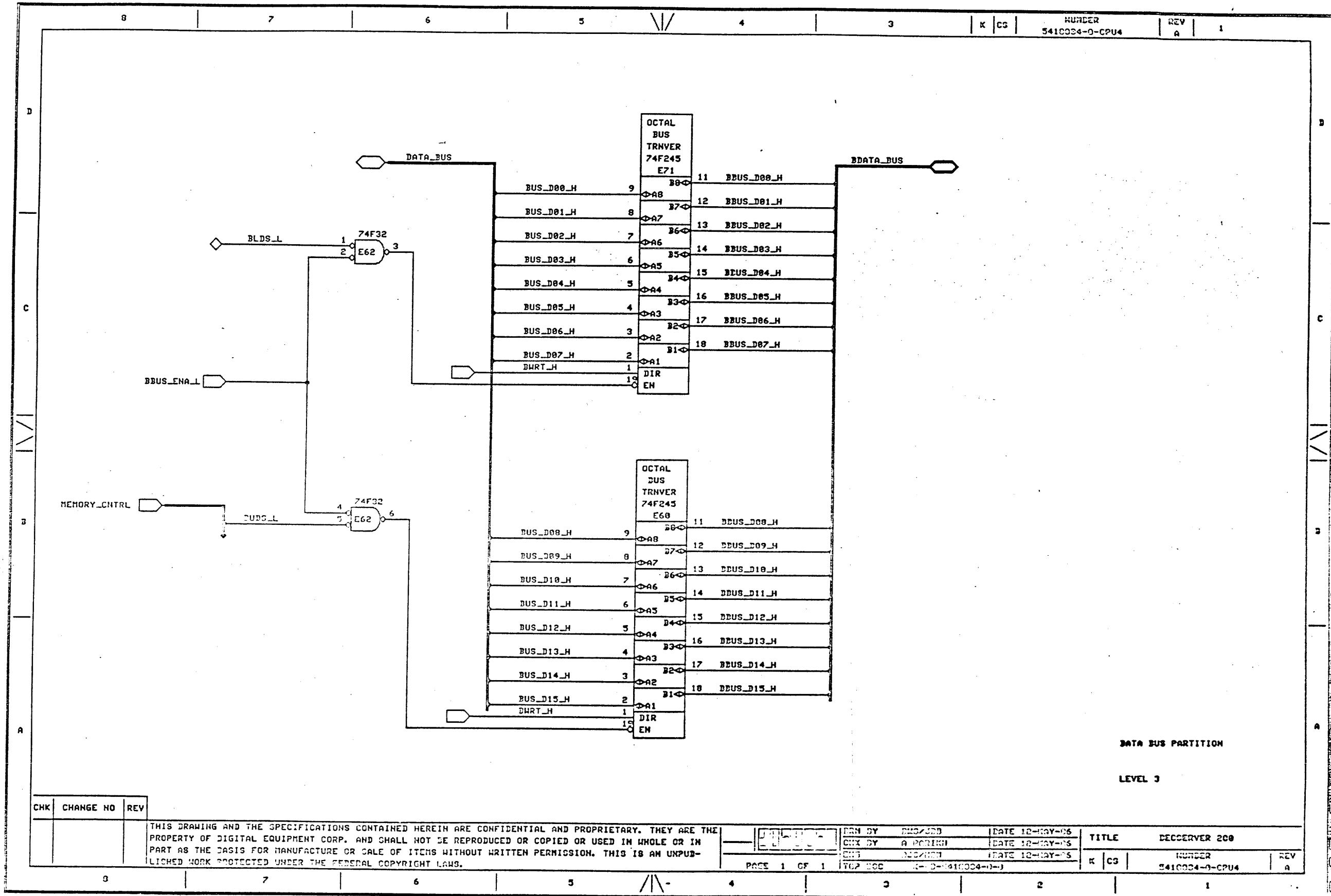


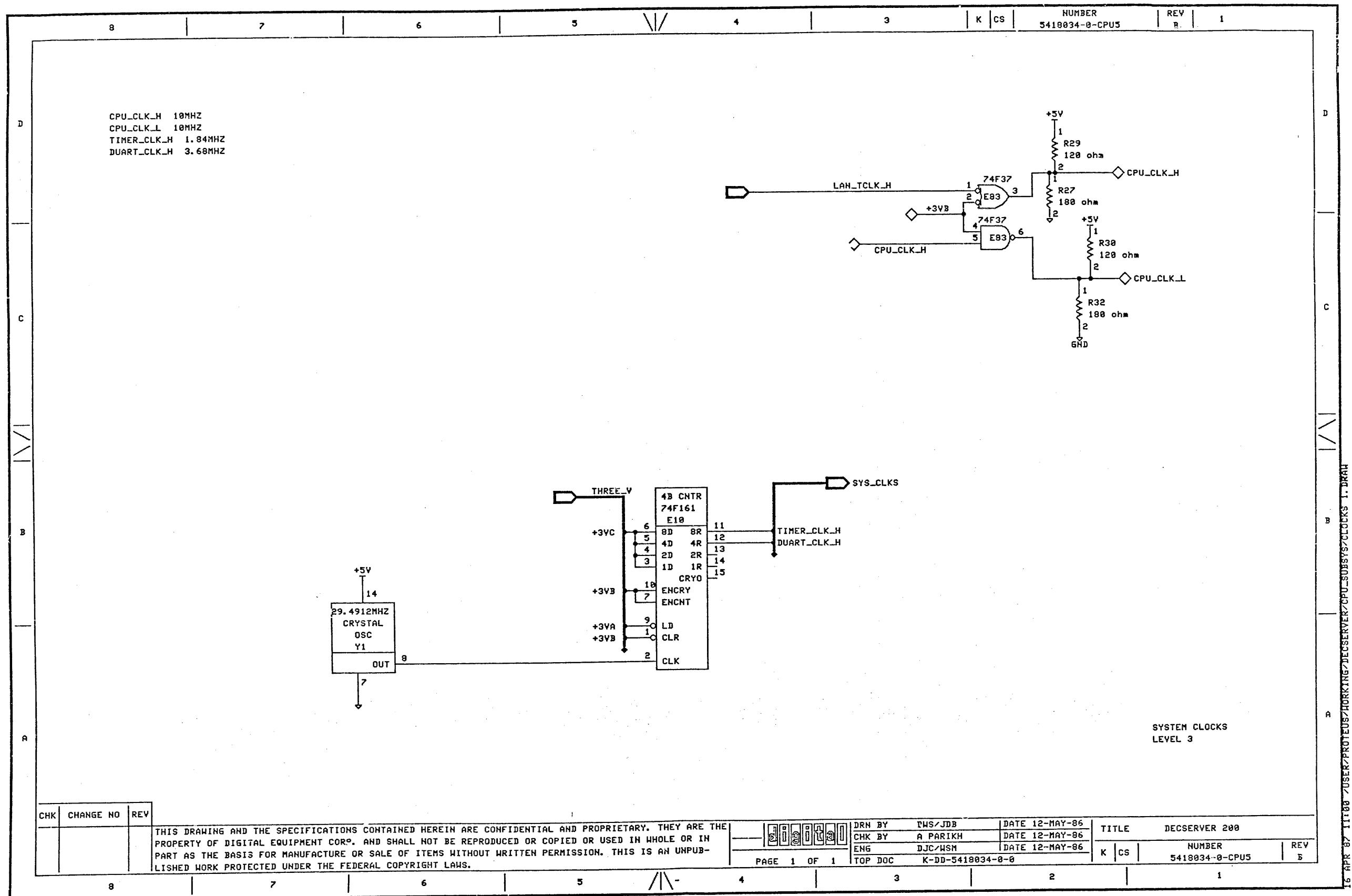
CHK | CHANGE NO | RE

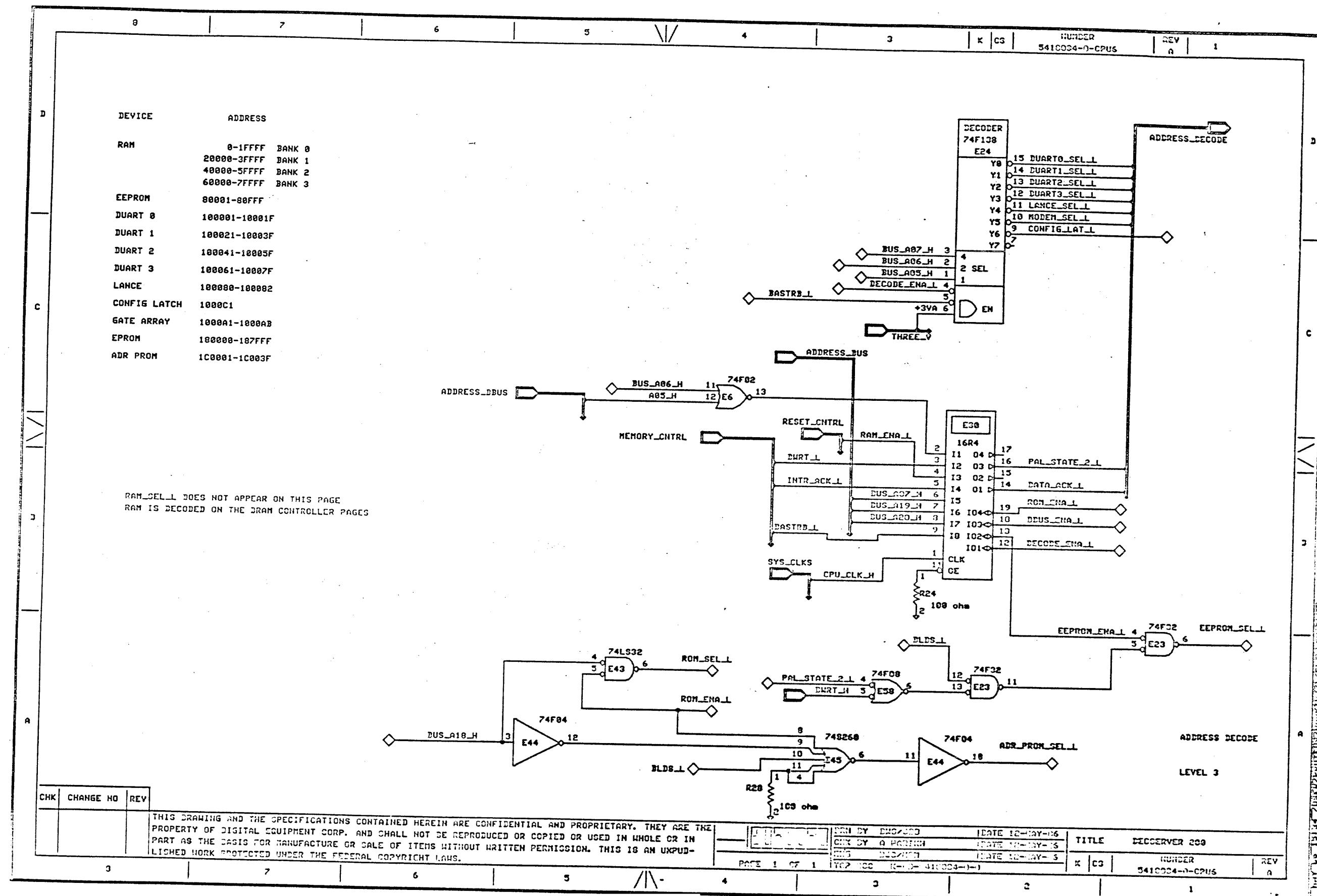
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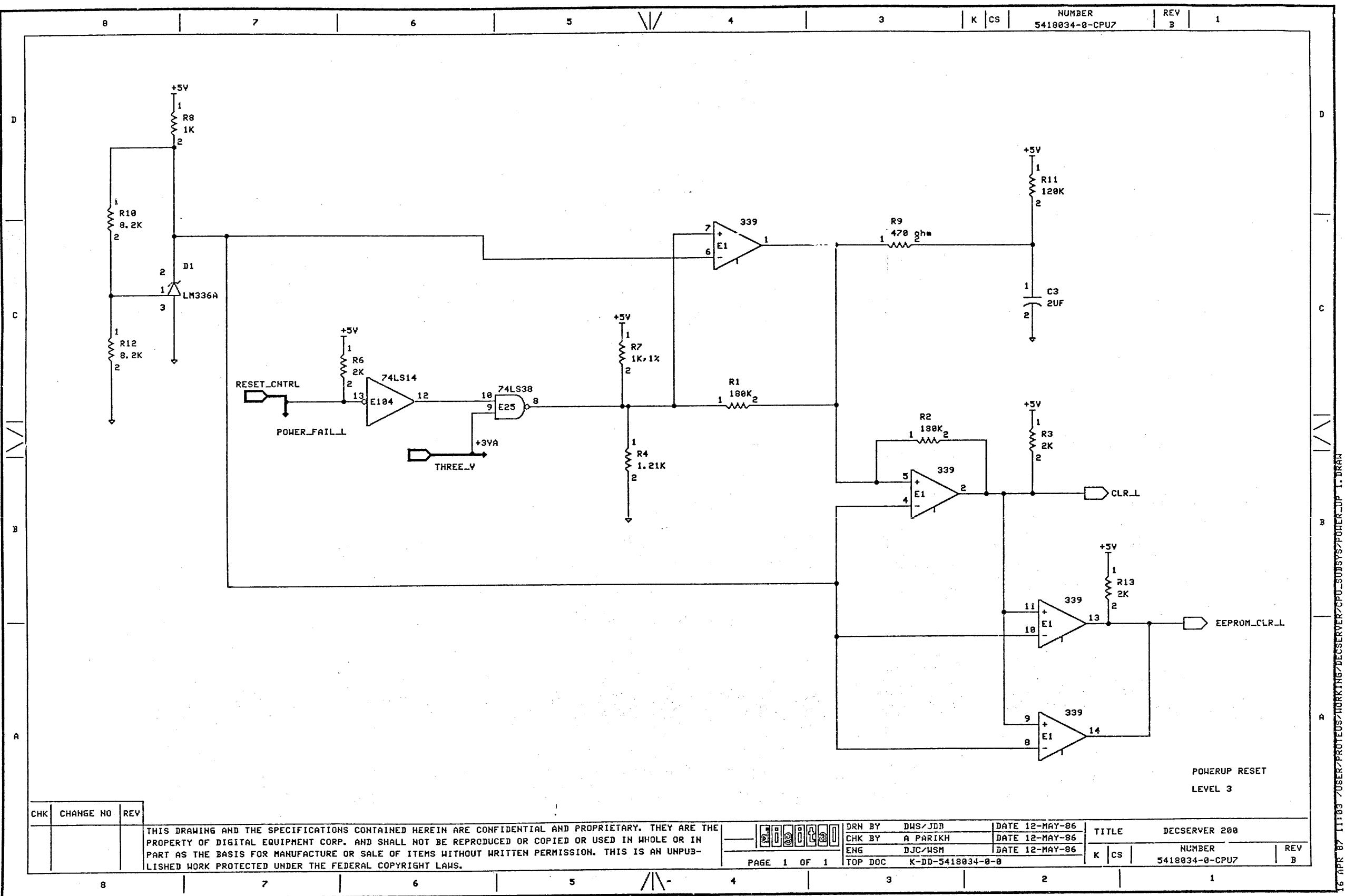
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CRX BY	A BRKRN	DATE	12-MAY-06	NUMBER	
BRG	1000000000	DATE	12-MAY-06	K	CJ
PAGE	1	CT	1	102-000-K-0-1410024-0-0	

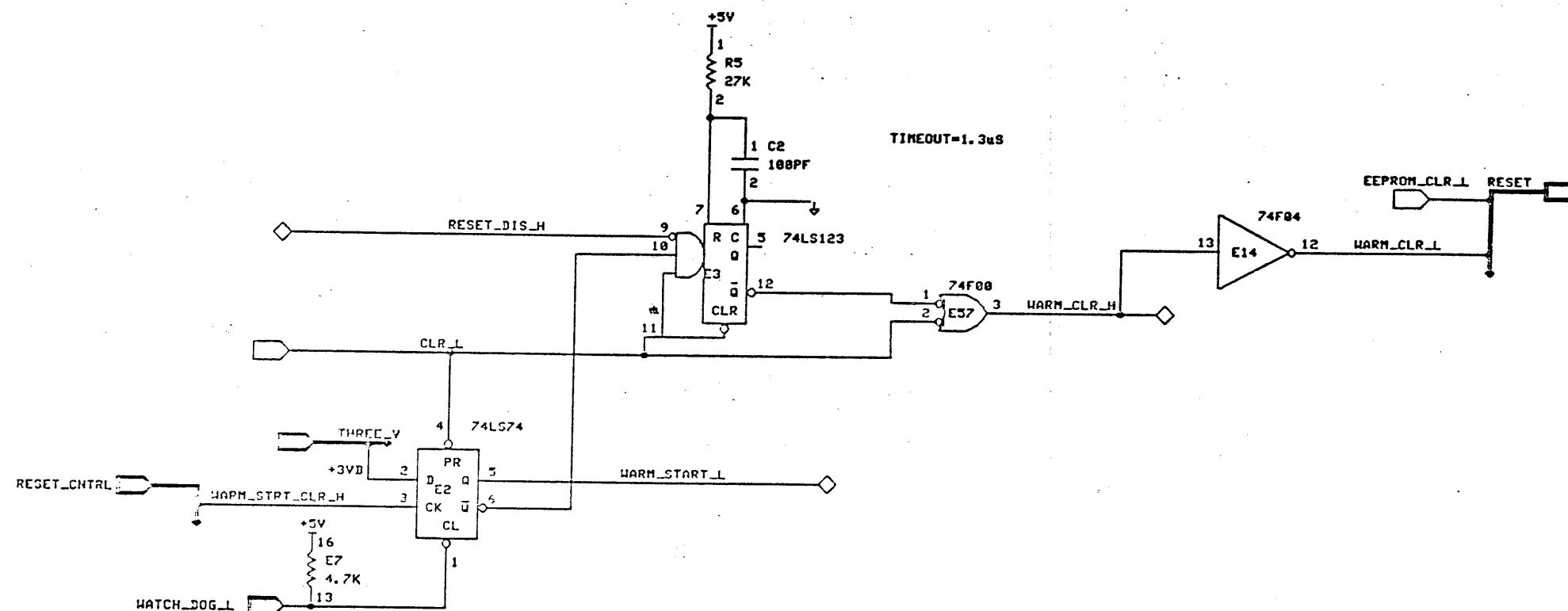








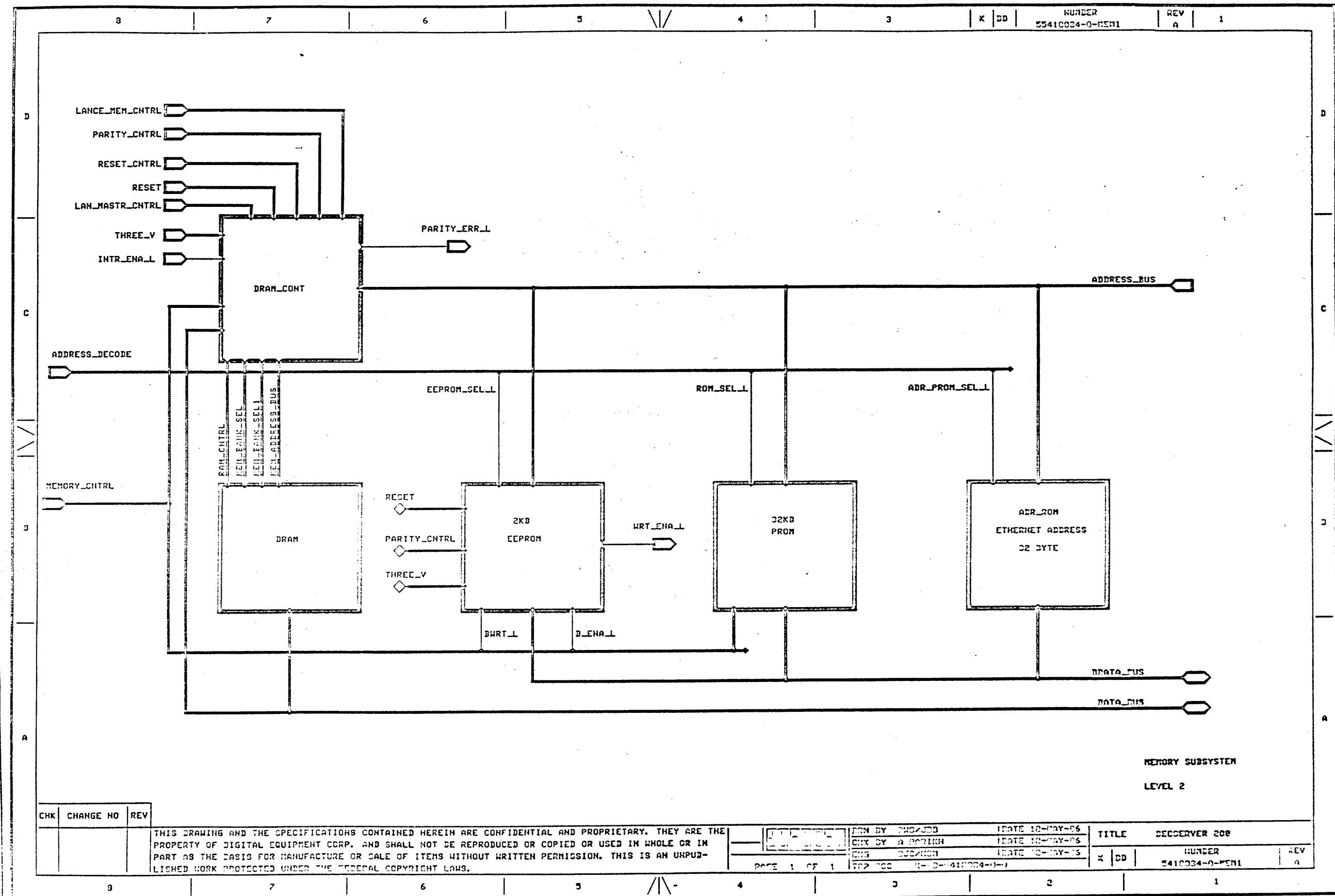




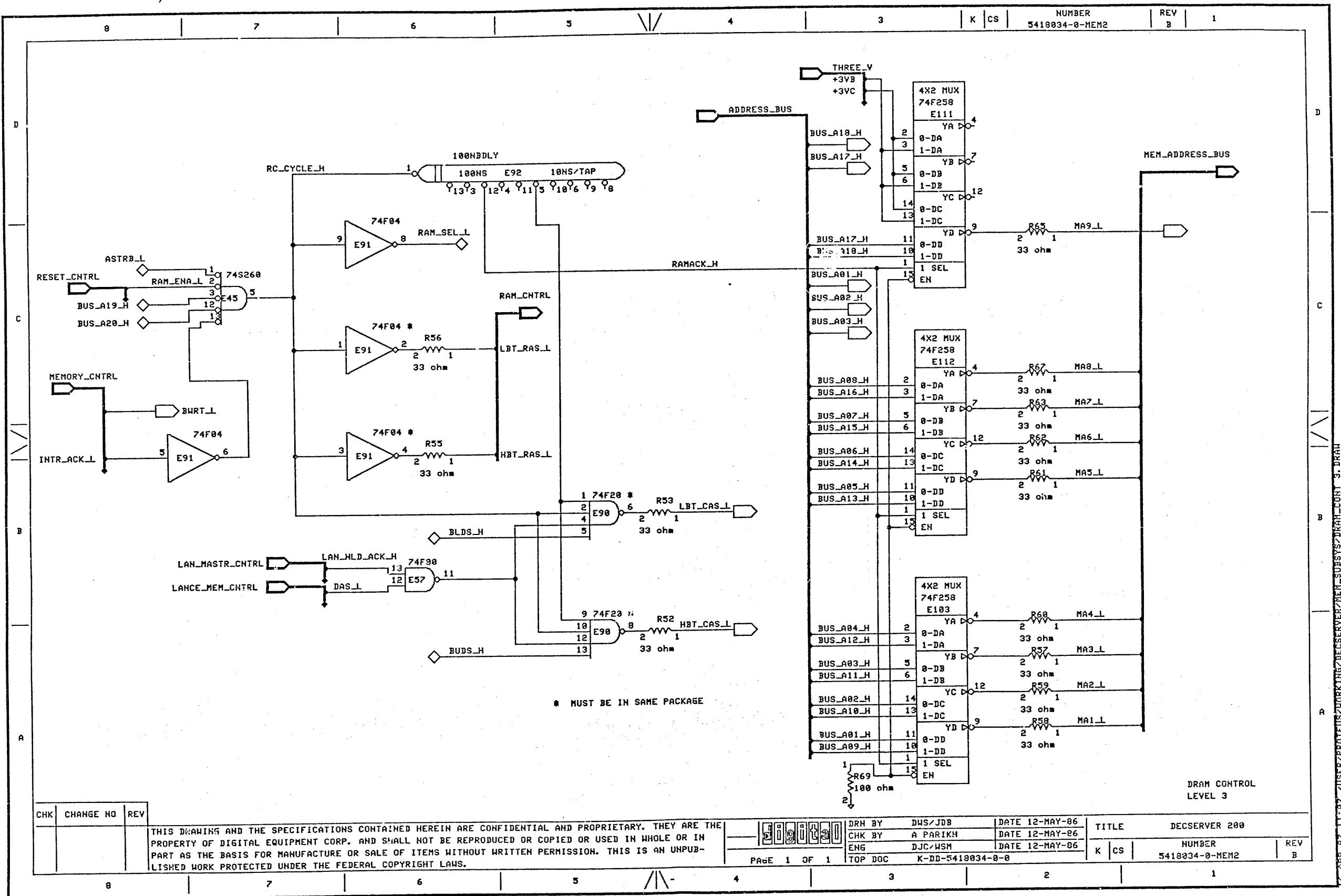
WARM START RESET
LEVEL 3

CHK | CHANGE NO | REV

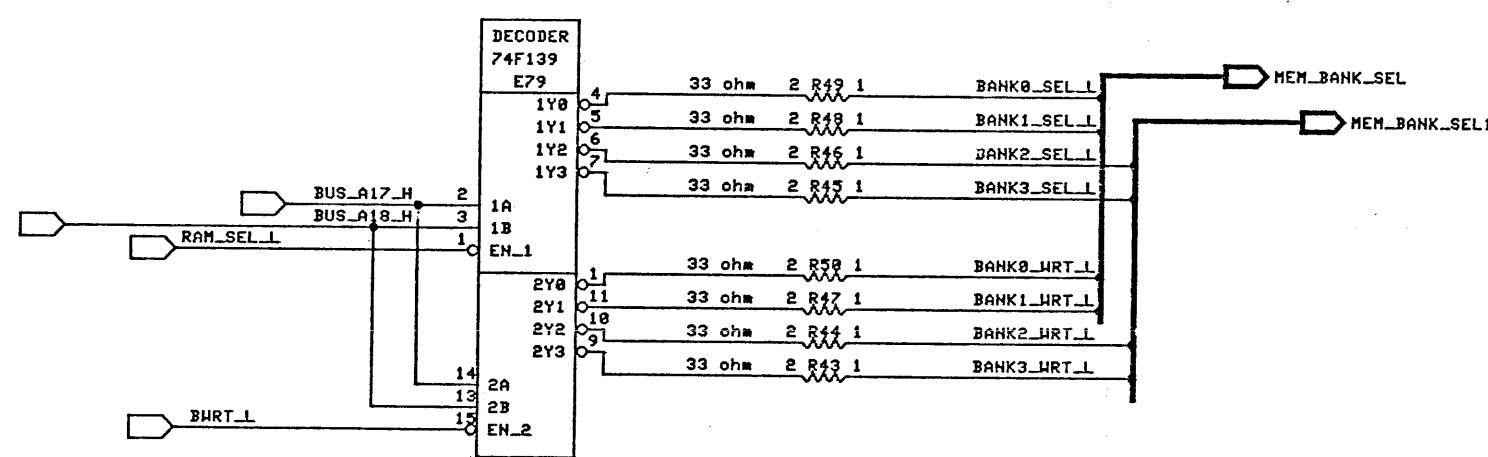
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8 | 7 | 6 | 5 | // | 4 | 3 | K | CS | NUMBER
5418034-8-MEM3 | REV
A | 1



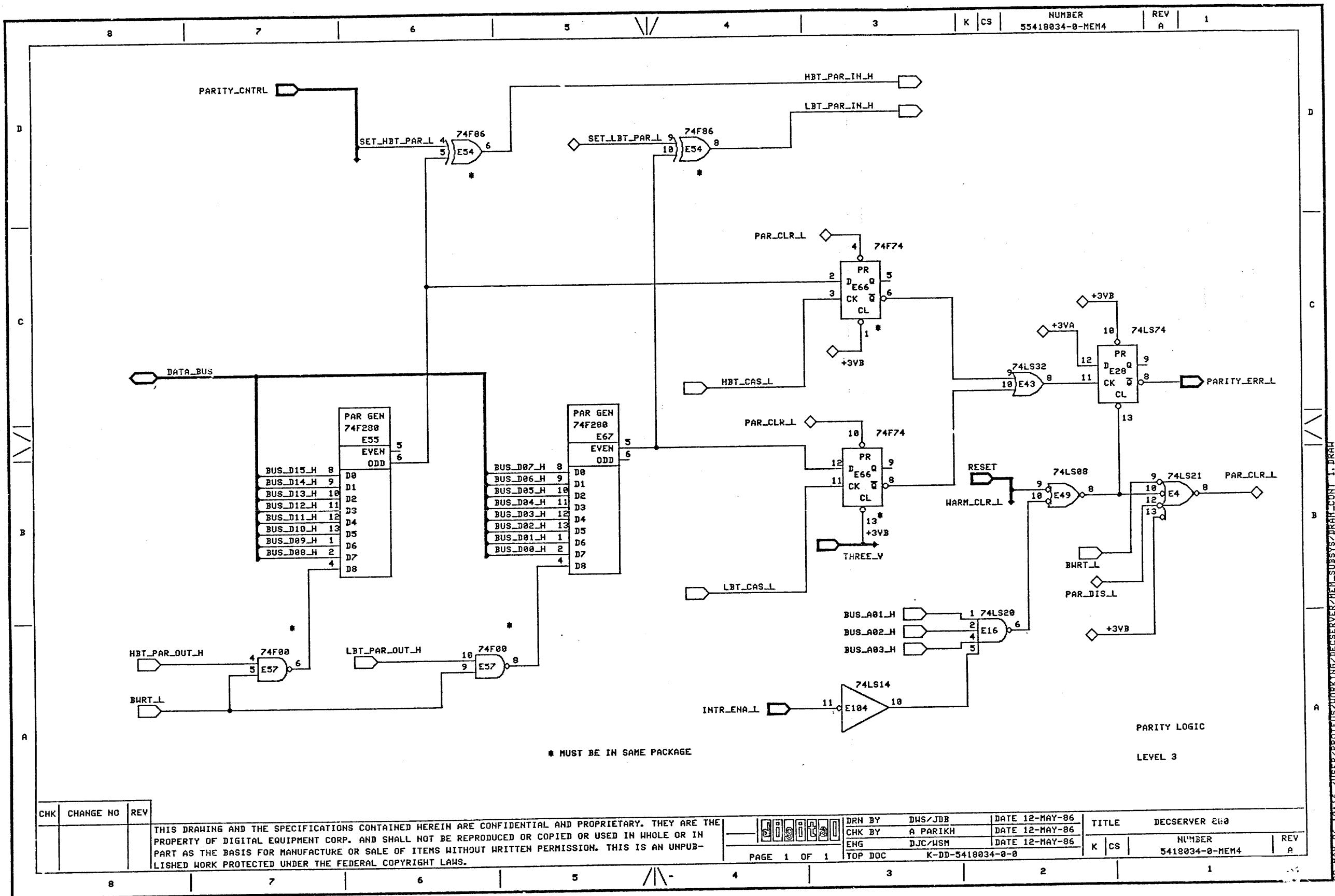
MEMORY BANK DECODER

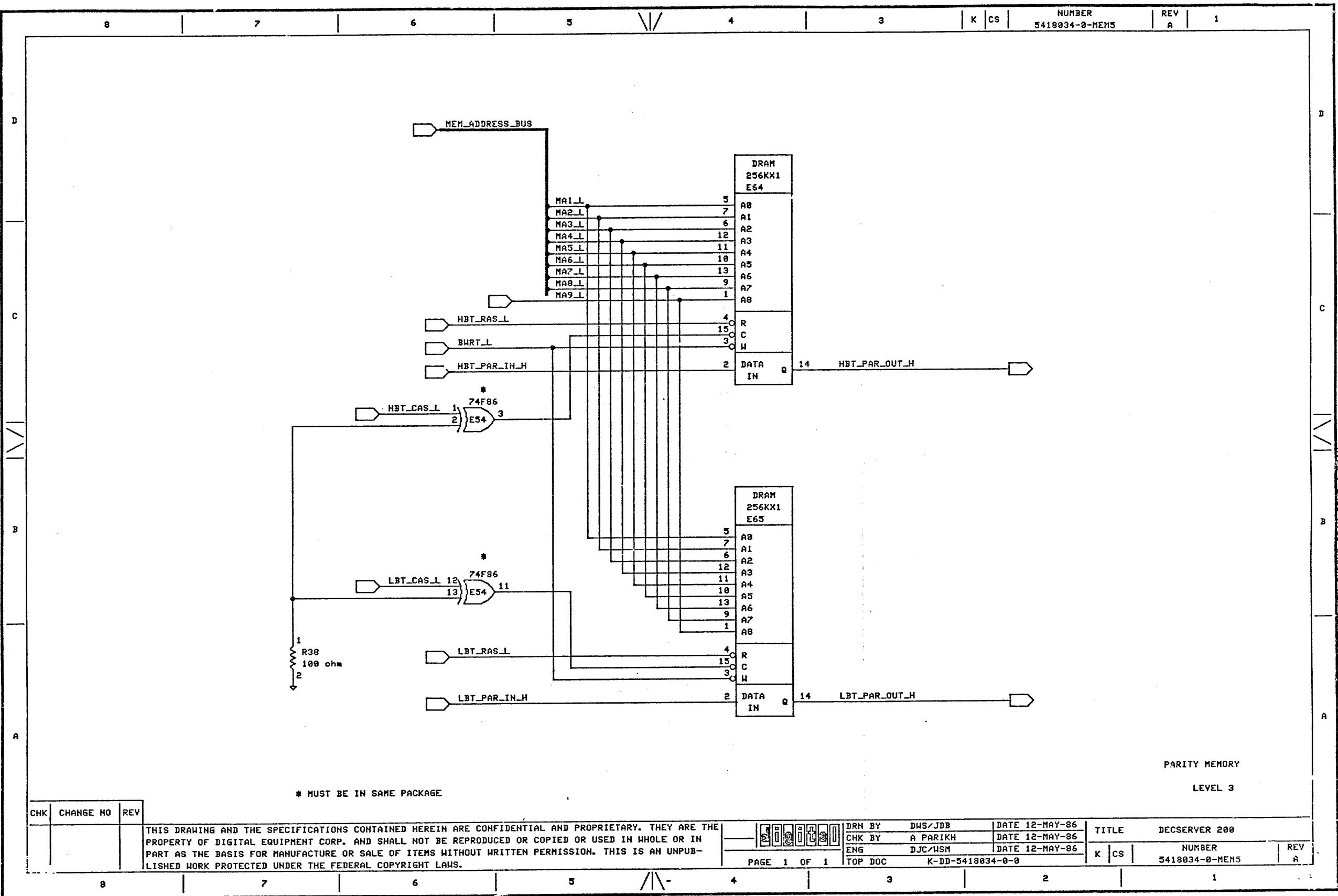
LEVEL 3

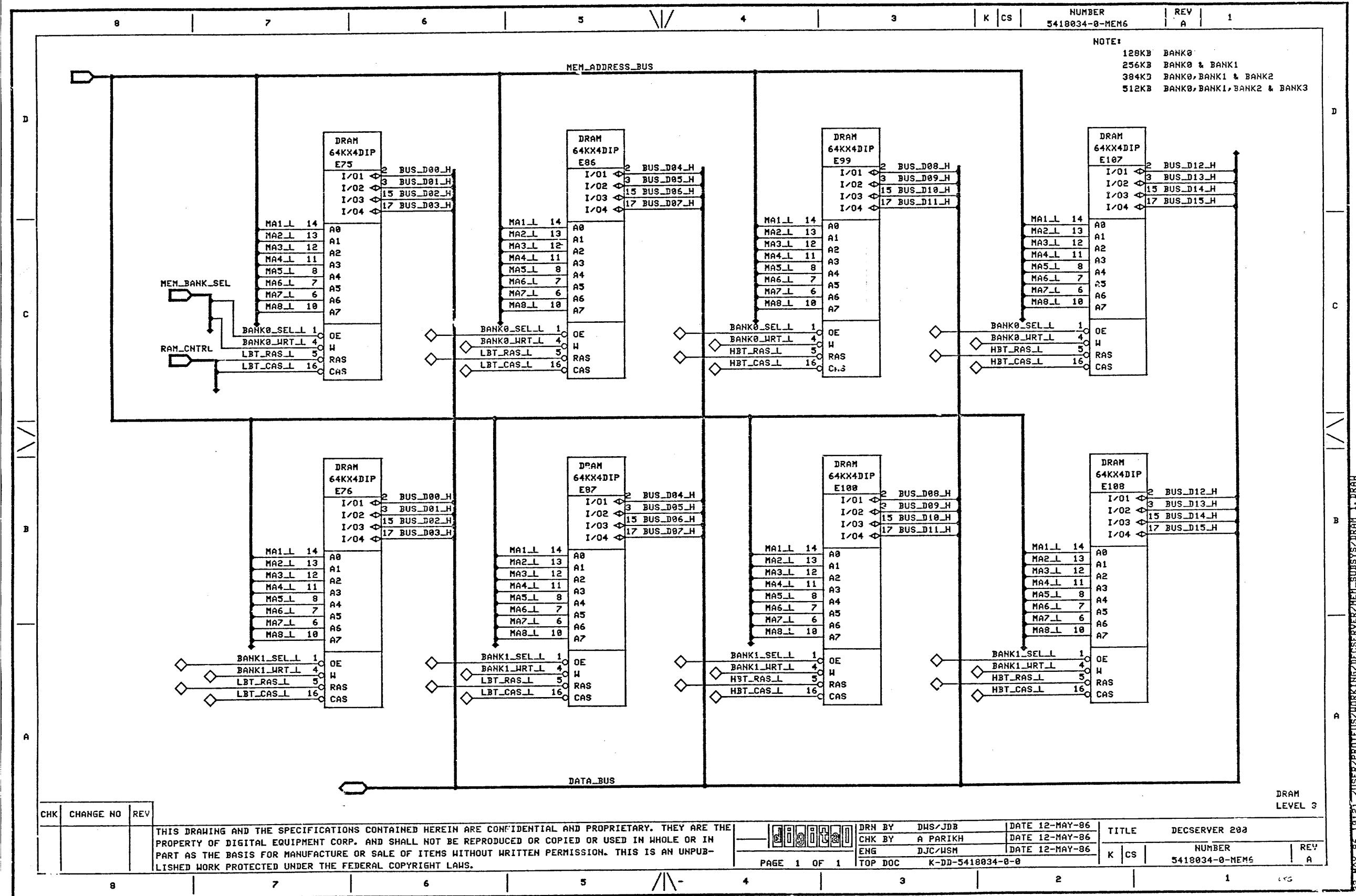
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E		DRN BY	DHS/JDB	DATE 12-MAY-86	TITLE	DECSERVER 200	
		CHK BY	A PARIKH	DATE 12-MAY-86			
		ENG	DJC/HSM	DATE 12-MAY-86			
PAGE 1 OF 1	TOP DOC	K-DD-5418034-0-0			K CS	NUMBER 5418034-0-MEM3	REV A

BUNDING



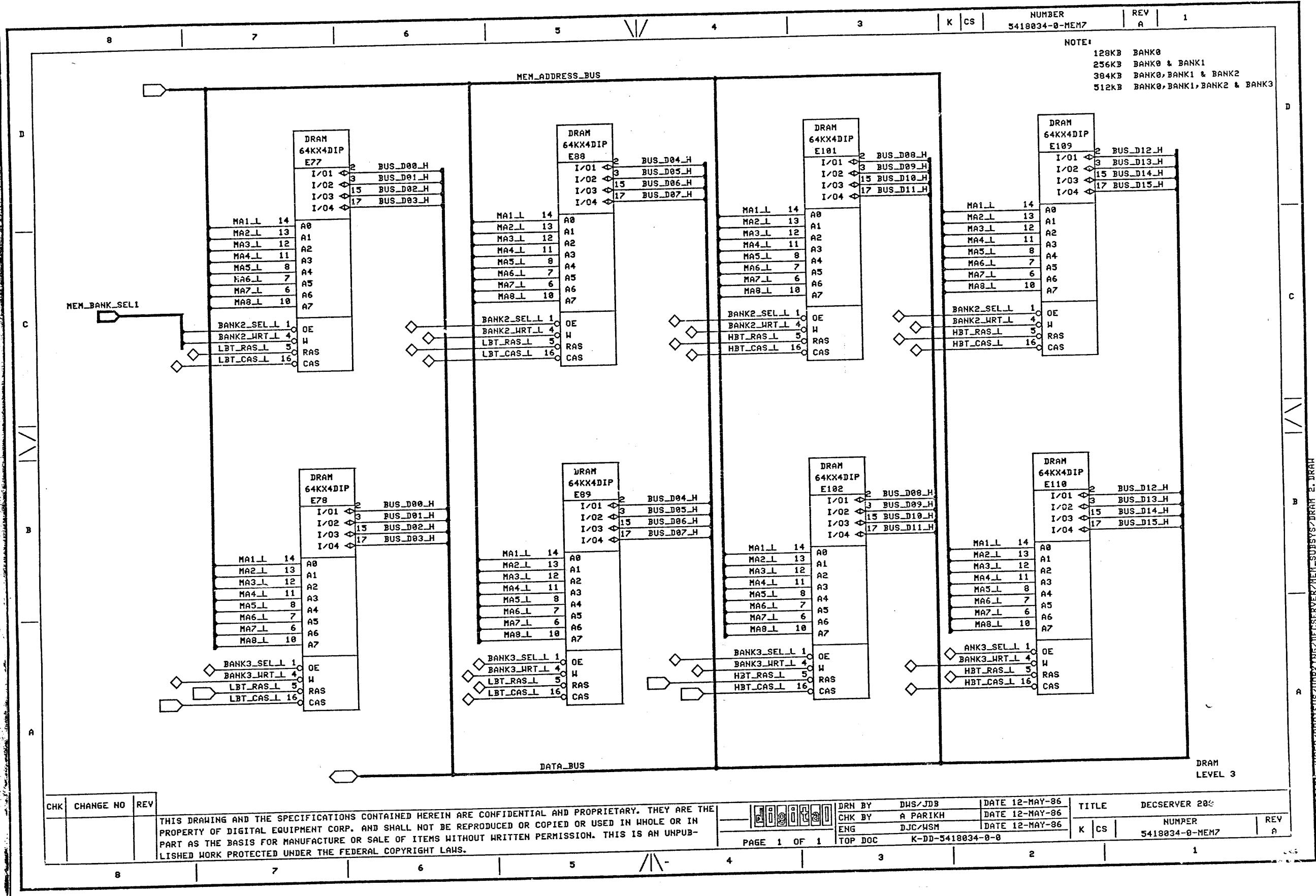


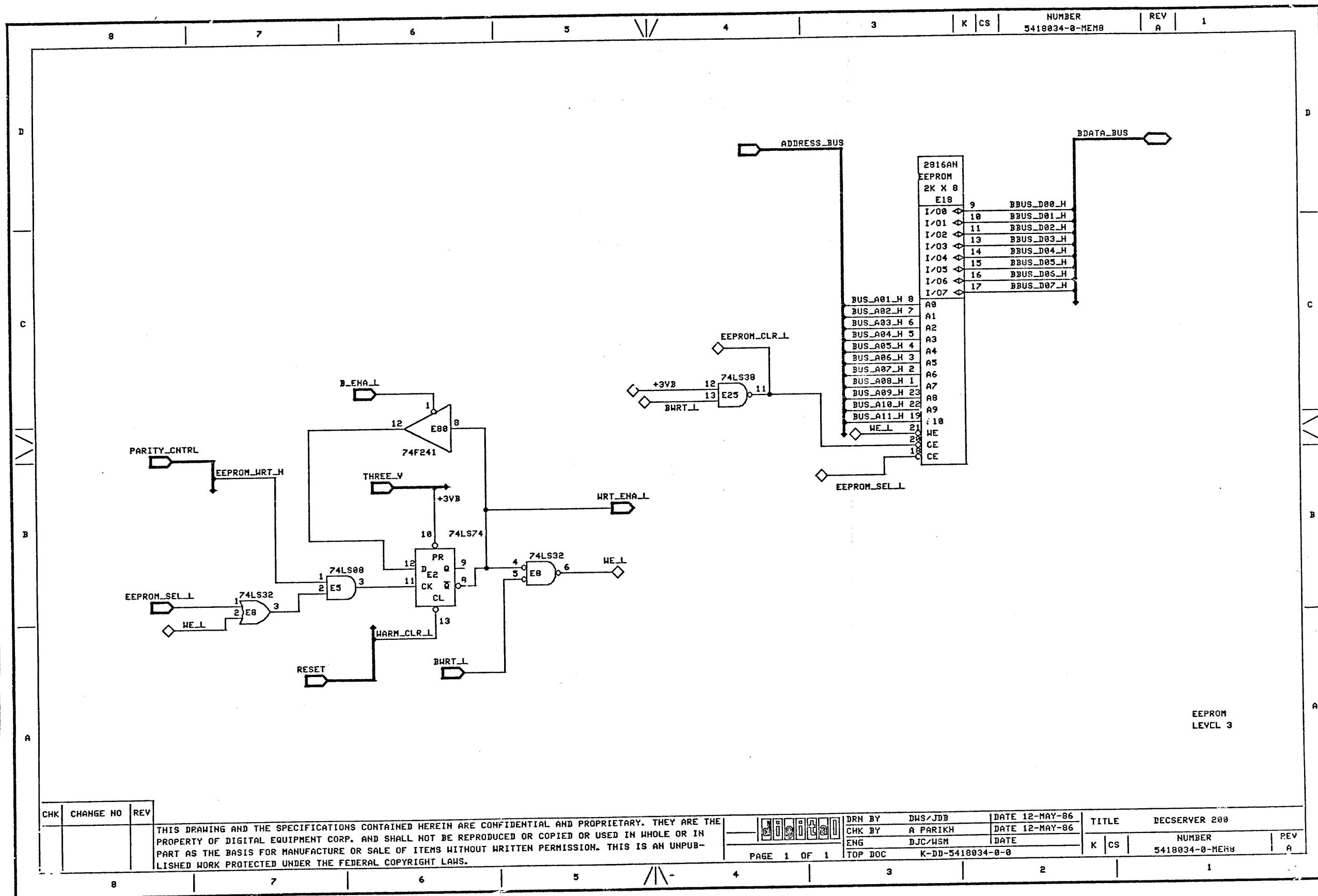


K | CS | NUMBER 5418034-0-MEM7 | REV A | 1

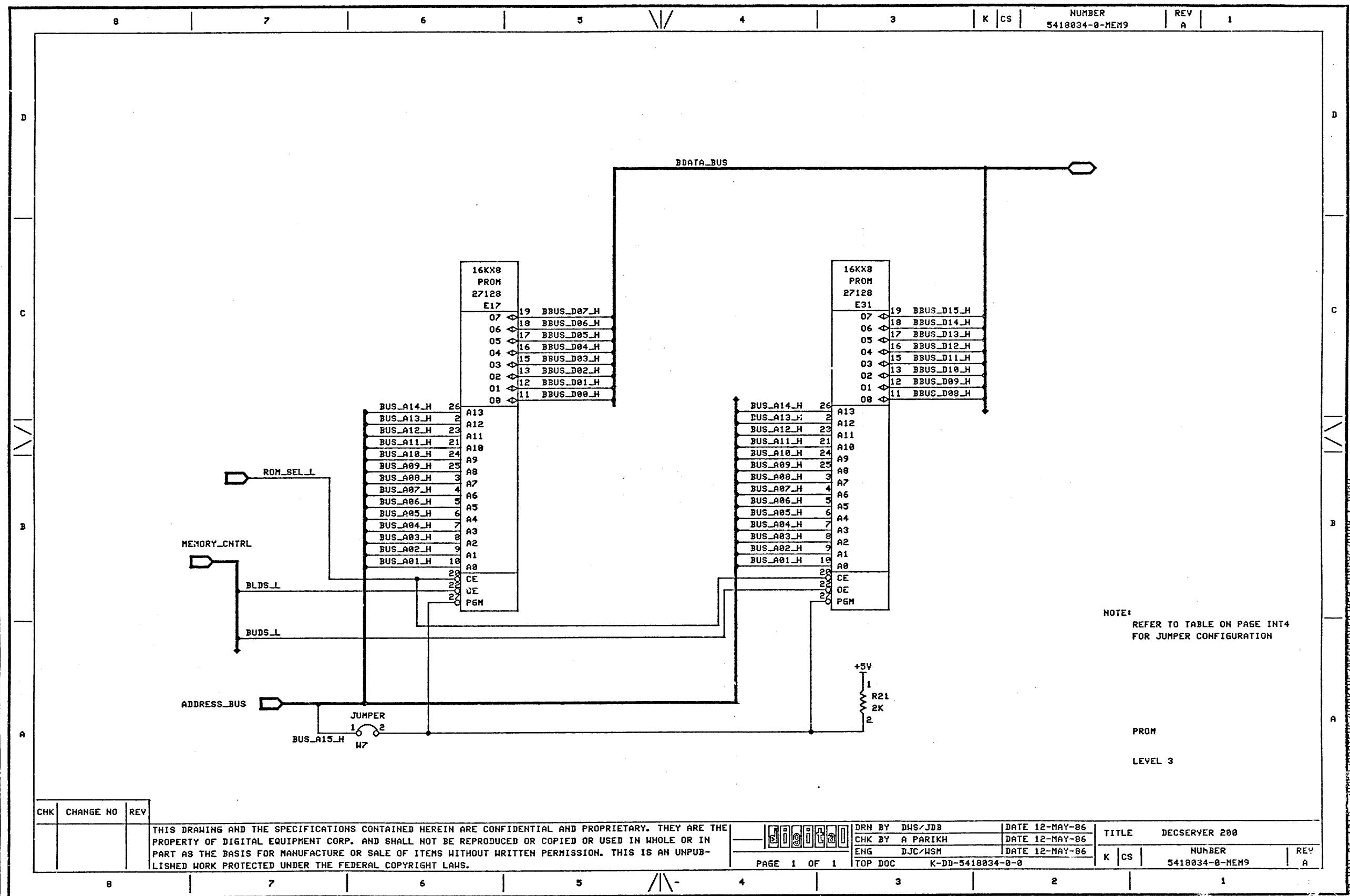
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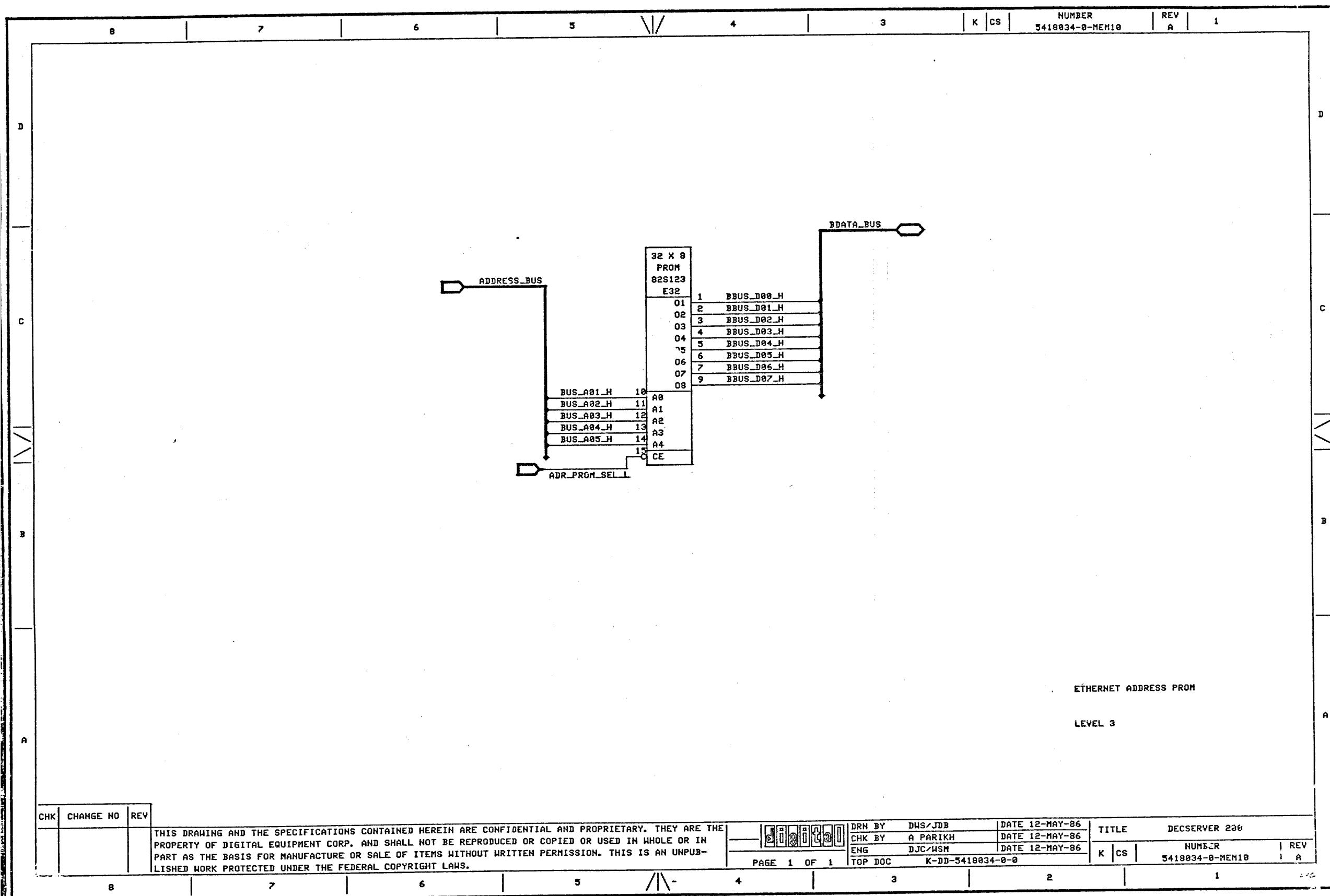
128KB BANK0
256KB BANK0 & BANK1
384KB BANK0, BANK1 & BANK2
512KB BANK0, BANK1, BANK2 & BANK3





WORKING/DECSERVER/MEN-SUBSYS/EEPROM 1. DRAM





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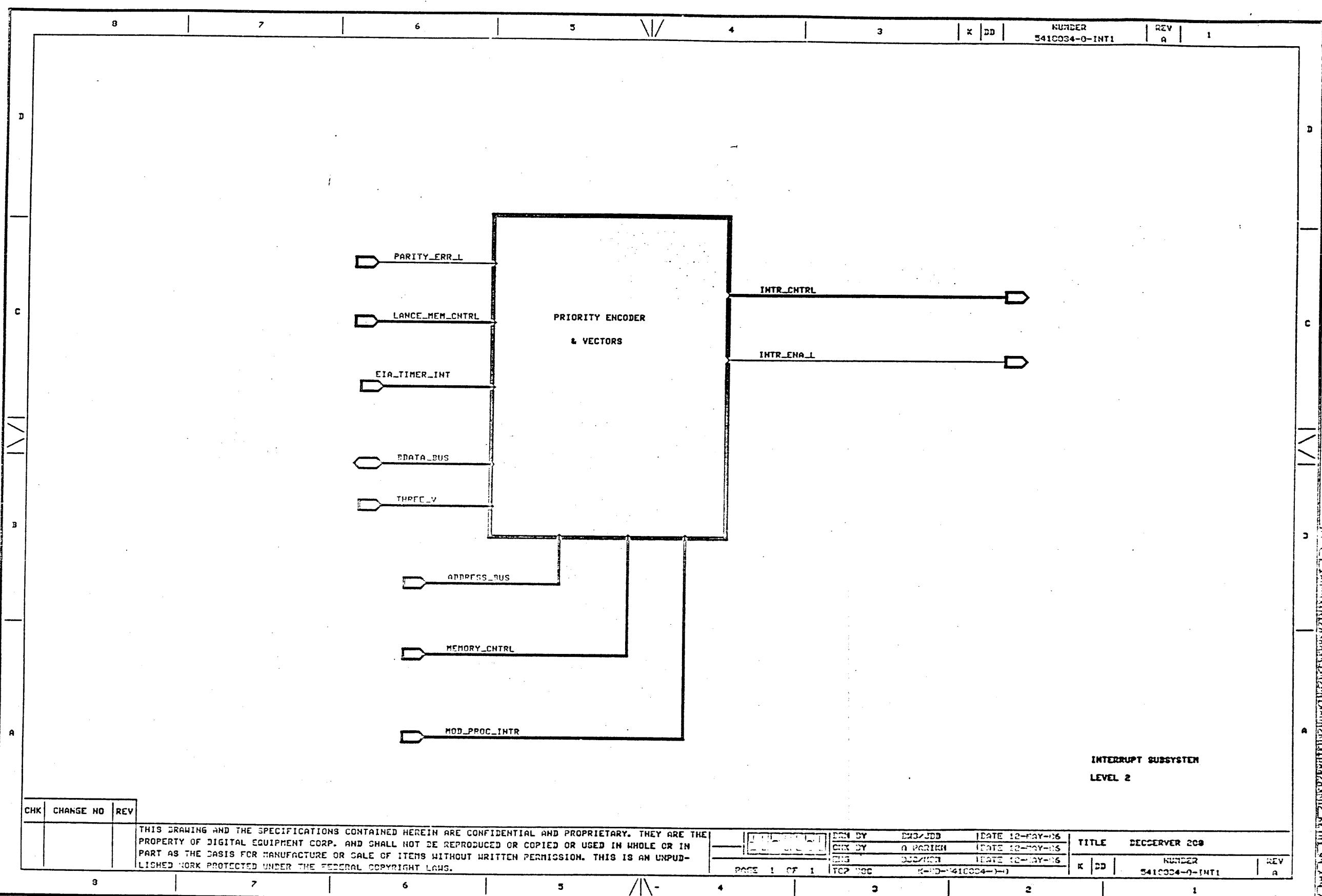
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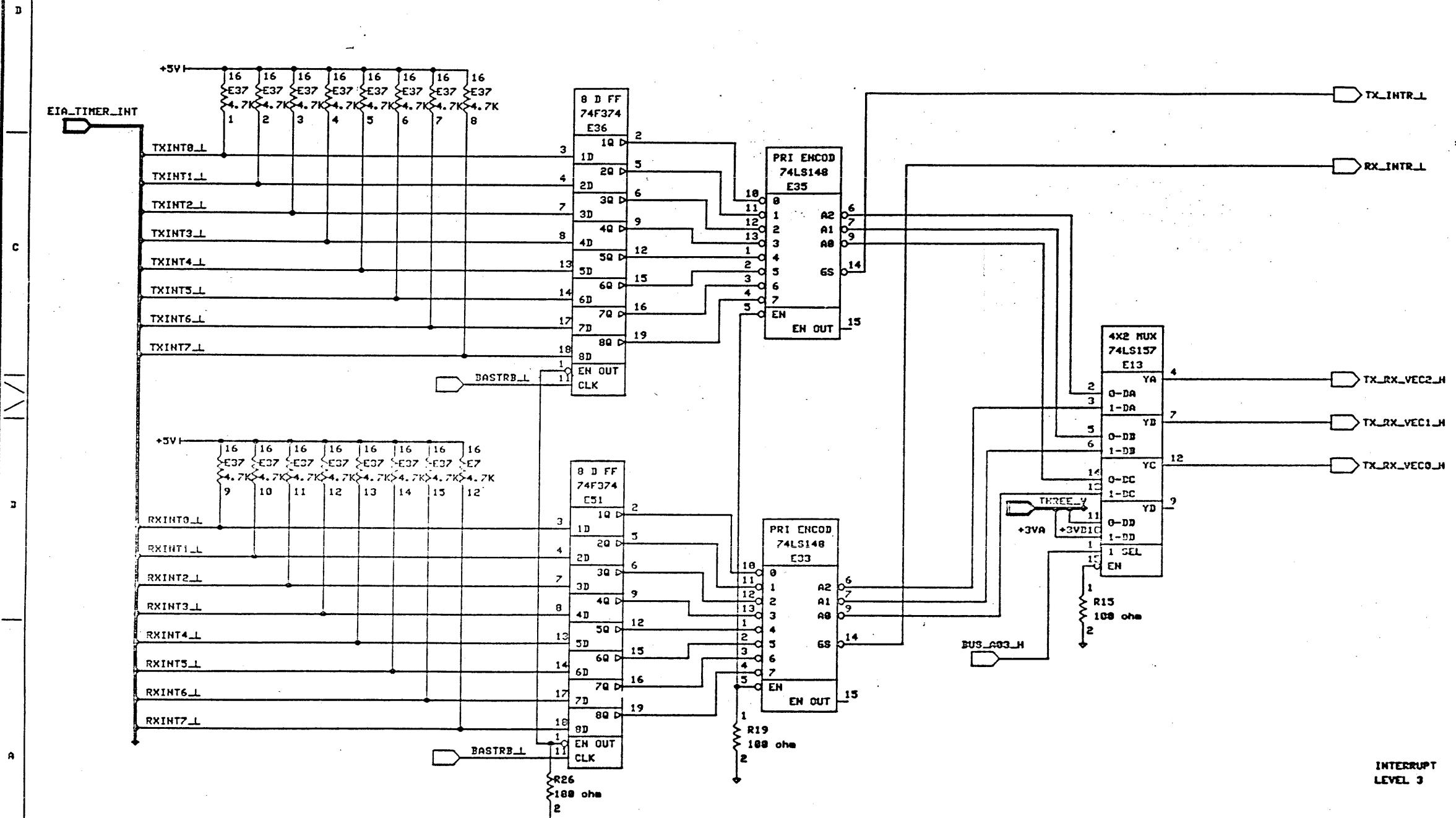
NUMBER
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REV

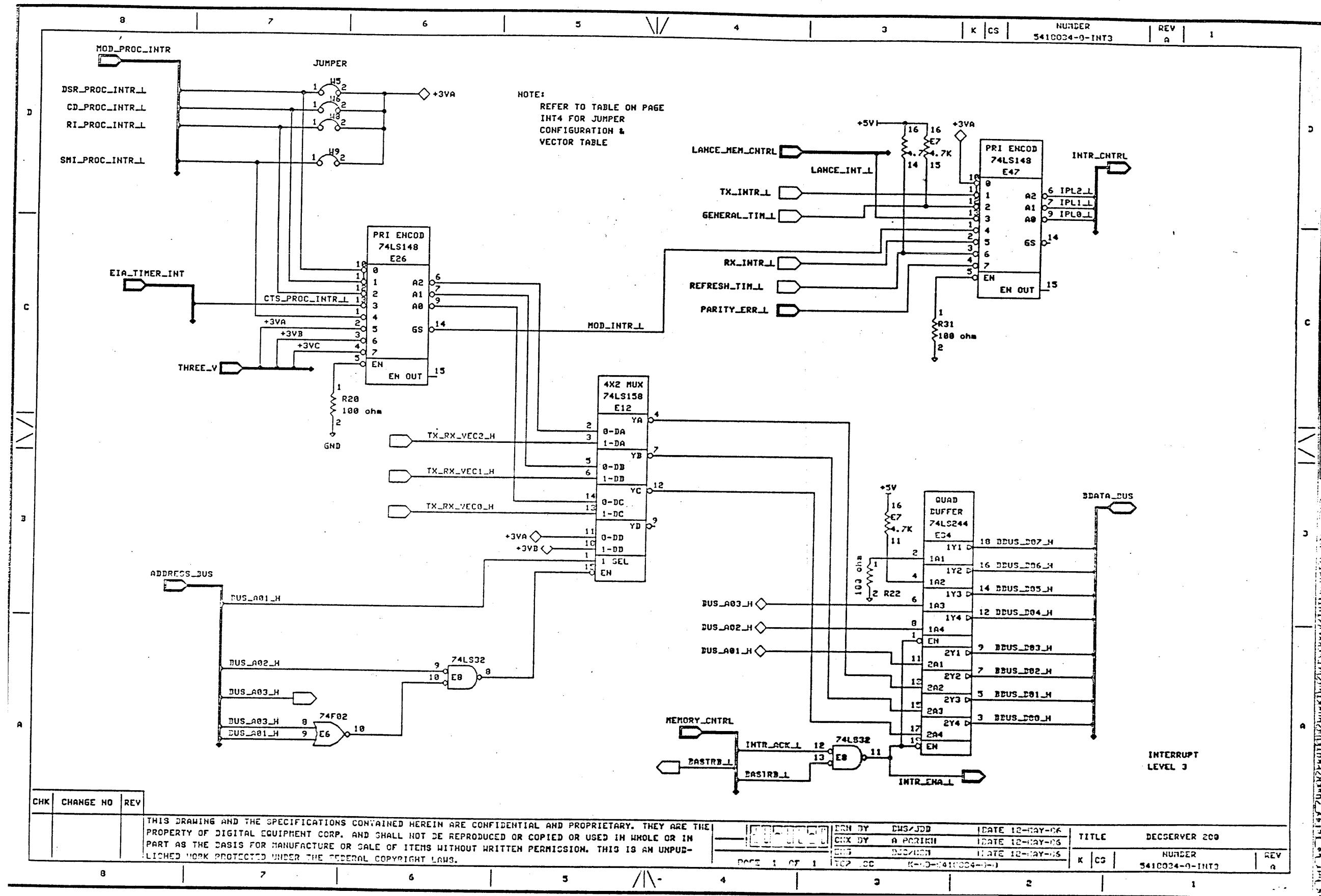
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INTERRUPT
LEVEL 3



COMPONENT CONFIGURATIONS

COMPONENT IN	COMPONENT OUT	FUNCTION	PRODUCT
	W1, W2, E77, E78, E88, E89, E101, E102, E109, E110	256KB DRAM	ANY
H2	E78, E89, E102, E110, W1	384KB DRAM	ANY
W1	H2	512KB DRAM	ANY
H4		ROUTER	DSRVC-AA DSRVC-AB
	H4	SERVER	DSRVB-AA DSRVB-AB DSRVB-BA DSRVB-BB
E52, E53, E72, E73, E74, E84, E85, E97, E98 J7, D2-D5, C146, C147, R34, R39, R42 R80, R81	W3, W5, W6, W8-W17, W22	RS232 INTERFACE	DSRVB-AA DSRVB-AB
W3, W5, W6, W8-W17, W22	E52, E53, E72, E73, E74, E84, E85, E97, E98 J7, D2-D5, C146, C147, R34, R39, R42, R80, R81	DEC 423 INTERFACE	DSRVB-BA DSRVB-BB
H7	R21	32Kx8 EPROM	ANY
R21	H7	16Kx8 EPROM	ANY
W18-W21, W23	E96, R82	NO LANCE HARDWARE WORKAROUNDS	ANY
R82, E96	W18-W21, W23	LANCE HARDWARE WORKAROUNDS	ANY
H24		FULL STEP	ANY
	H24	HALF STEP	ANY

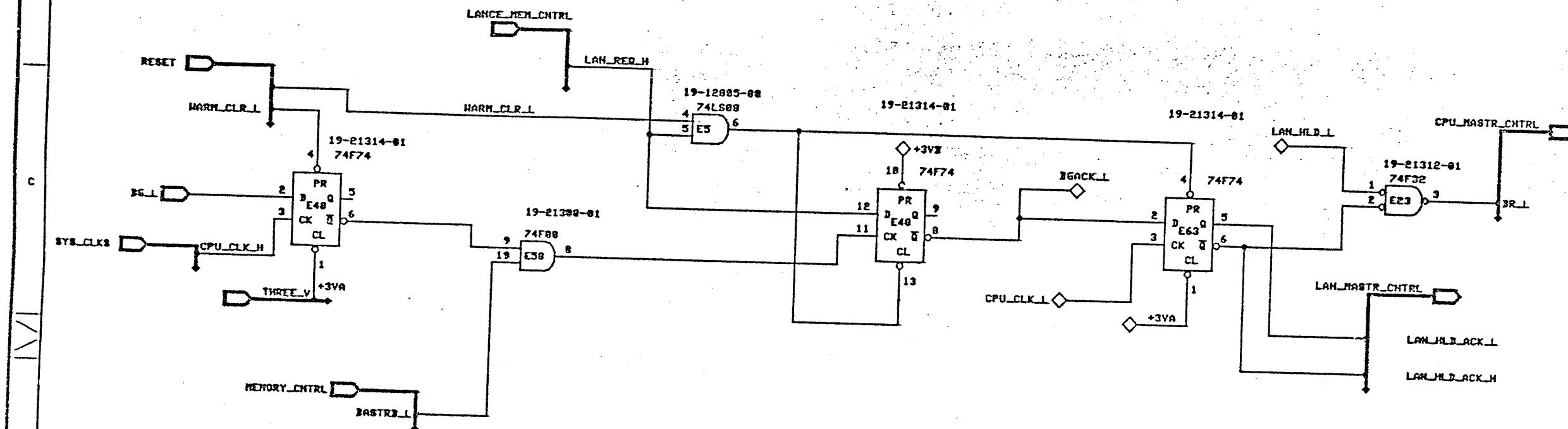
INTERRUPT VECTORS

DEVICE	INTERRUPT VECTOR	68000 VECTOR
TXINT0	48	128
TXINT1	49	124
TXINT2	4A	128
TXINT3	4B	12C
TXINT4	4C	130
TXINT5	4D	134
TXINT6	4E	138
TXINT7	4F	13C
GENERAL_TIM	57	15C
LANCE_INT	5F	17C
DSR_PROC_INT	60	180
CD_PROC_INT	61	184
RI_PROC_INT	62	188
CTS_PROC_INT	63	18C
SMI_PROC_INT	64	190
RXIINT0	68	1A8
RXIINT1	69	1A4
RXIINT2	6A	1A8
RXIINT3	6B	1AC
RXIINT4	6C	1D8
RXIINT5	6D	1D4
RXIINT6	6E	1D8
RXIINT7	6F	1DC
REFRESH_TIM	77	1DC
PARITY_ERR	7F	1FC

CHK	CHANGE NO	REV

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CHK	CHANGE NO	REV

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DRW BY	DWS/JDB	DATE 12-MAY-86	TITLE DECSERVER 208
CHK BY	A PARikh	DATE 12-MAY-86	
ENG	DJC/HSM	DATE 12-MAY-86	
PAGE 1 OF 1	TOP DOC	K-DD-5416934-0-8	K CS NUMBER 5416934-0-AR81 REV A

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V/V

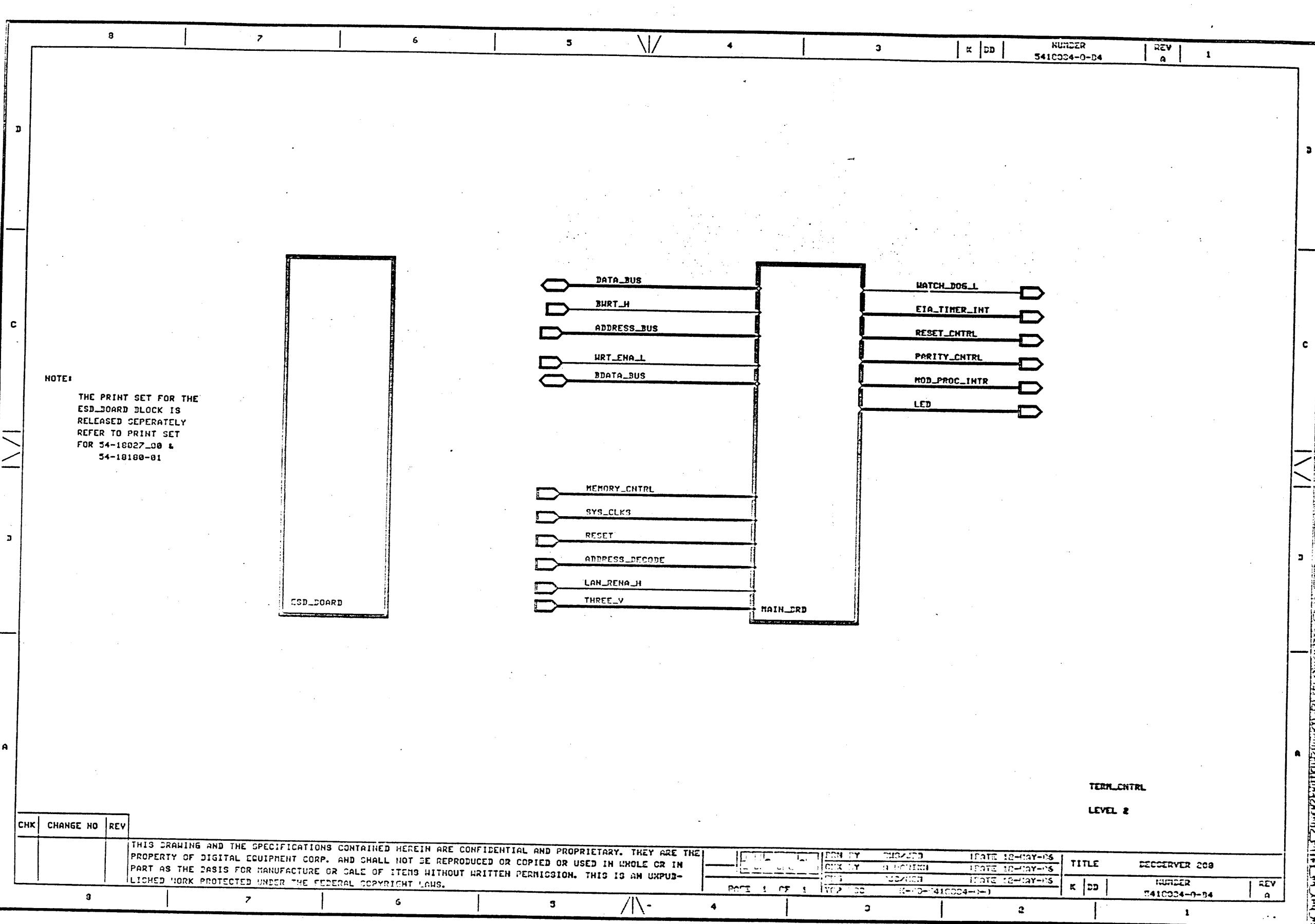
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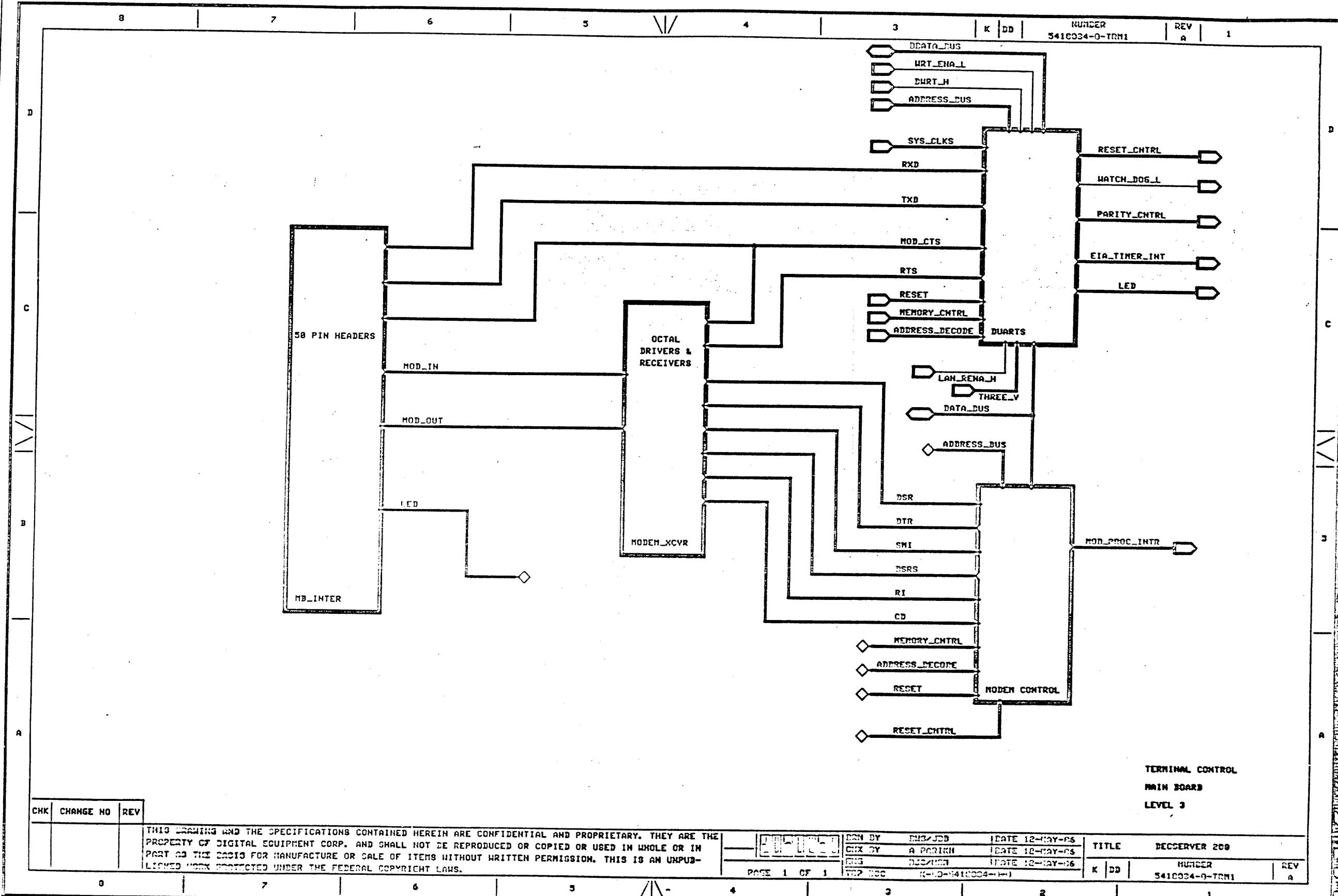
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541C034-0-D4

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CHG | CHANGE NO | RE

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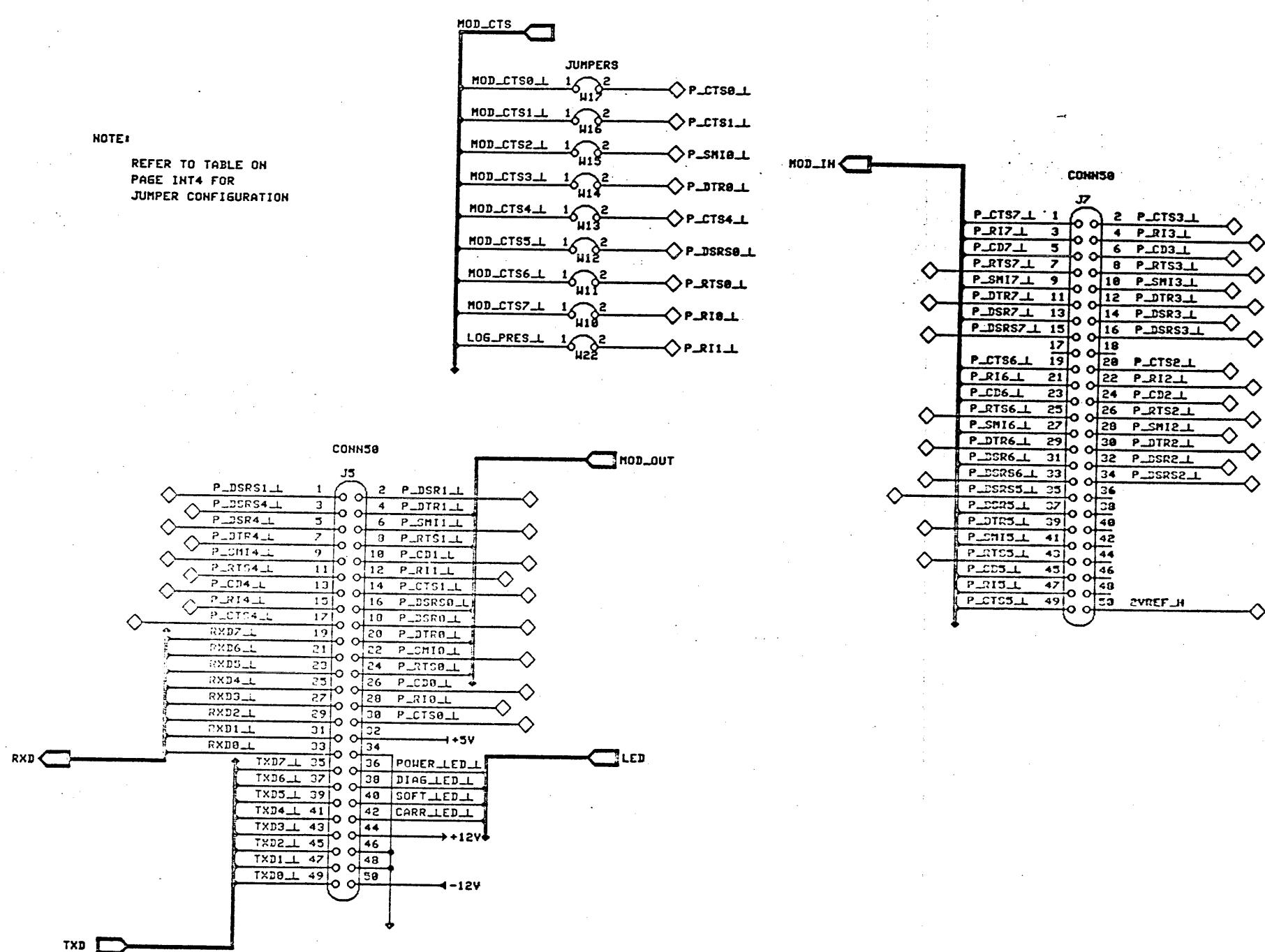
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NOTE:

REFER TO TABLE ON
PAGE INT4 FOR
JUMPER CONFIGURATION



MAIN-TO-ESB-BOARD INTERFACE
LEVEL 4

CHK	CHANGE NO	REV

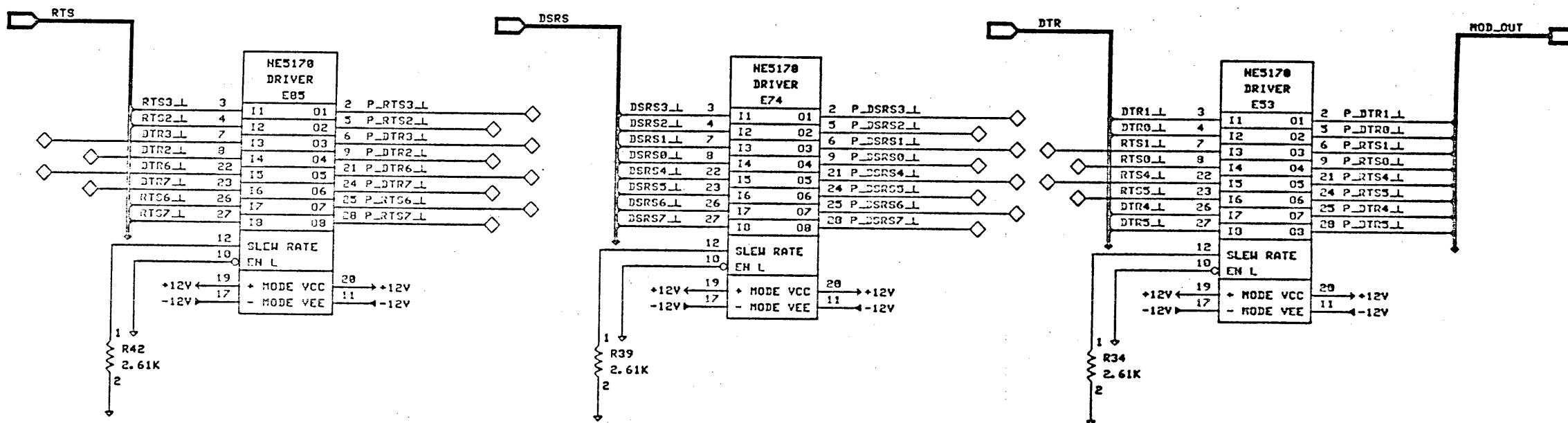
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DESIGNED BY	REVIEWED BY	DATE 12-MAY-85	TITLE	NUMBER	REV
CON. BY	M. PERRIN	DATE 12-MAY-85			
CNS BY	M. PERRIN	DATE 12-MAY-85			

PAGE 1 OF 1 TCP 100 K | C0 | NUMBER 5410034-0-TRM2 | REV A

NOTE 1

REFER TO TABLE ON PAGE INT4
FOR PART CONFIGURATION



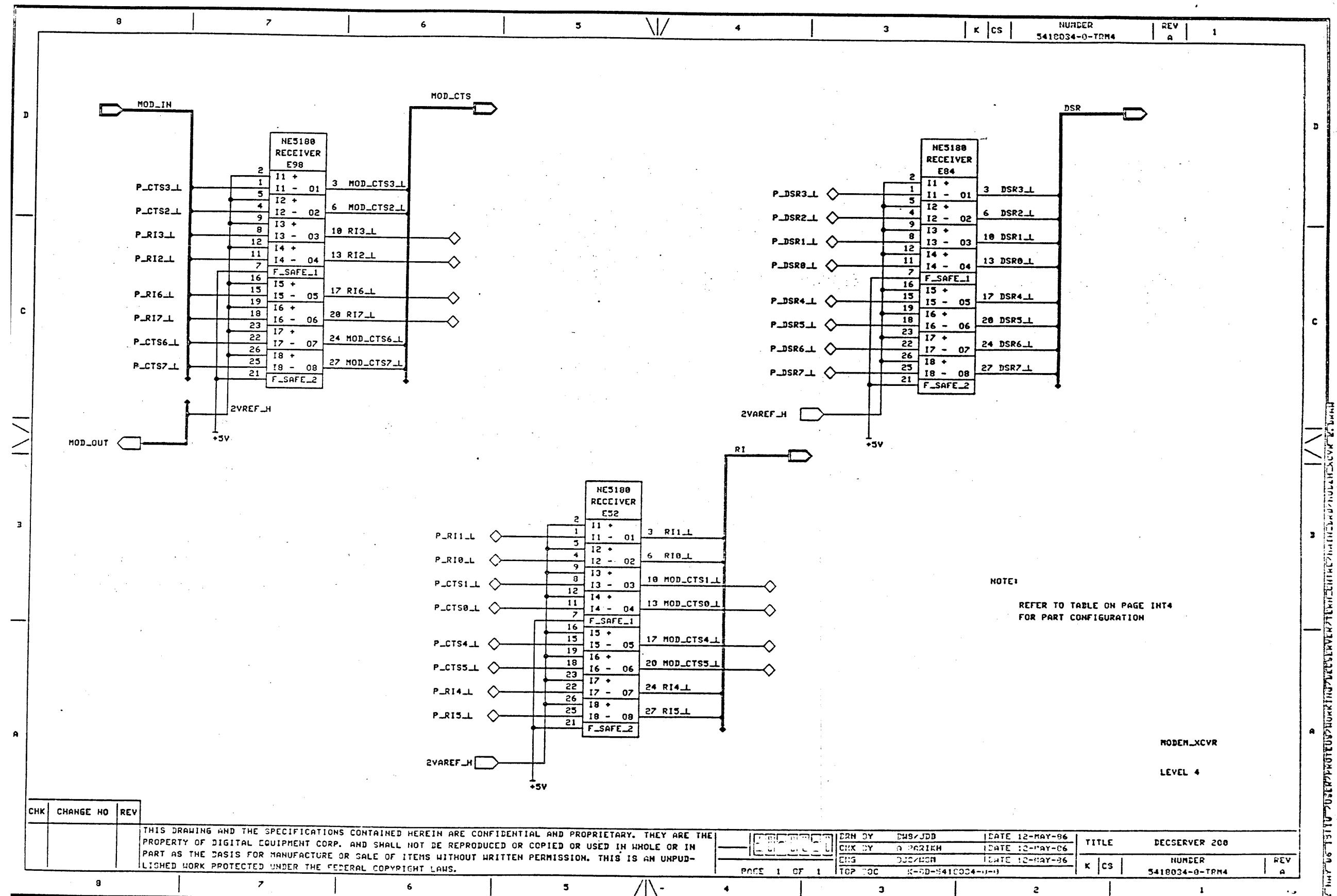
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LEVEL 1

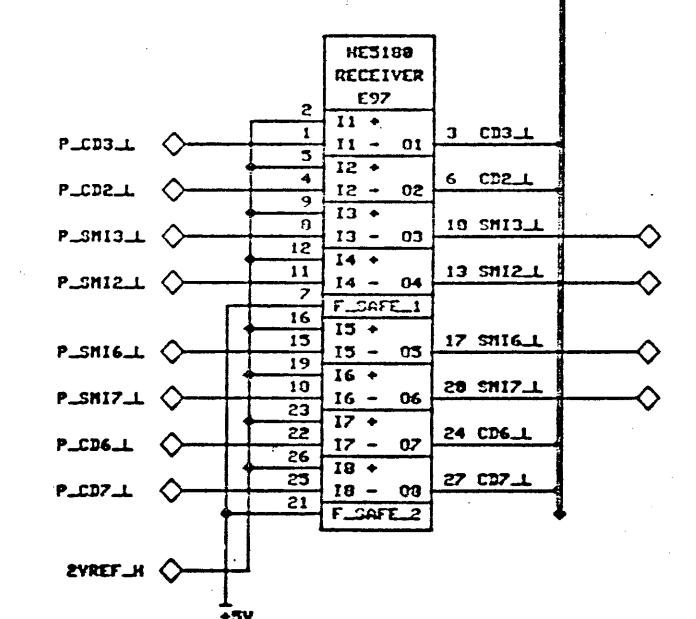
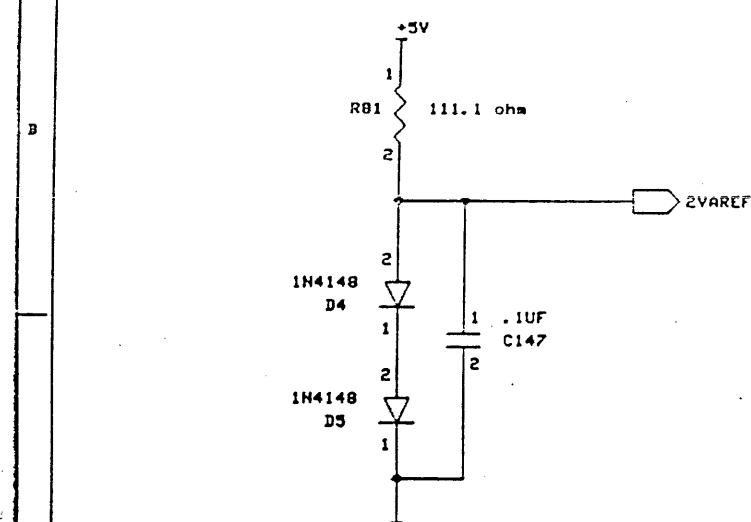
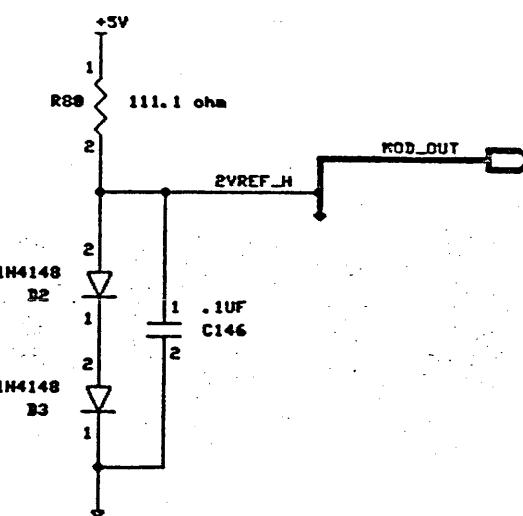
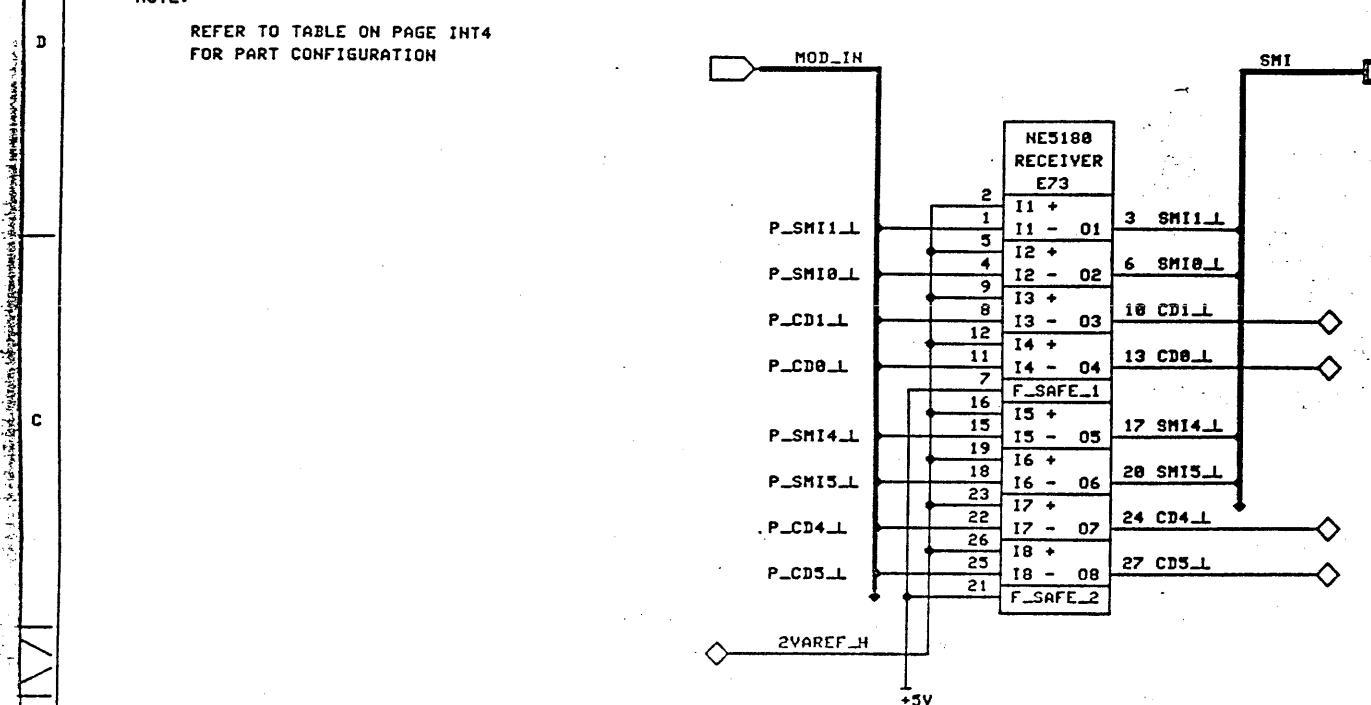
Surveillance Log

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SEARCHED	SERIALIZED	INDEXED	FILED	SEARCHED	SERIALIZED	INDEXED	FILED
1	2	3	4	5	6	7	8
DATE 12-MAY-96				DATE 12-MAY-96			
SERIAL NO.				NUMBER			
TOP REC				K-9D-3410034-0-TRM3			



NOTE:

REFER TO TABLE ON PAGE INT4
FOR PART CONFIGURATION

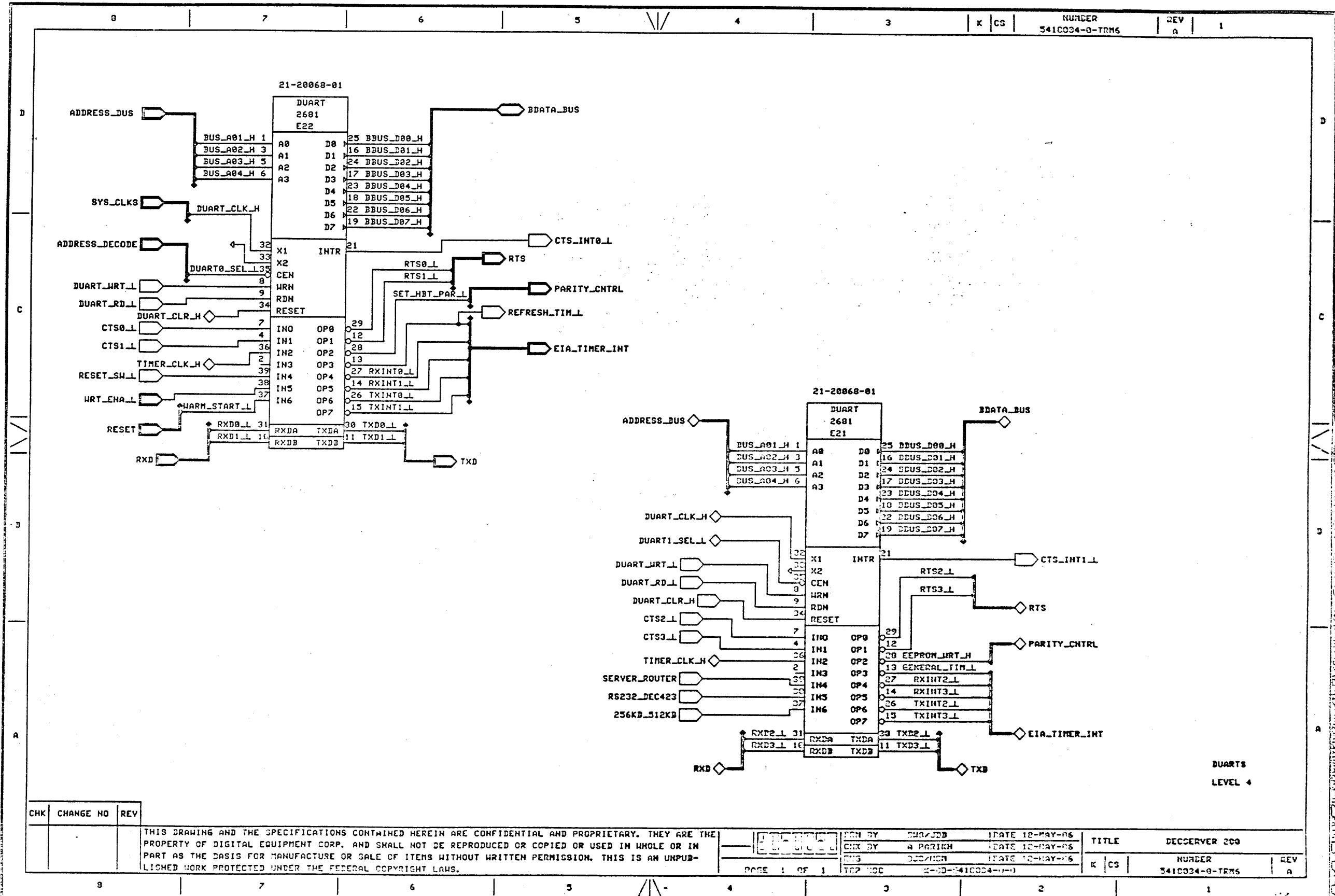
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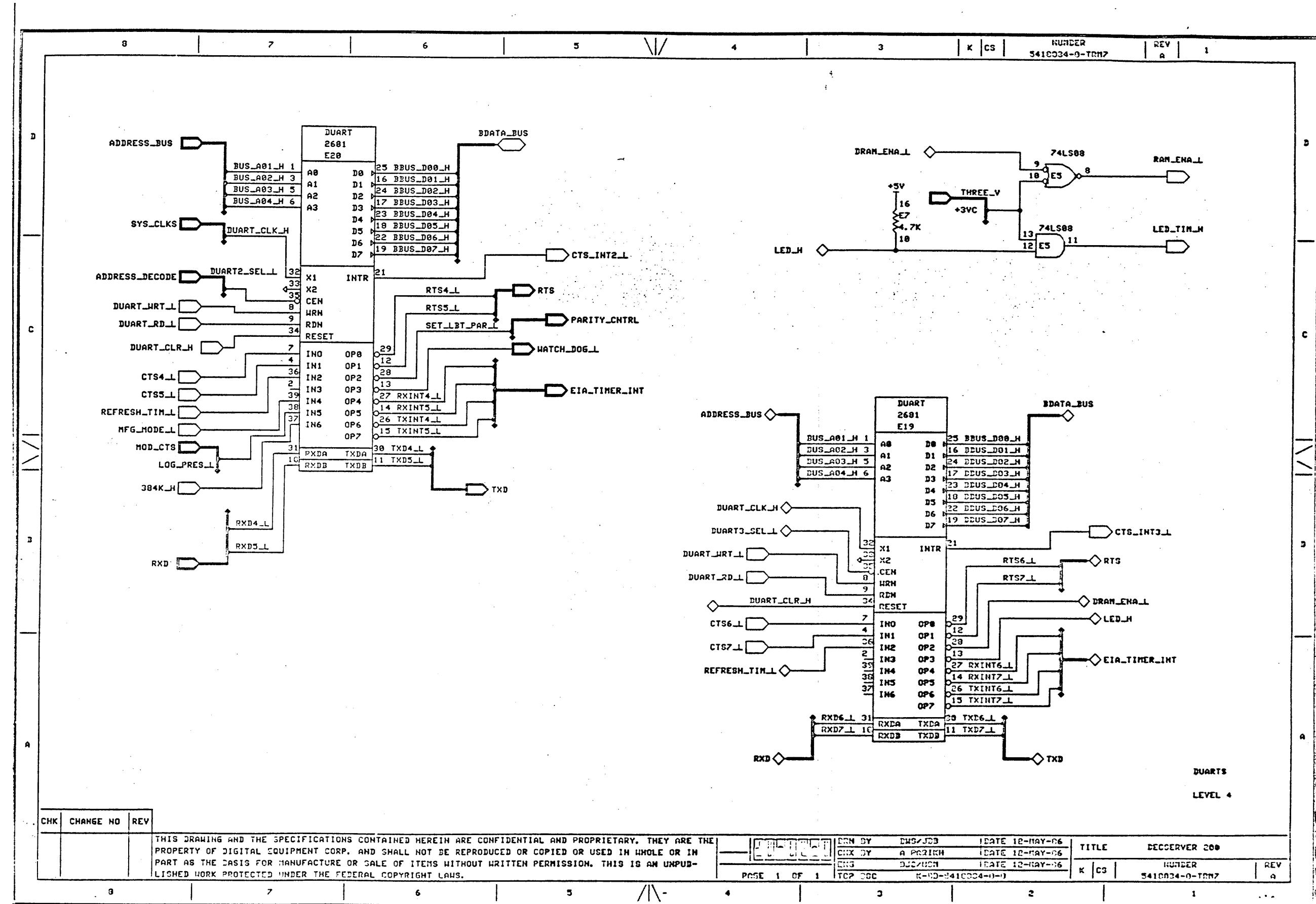
LEVEL 4

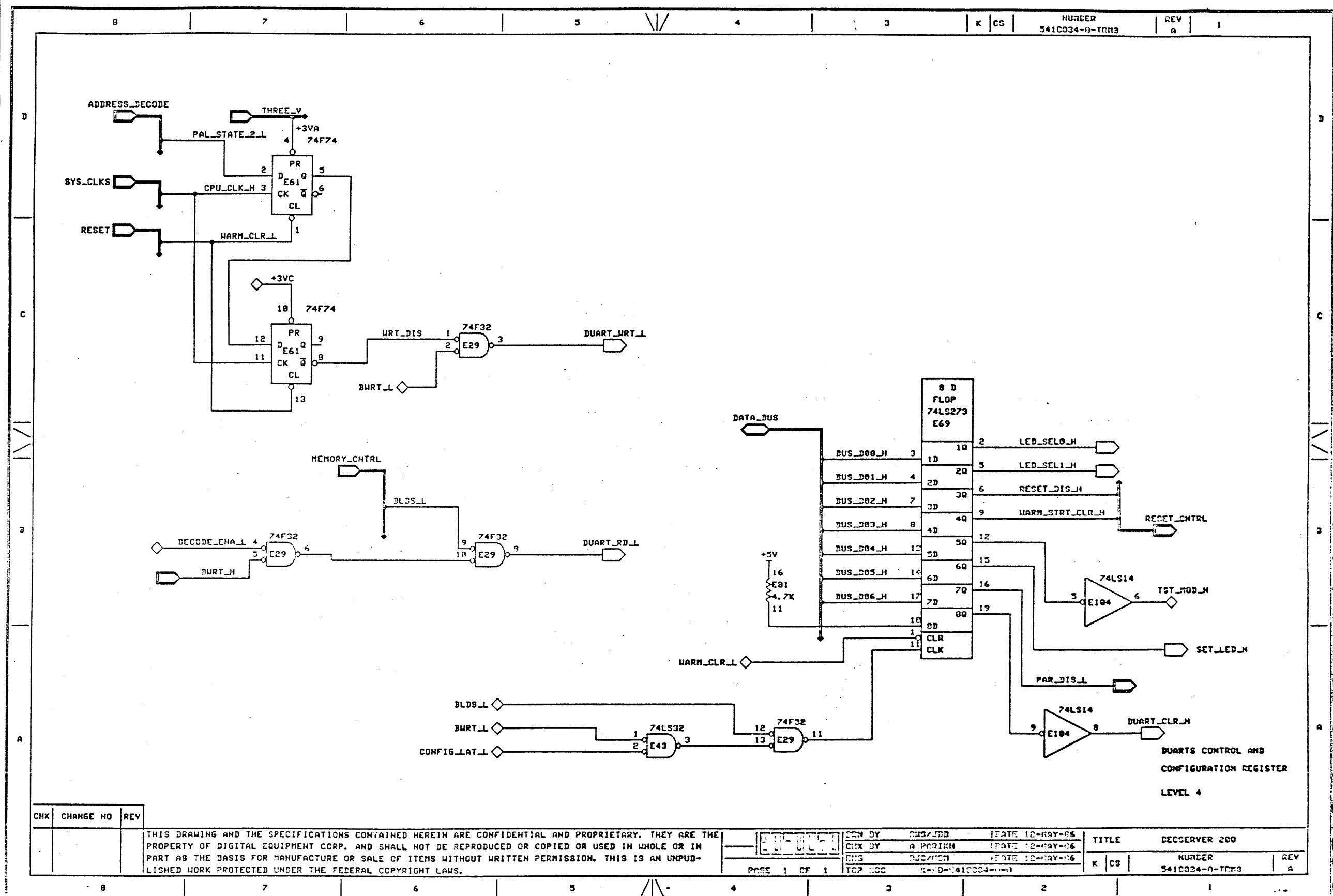
CHK | CHANGE NO | REV

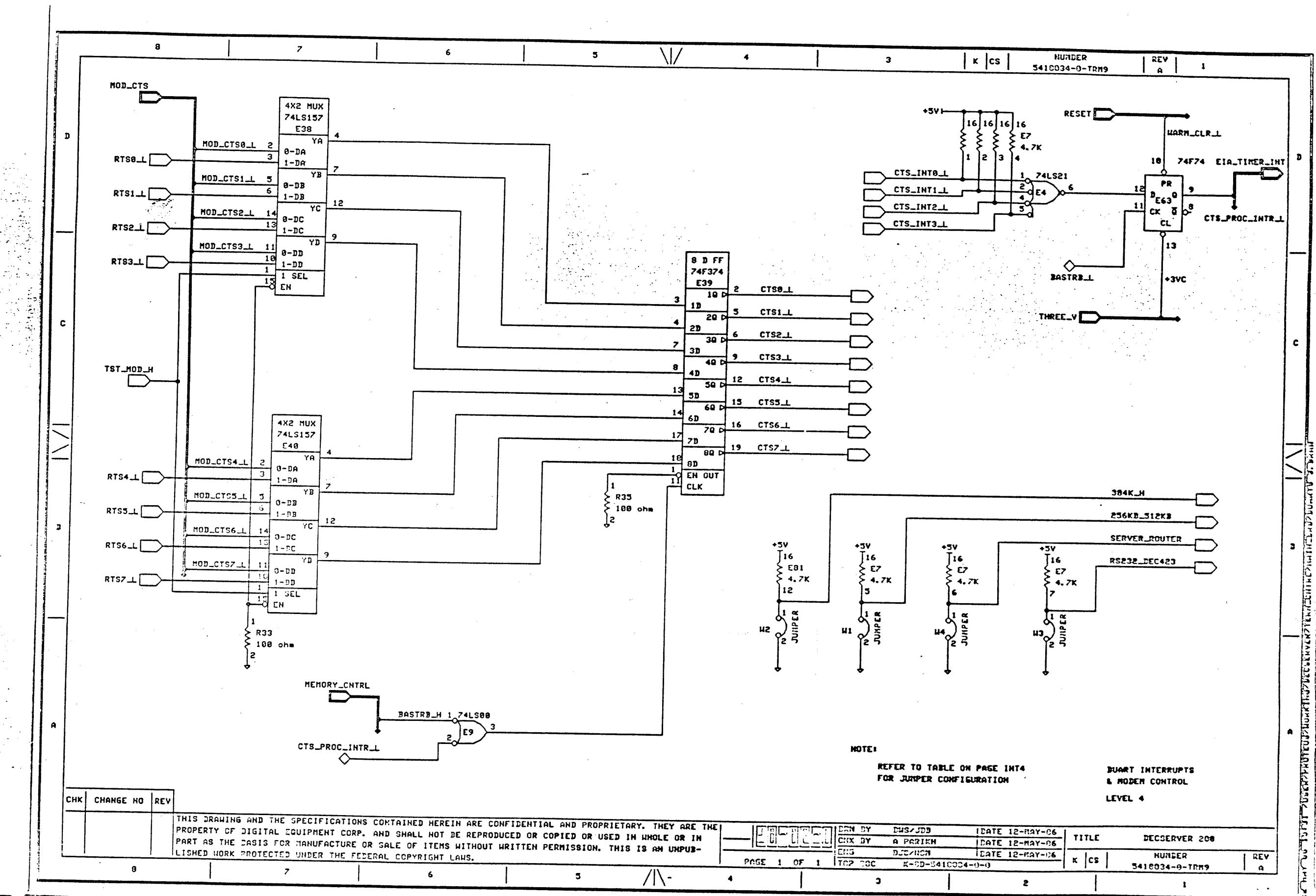
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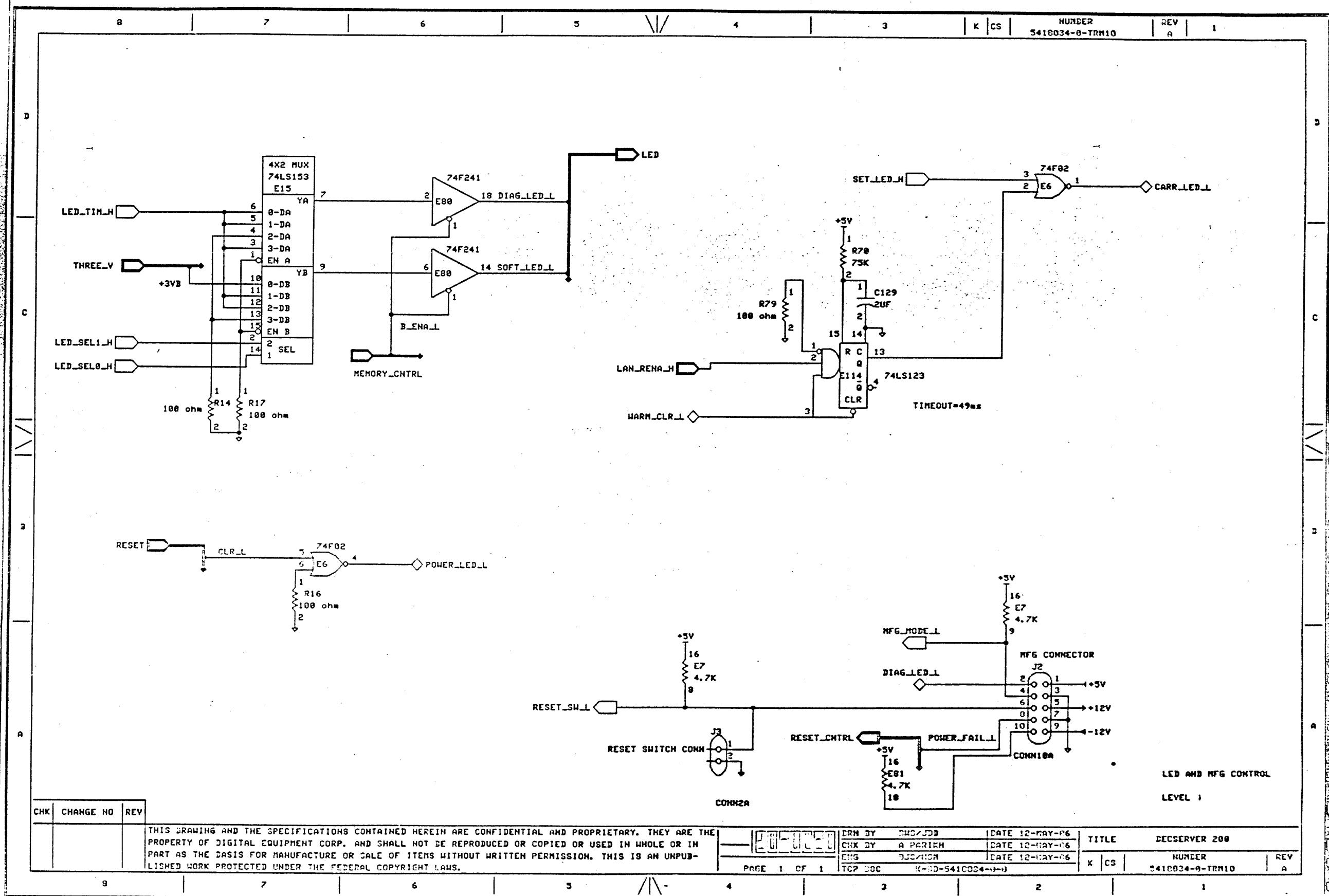
CHG BY	DHS/JDD	DATE 12-MAY-86	TITLE	DECSERVER 200
CIVL BY	O PARikh	DATE 12-MAY-86	K	NUMBER
CHG	DISCHEN	DATE 12-MAY-86	CS	5410334-0-TRM3
PAGE 1 OF 1		TOP EOC	REV	A

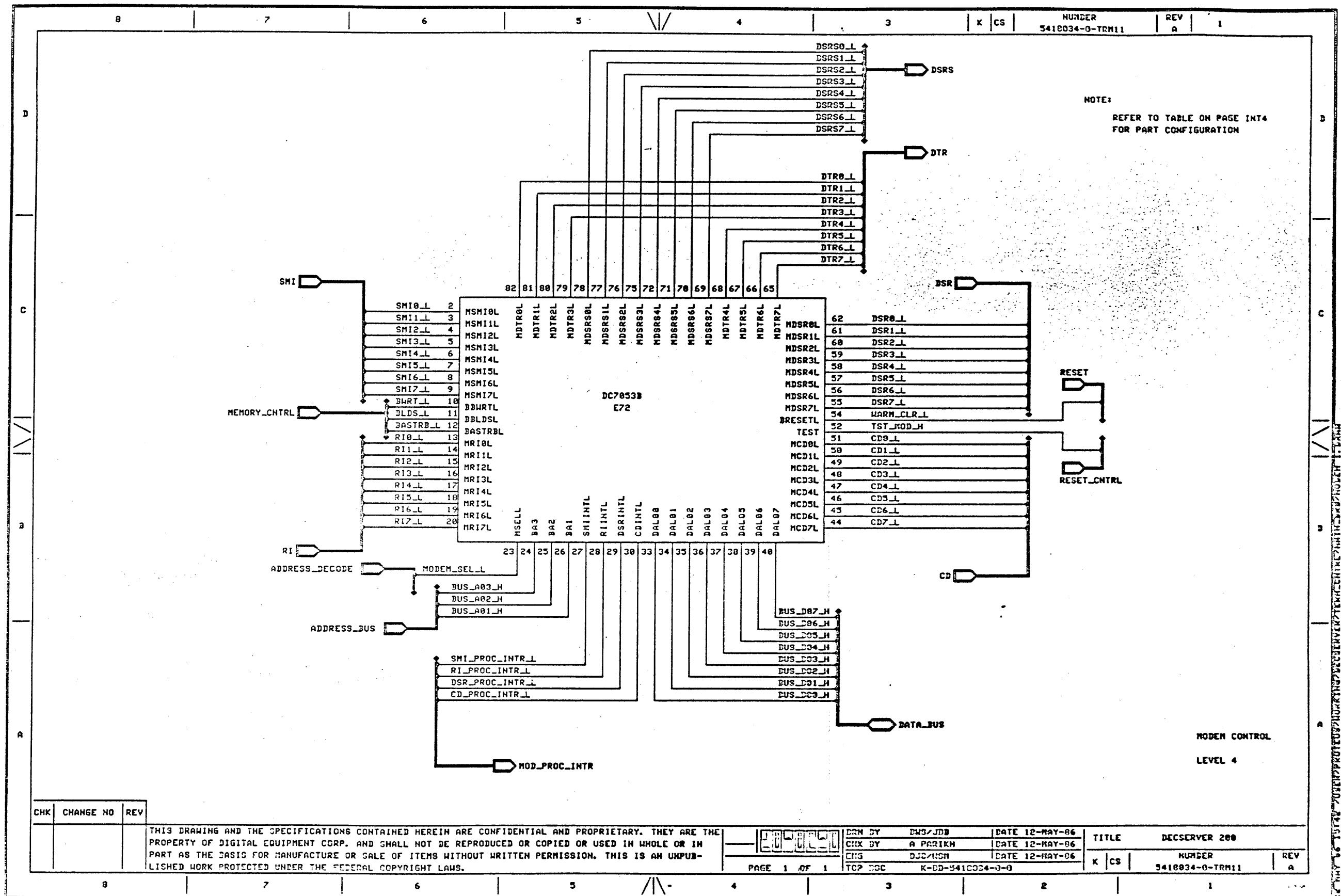


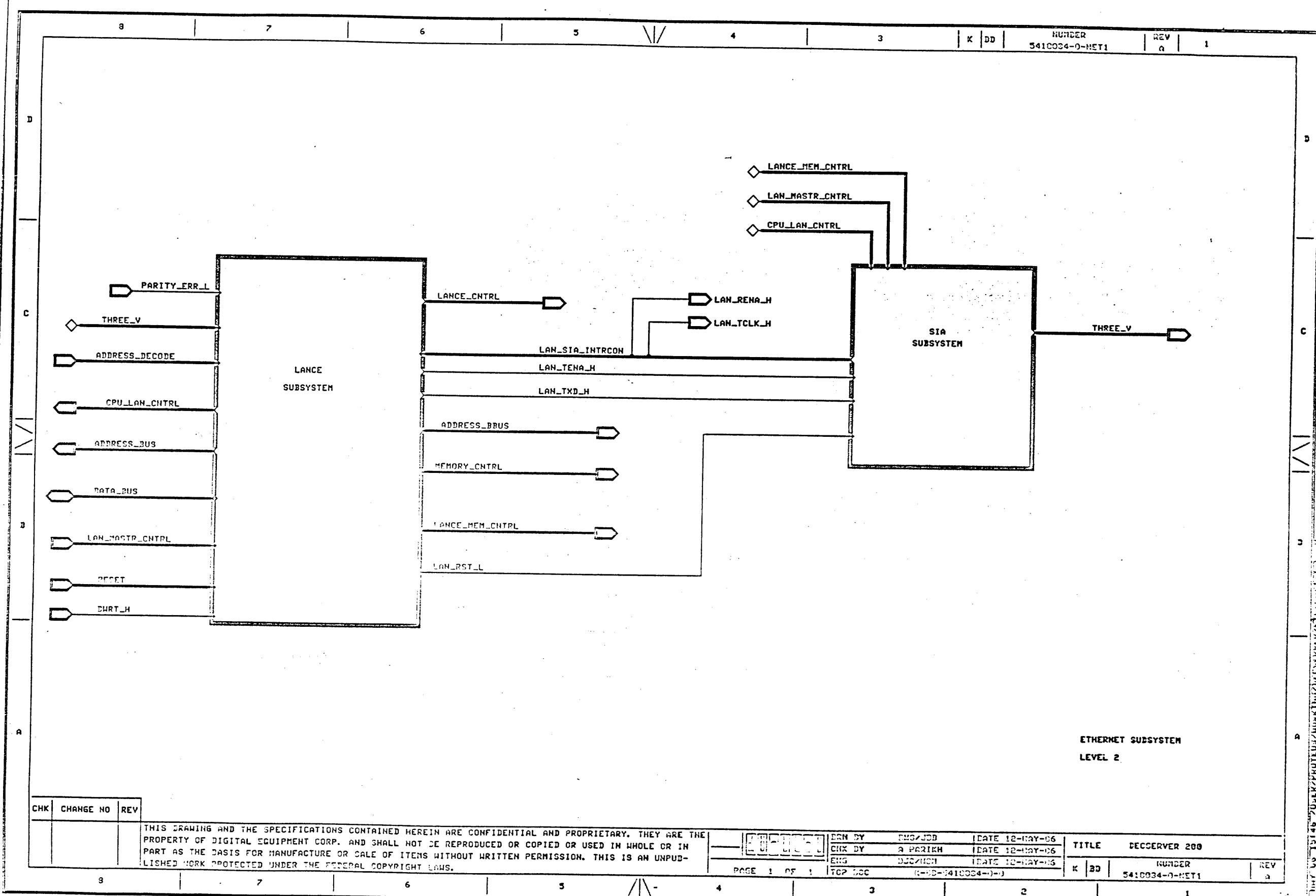


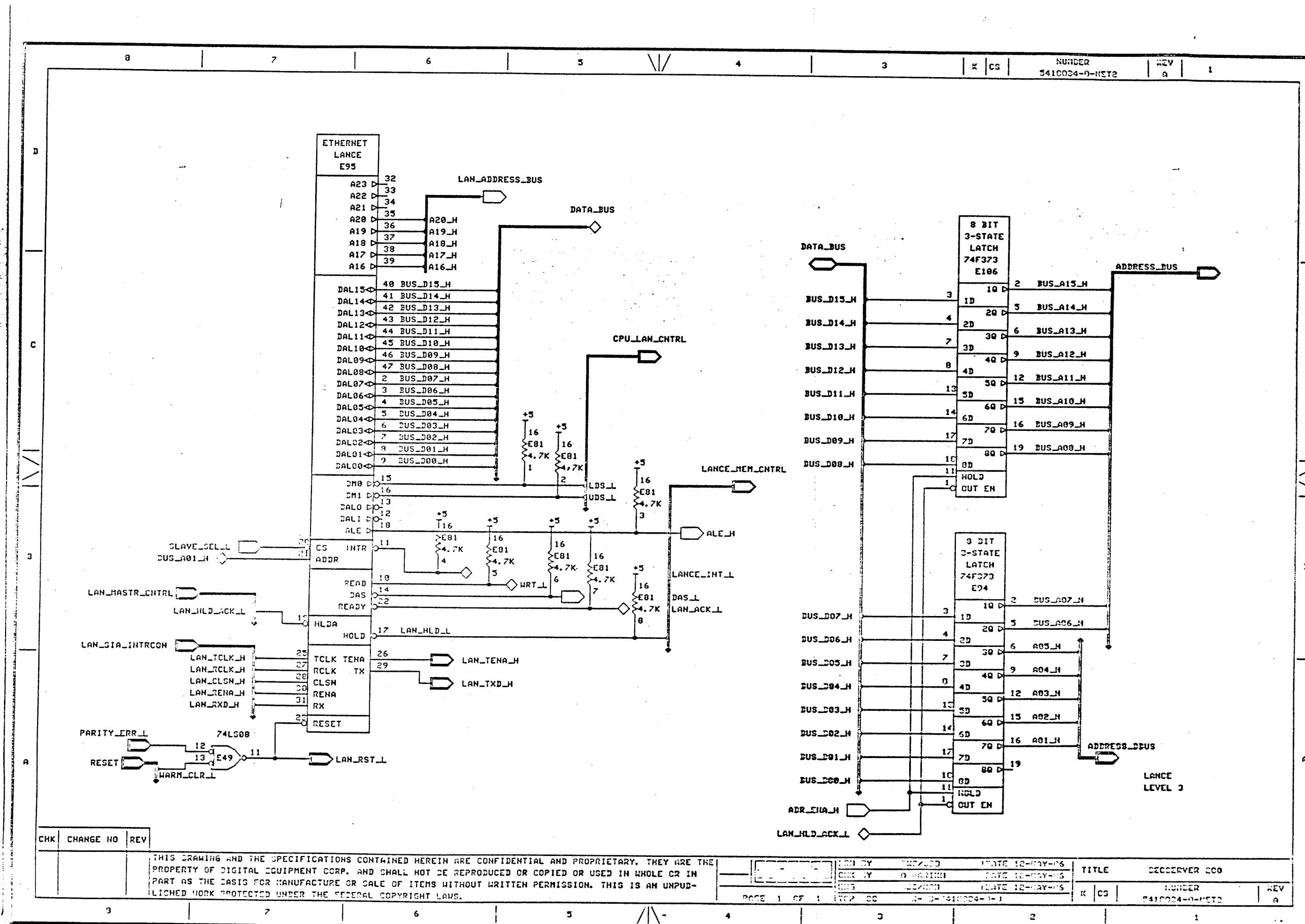








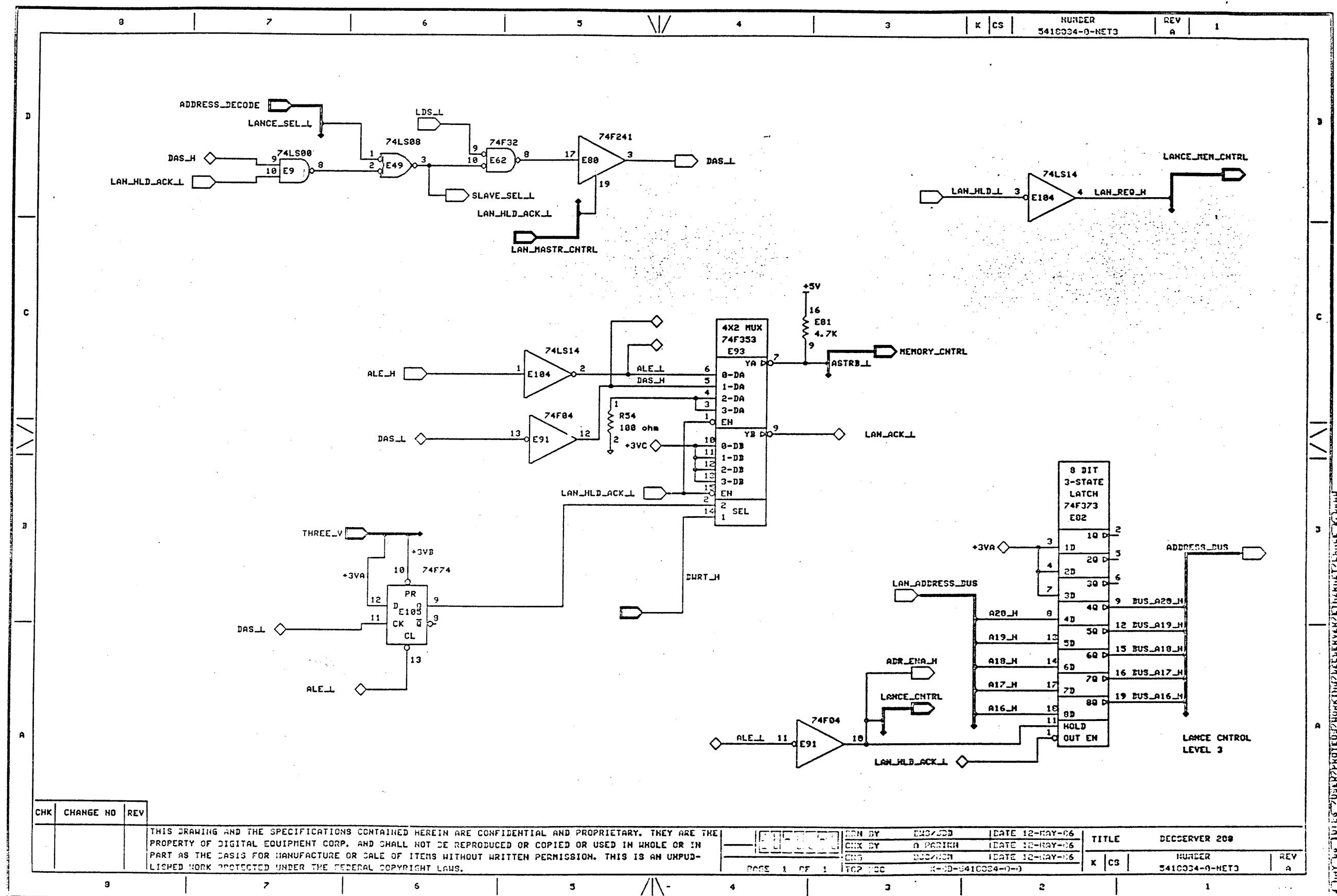


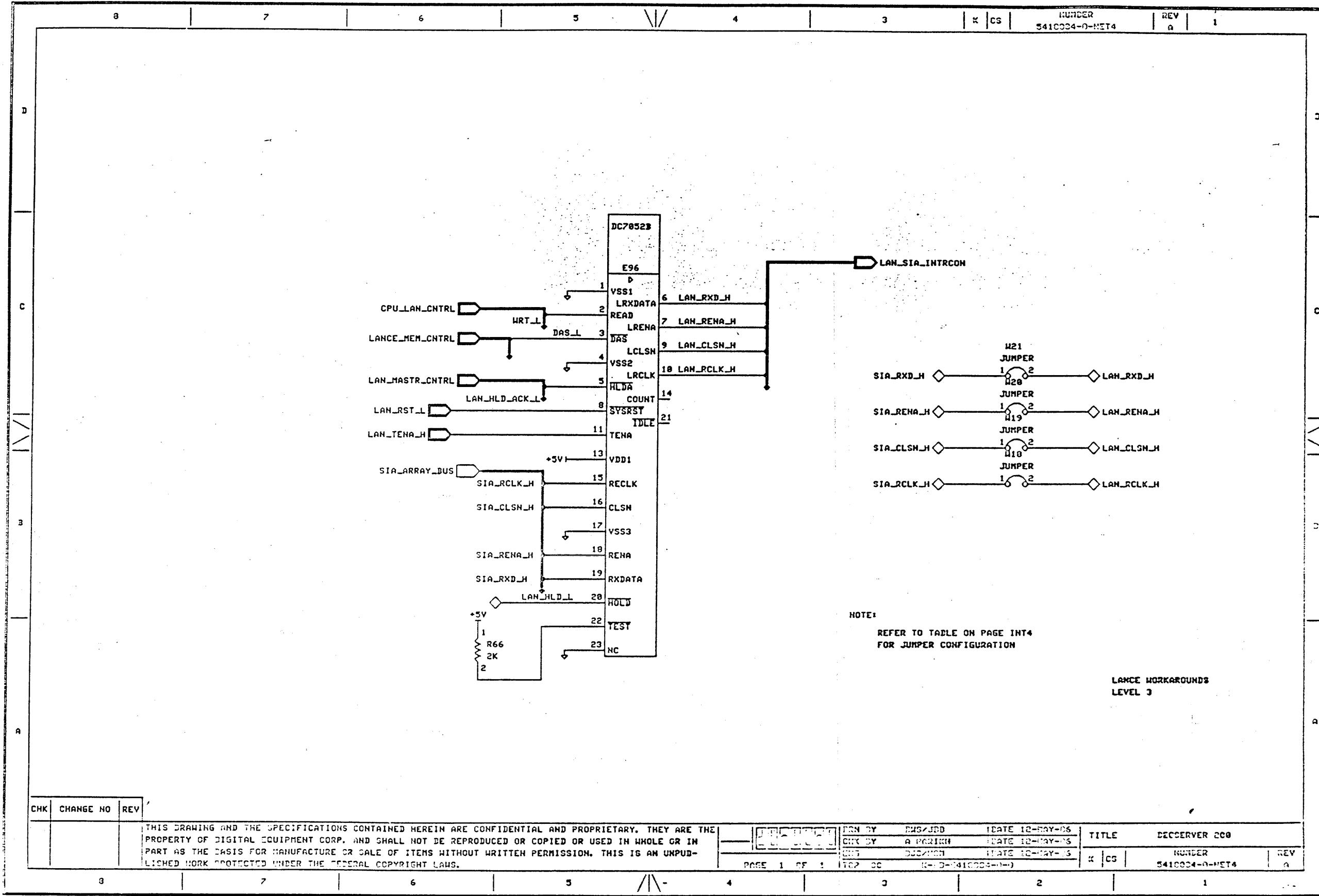


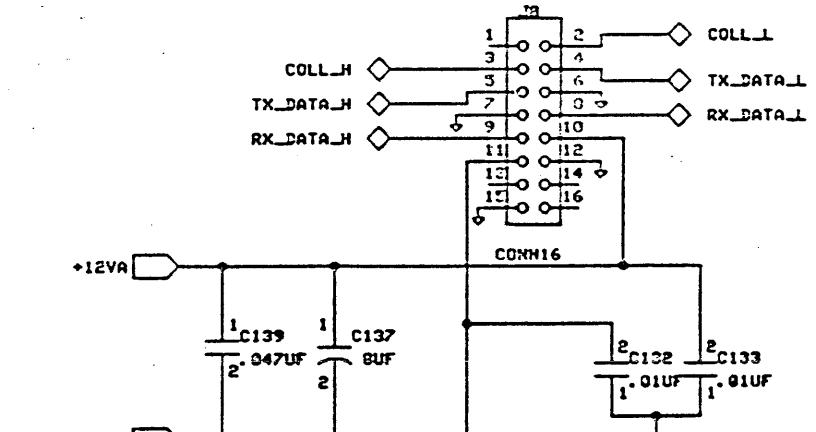
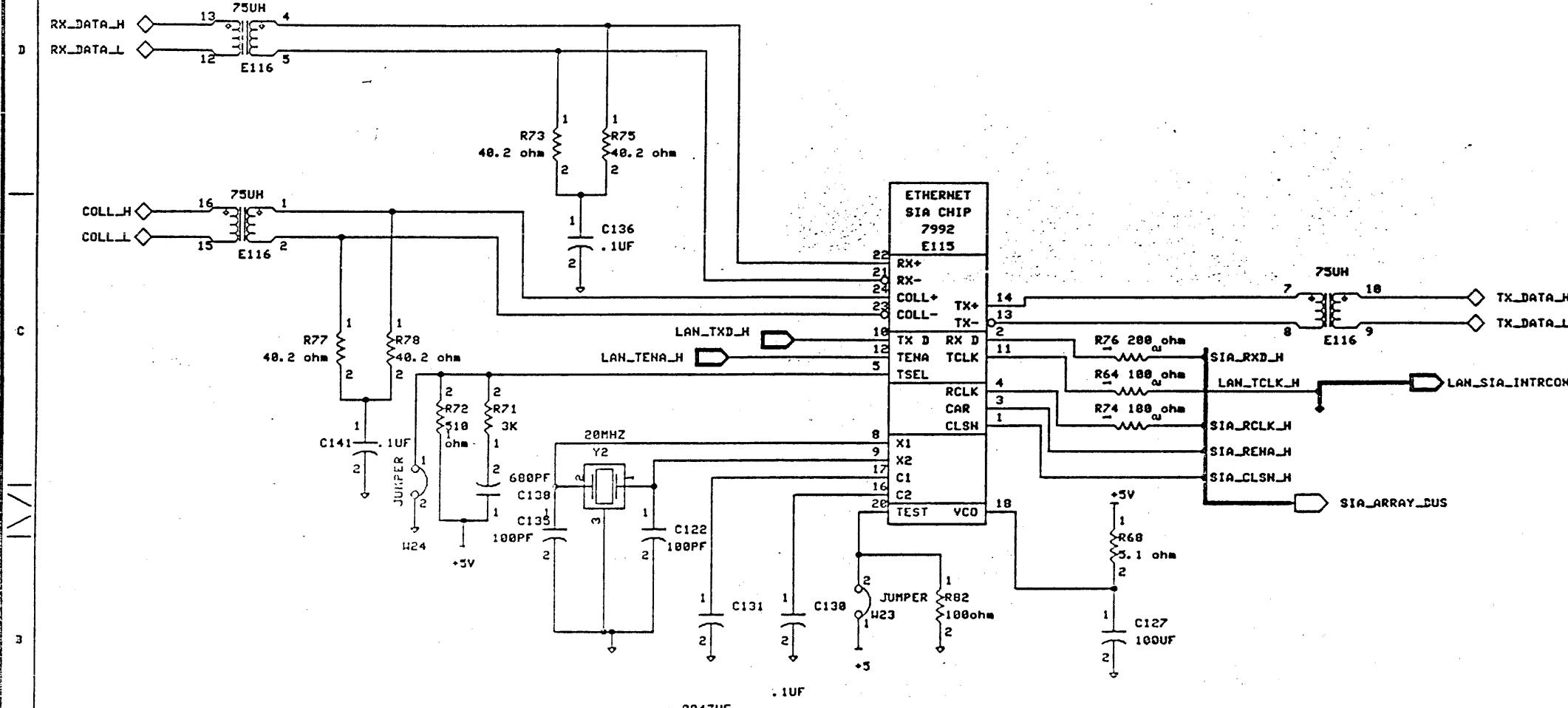
CHK | CHANGE NO | RE

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LEN BY	THOMAS	DATE 12-MAY-76	TITLE	DECCOVER 200
GCR BY	O. MANNING	DATE 12-MAY-76		
GCR	SEARCHED	DATE 12-MAY-76		
PCRE	PCRE	DATE 12-MAY-76		
RCS	CS		NUMBER	7410024-2-NETD







CHK	CHANGE NO	REV
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DESIGN BY	DES/EDD	DATE 12-MAY-96	TITLE	DECSERVER 200
CHG BY	R. POZIKH	DATE 12-MAY-96	X CS	NUMBER 541C034-0-NETS
CHG	POZIKH	DATE 12-MAY-96	REV	A
PAGE 1 OF 1	TC2 REC	(FD-541C034-0-0)		

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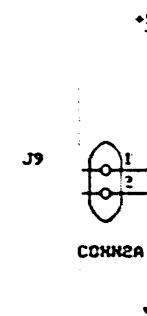
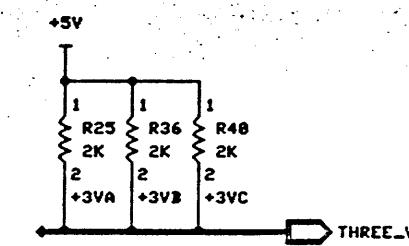
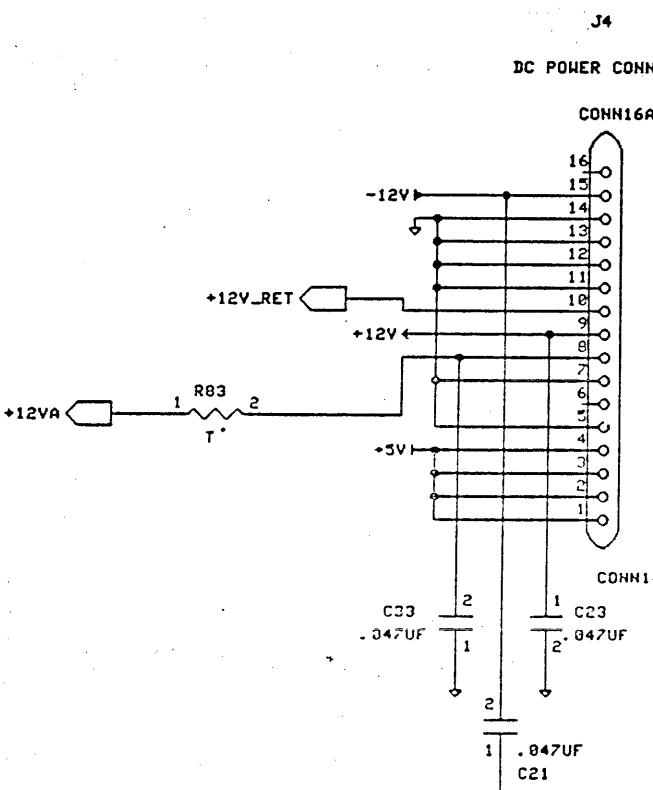
CS

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5410034-0-PWR

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**DC POWER CONNECTOR**

CHK	CHANGE NO	REV

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DRW BY	DMS/JDD	DATE 12-MAY-86	TITLE	DECSERVER 200
CHK BY	A PORIKH	DATE 12-MAY-86	NUMBER	5410034-0-PWR
ENG	DUGKICH	DATE 12-MAY-86	REV	A

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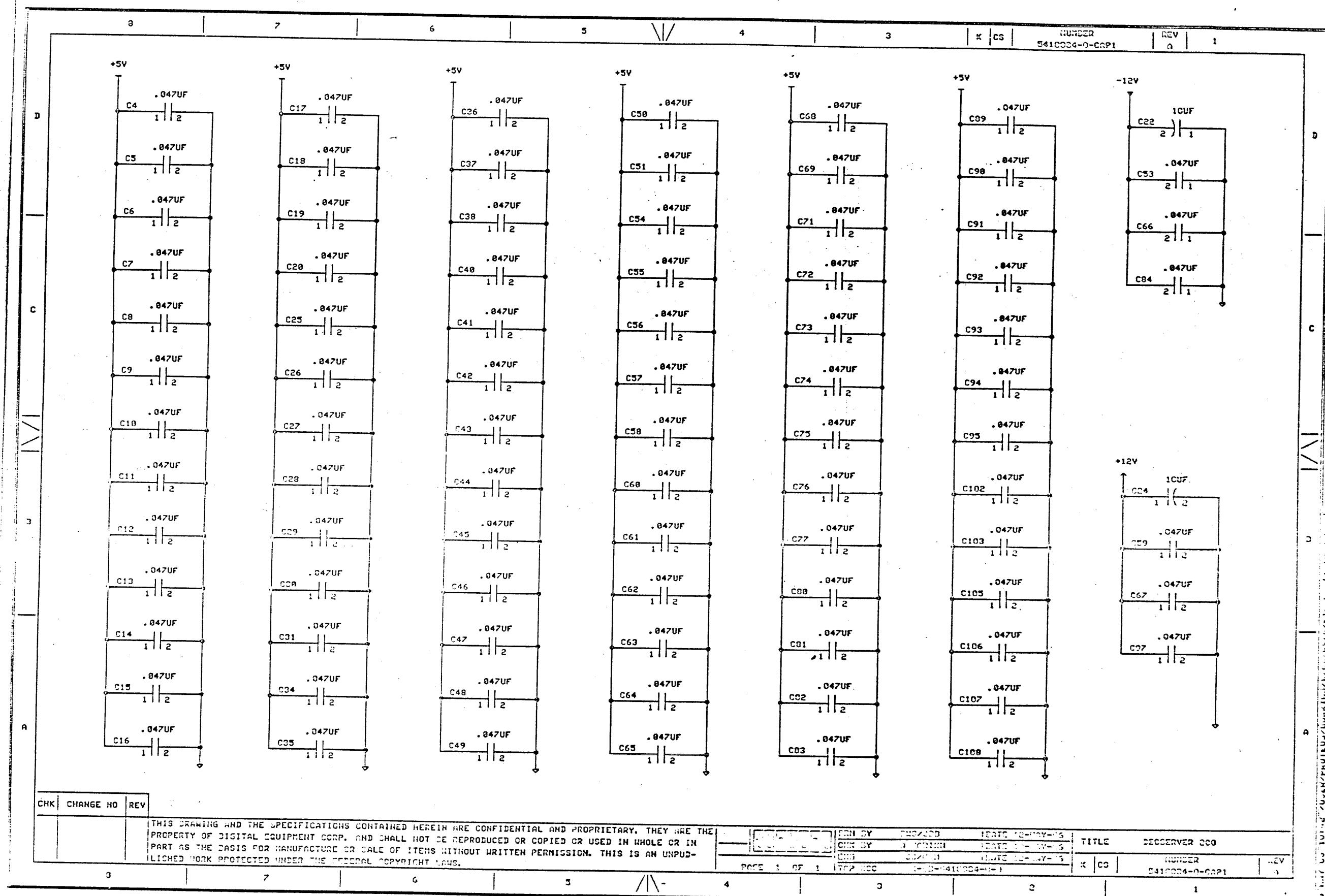
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541C034-0-CAP2

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C109 - □ a

C128 - □ a

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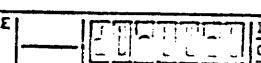
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CHK	CHANGE NO	REV

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ISSUED BY DRAFTED DATE 12-MAY-06
CHK BY A PARSONS DATE 12-MAY-06
CNS DJS/MCH DATE 12-MAY-06

TITLE DEC SERVER 200
NUMBER 541C034-0-CAP2
REV A

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541C034-0-CAP2.DWG 12-MAY-06

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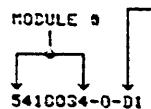
CS

NUMBER
541C034-0-01

REV

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FUNCTIONAL
ADDRESS/NAME
OF SUBSYSTEMPAGE 8 WITHIN
SUBSYSTEMCONNECTOR
TYPE

DESCRIPTION

			1. USED WHEN A SIGNAL NEEDS TO BE SENT FROM ONE PART OF A PAGE TO ANOTHER WITHOUT THE USE OF A WIRE 2. IF THE SIGNAL HAS ALREADY BEEN BROUGHT ON TO THE PAGE BY ANOTHER CONNECTOR.
			1. FINGER PIN CONNECTOR.
			1. BRINGS A SINGLE SIGNAL FROM OR TO A HIGHER LEVEL DRAWING.
INPUT	OUTPUT	BI-DIRECTIONAL	
			1. BRINGS A GROUP OF SIGNALS THROUGH ONE BUS FROM OR TO A HIGHER LEVEL DRAWING.
INPUT	OUTPUT	BI-DIRECTIONAL	
			1. BRINGS A SINGLE SIGNAL FROM PAGE TO PAGE WITHIN THE SAME MODULE.
INPUT	OUTPUT	BI-DIRECTIONAL	
			1. BRINGS A GROUP OF SIGNALS THROUGH A BUS FROM PAGE TO PAGE WITHIN THE SAME MODULE.
INPUT	OUTPUT	BI-DIRECTIONAL	

CHK | CHANGE NO | REV

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DES BY	TM3/JDD	DATE 12-MAY-86	TITLE	DECSERVER 200
CHG BY	A PGRIKH	DATE 12-MAY-86	NUMBER	541C034-0-01
CHG	DISCVER	DATE 10-MAY-86	REV	A

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1994-09-25 10:49:57

NOTE: THIS IS ONE OF TWO PAGES WHICH
DEFINES THE CONTENTS OF EACH
BUNDLE USED IN THE PRINT SET
FOR THE 5418034 MODULE

BUNDLE NAME CONTENTS OF BUNDLE

ADDRESS_BBUS	A01_H, A02_H, A03_H, A04_H & A05_H
ADDRESS_BUS	BUS_A01_H BUS_A20_H
ADDRESS_DECODE	DATA_ACK_L, PAL_STATE_2_L, DUART0_SEL_L, DUART1_SEL_L, DUART2_SEL_L, DUART3_SEL_L, MODEM_SEL_L, LANCE_SEL_L, ROM_SEL_L, ADR_PRGM_SEL_L, BBUS_ENA_L, EEPROM_SEL_L, CONFIG_LAT_L & DECODE_ENA_L
BDATA_BUS	BBUS_D00_H BBUS_D15_H
CD	CD0_L, CD1_L, CD2_L, CD3_L, CD4_L, CD5_L, CD6_L & CD7_L
CPU_LAN_CTRL	UDS_L, LDS_L & HRT_L
CPU_MASTR_CTRL	BR_L & BEACK_L
DATA_BUS	BUS_D00_H BUS_D15_H
DIAG_BUS	SYNC_H & BIT1_H BIT7_H
DSR	DSR0_L DSR7_L
DSRS	DSRS0_L DSRS7_L
DTR	DTR0_L DTR7_L
EIA_TIMER_INT	RXINT0_L RXINT7_L, TXINT0_L TXINT7_L, REFRESH_TIM_L, GENERAL_TIM_L & CTS_PROC_INTR_L
INTR_CTRL	IPL0_L, IPL1_L & IPL2_L
LAN_ADDRESS_BUS	A16_H A20_H
LAN_MASTR_CTRL	LAN_HLD_ACK_L & LAN_HLD_ACK_H
LAN_SIA_INTRCON	LAN_TCLK_H, LAN_RENA_H, LAN_CLSH_H, LAN_RXD_H & LAN_RCLK_H
LANCE_CTRL	LAN_ACK_L & ADR_ENA_H
LANCE_MEM_CTRL	LANCE_INT_L, DAS_L, ALE_H, LAN_REQ_H & LAN_HLD_L
LED	POWER_LED_L, DIAG_LED_L, SOFT_LED_L & CARR_LED_L
MEM_ADDRESS_BUS	MA1_L MA8_L
MEM_BANK_SEL	BANK0_SEL_L, BANK1_SEL_L, BANK0_HRT_L & BANK1_HRT_L
MEM_BANK_SEL1	BANK2_SEL_L, BANK3_SEL_L, BANK2_HRT_L & BANK3_HRT_L
MEMORY_CTRL	BUDS_L, BDLS_L, BWRT_L, ASTRB_L, BASTRB_L, BUDS_H, BDLS_H, INTR_ACK_L & B_ENA_L
MOD_IN	P_CTS0_L P_CTS7_L, P_RI0_L P_RI7_L, P_DSR_L P_DSR7_L, P_SM10_L P_SM17_L & P_CD0_L P_CD7_L
MOD_OUT	P_RTS0_L P_RTS7_L, P_DTR0_L P_DTR7_L, P_DSRS0_L P_DSRS7_L & 2VREF_H
MOD_PROC_INTR	DSR_PROC_INTR_L, CD_PROC_INTR_L, RI_PROC_INTR_L & SM1_PROC_INTR_L
MODEM_IN	P_RI0_L P_RI7_L, P_SM10_L P_SM17_L, P_CD0_L P_CD7_L & P_CTS0_L P_CTS7_L
MODEM_OUT	P_DSRS0_L P_DSRS7_L, P_DTR0_L P_DTR7_L & P_RTS0_L P_RTS7_L

CHK	CHANGE NO	REV
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NOTE: CONTINUATION OF FIRST PAGE WHICH
DEFINES THE CONTENTS OF EACH
BUNDLE USED IN THE PRINT SET
FOR THE 5410034 MODULE

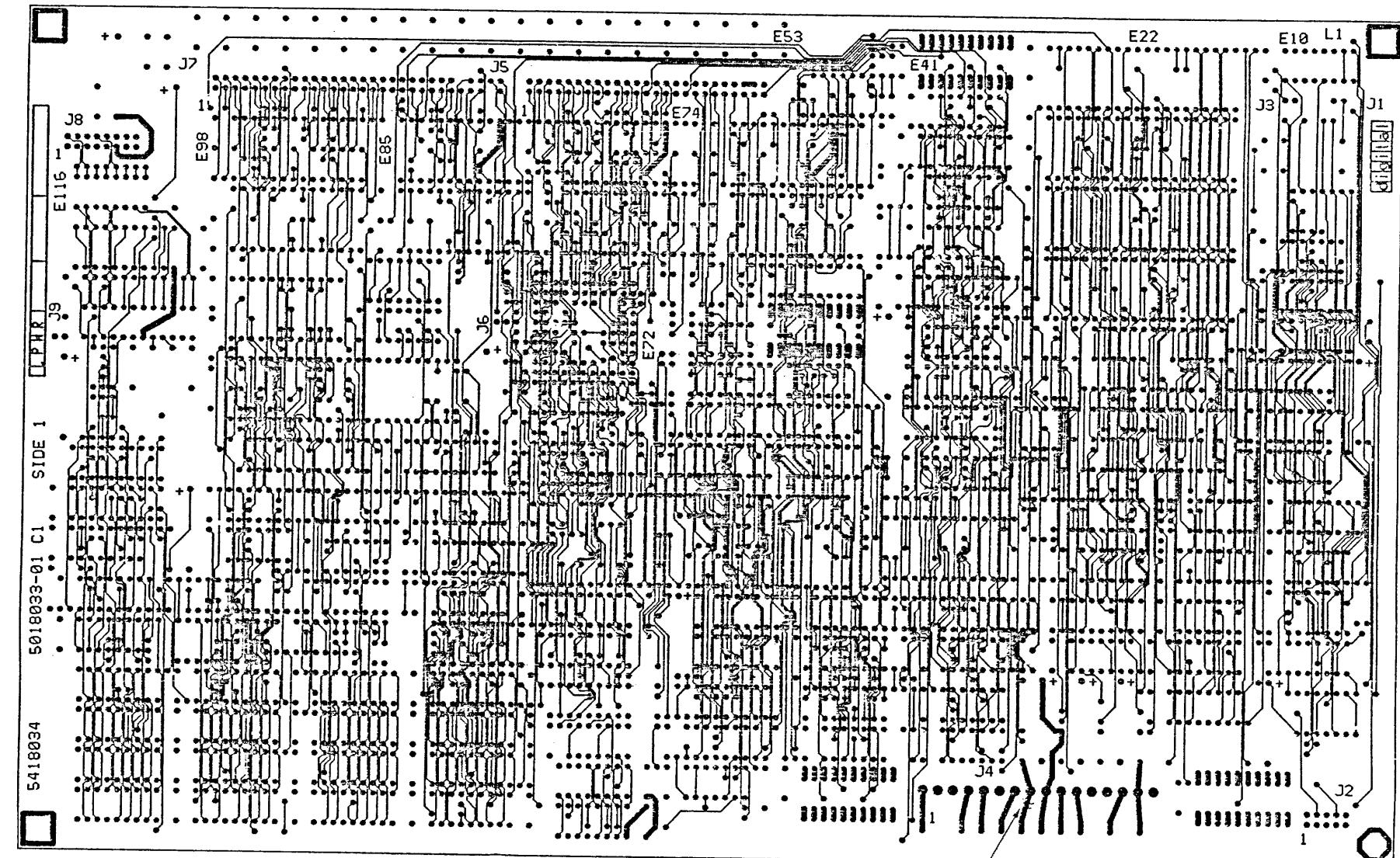
BUNDLE NAME CONTENTS OF BUNDLE

PARITY_CTRL	EEPROM_HRT_H, SET_LBT_PAR_L, SET_HBT_PAR_L & PAR_DIS_L
RAM_CTRL	HBT_RAS_L, HBT_CAS_L, LBT_RAS_L, LBT_CAS_L & RAM_SEL_L
RCVDATA	RX0_L RX7_L
RESET	CLR_L, HARM_CLR_L, HARM_START_L, HARM_CLR_H & EEPROM_CLR_L
RESET_CTRL	RESET_DIS_H, HARM_STRT_CLR_H, RAM_ENA_L, TST_MOD_H & POWER_FAIL_L
RI	RI0_L RI7_L
RTS	RTS0_L RTS7_L
RXD	RXD0_L RXD7_L
SIA_ARRAY_BUS	SIA_RXD_H, SIA_RCLK_H, SIA_RENA_H & SIA_CLSH_H
SMI	SMI0_L SMI7_L
SYS_CLKS	DUART_CLK_H, CPU_CLK_H, CPU_CLK_L & TIMER_CLK_H
THREE_V	+3VA, +3VB & +3VC
TXD	TXD0_L TXD7_L
XMTDATA	TX0_L TX7_L

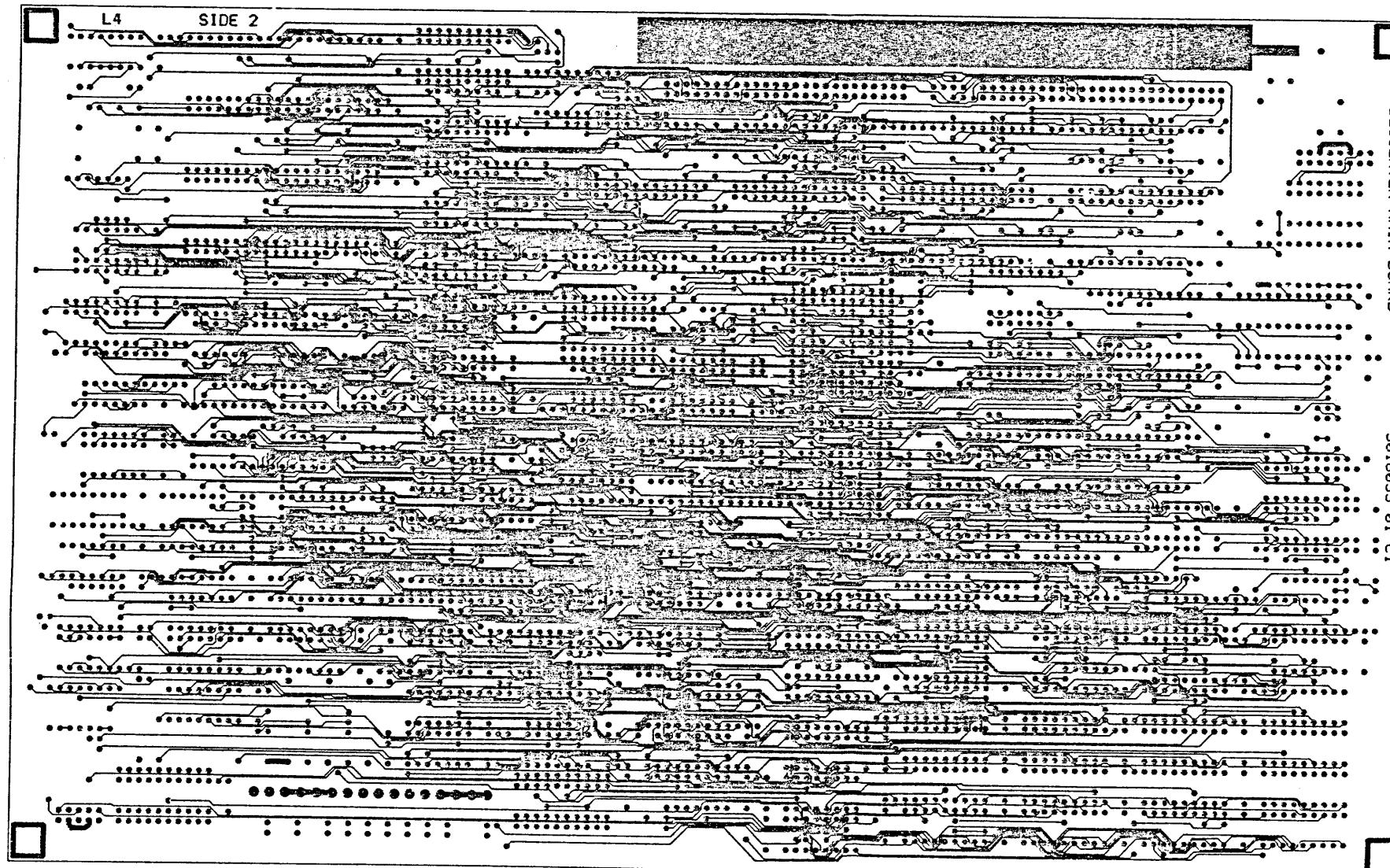
CHK	CHANGE NO	REV
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			5410034-0-03
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DRAWN	DATE	TITLE	digital				
CHECKED	DATE						
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RESP. ENG.	DATE	DOCUMENT NUMBER					
MFG. ENG.	DATE	SHEET NUMBER					



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REWORK INSTRUCTIONS
ETCH CUTS - SIDE 1
O-1 AT J4-8

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REVISION HISTORY		
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SIZE	CODE	NUMBER
DEC	5018033-0-0	A
SCALE	SHEET	1 OF 3

TITLE
ETCH CUT DRAWING